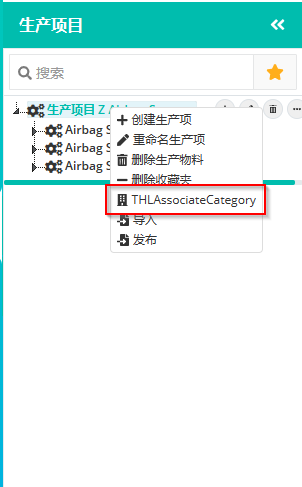
**1130 Chinese Language verification**

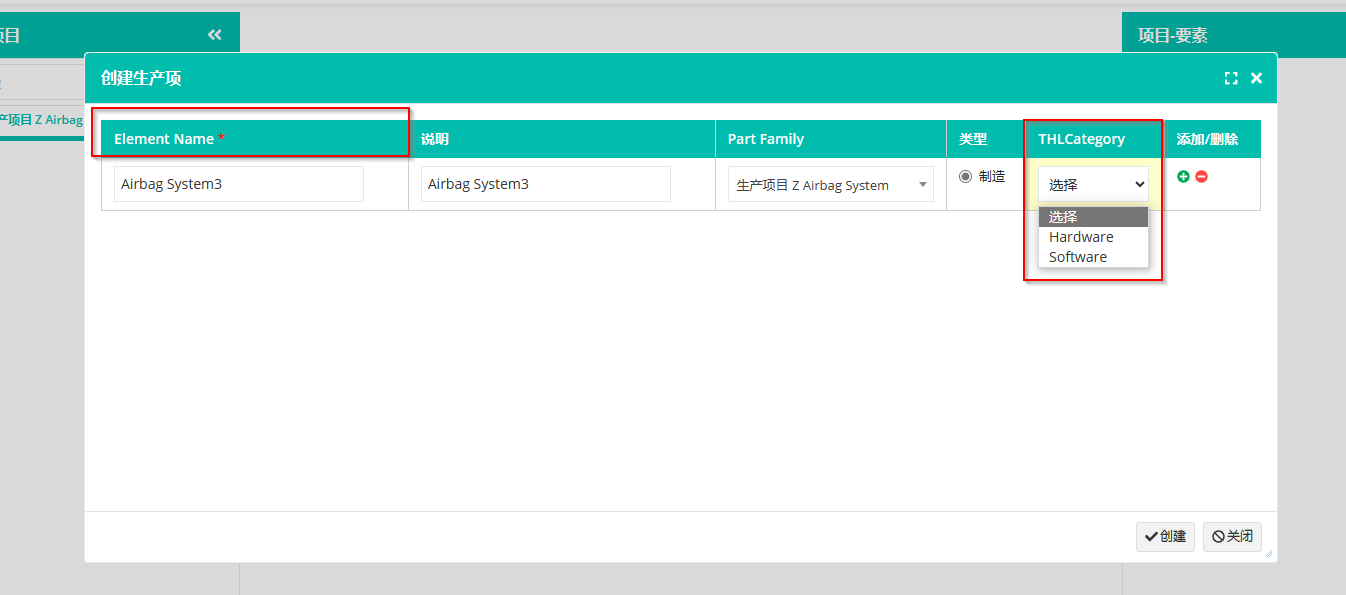
**Numbering Scheme**



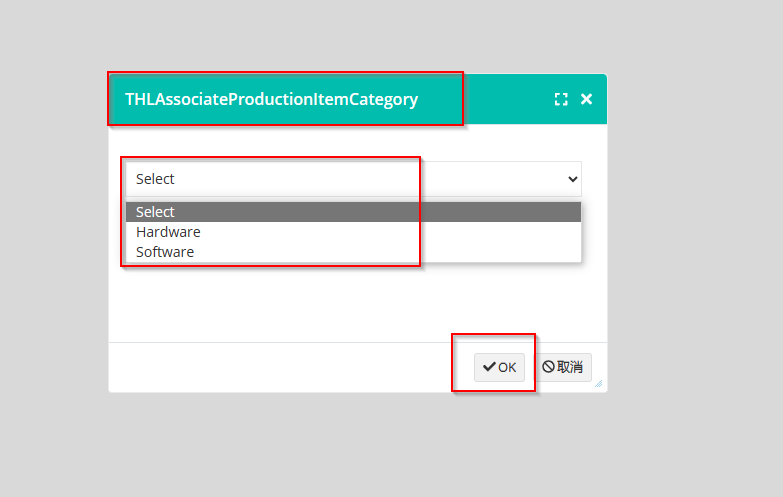
**Production Item Tree**

****

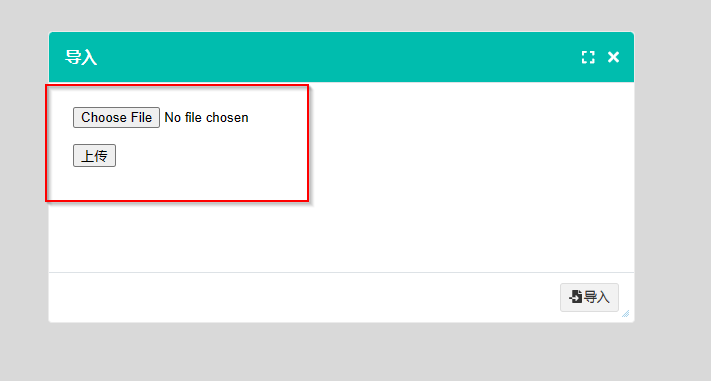
**Create PI**

****

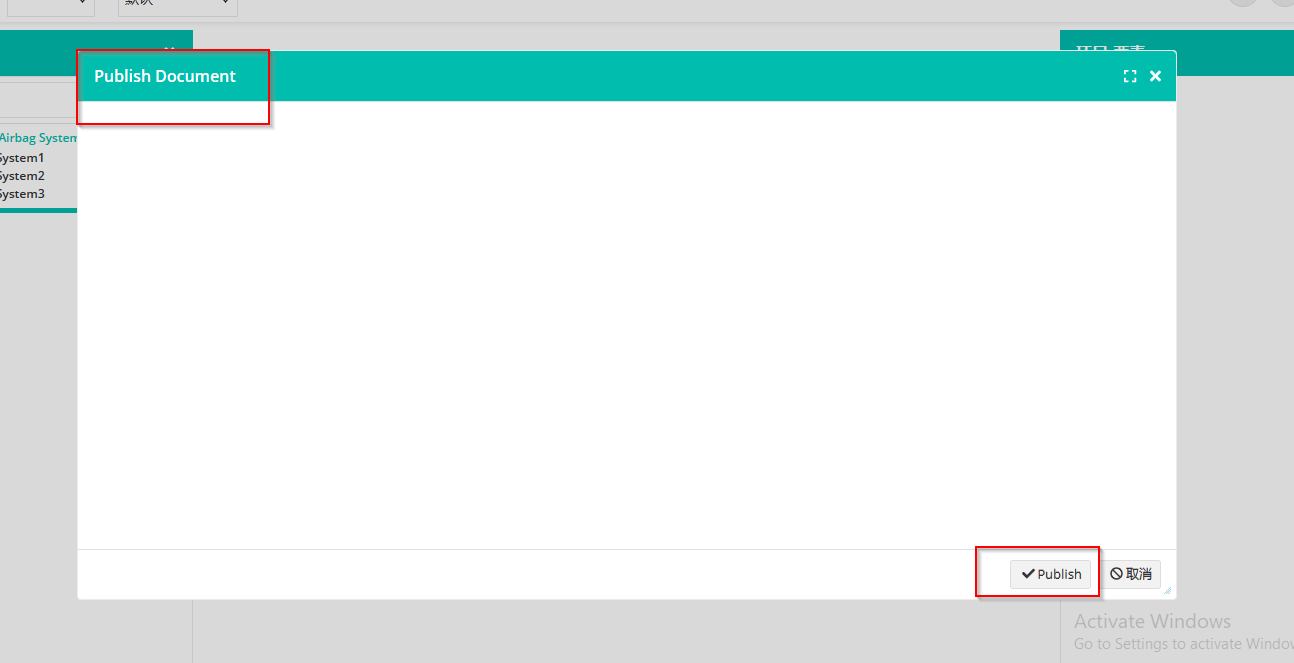
**Associate category**

****

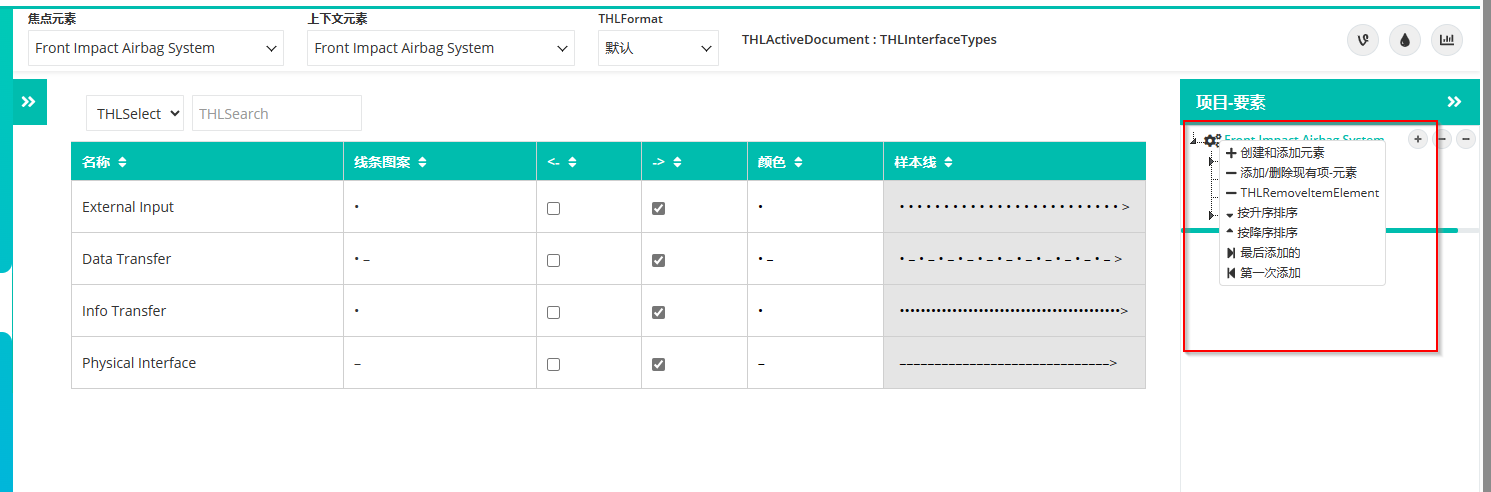
**Import PI**

****

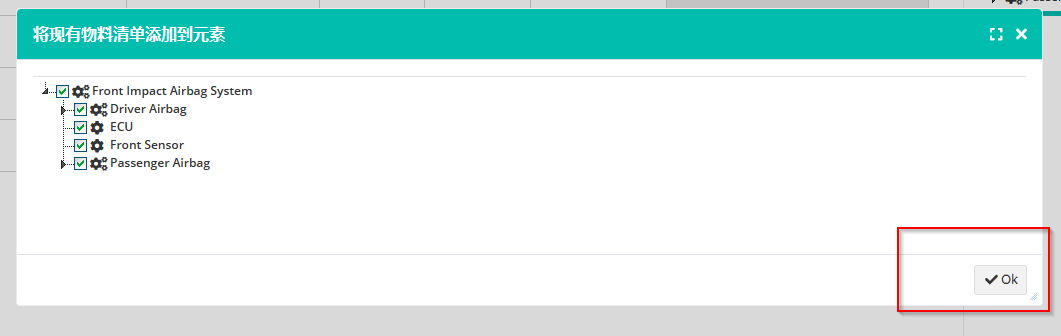
**Publish Document**

****

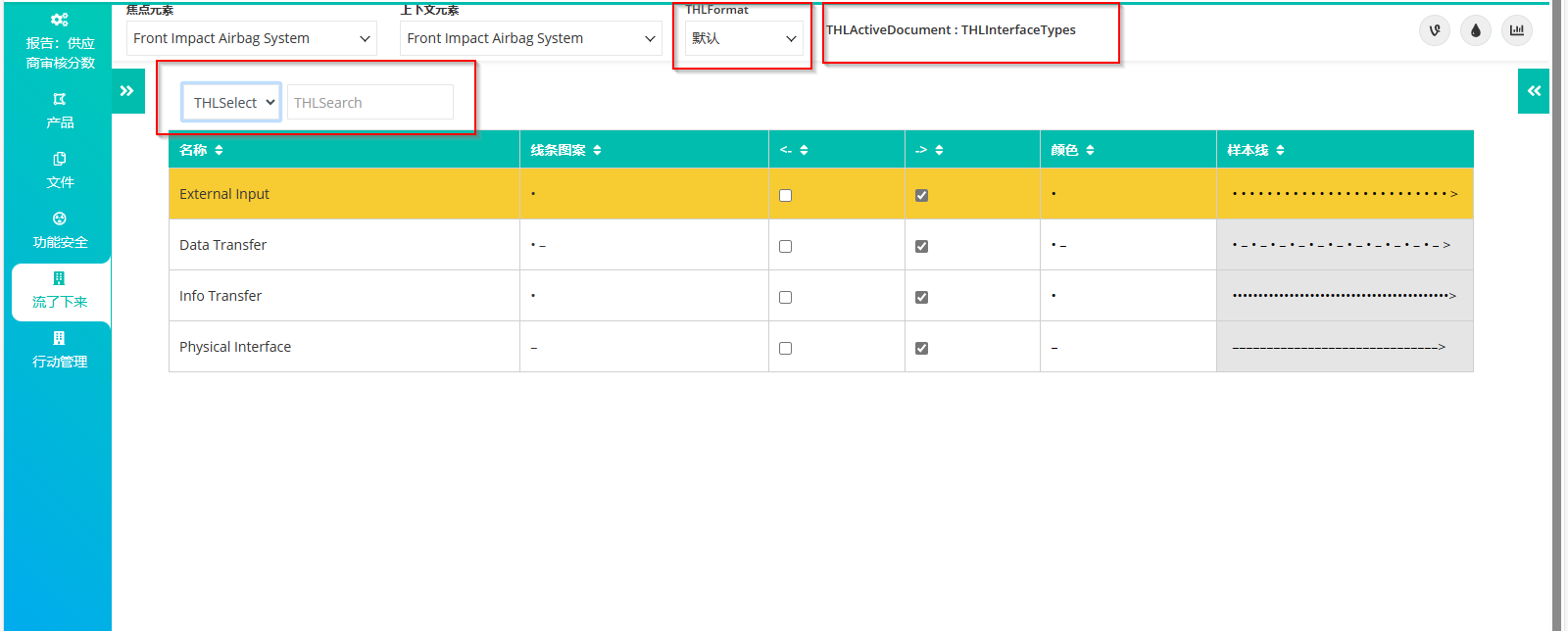
**BOM Tree**

****

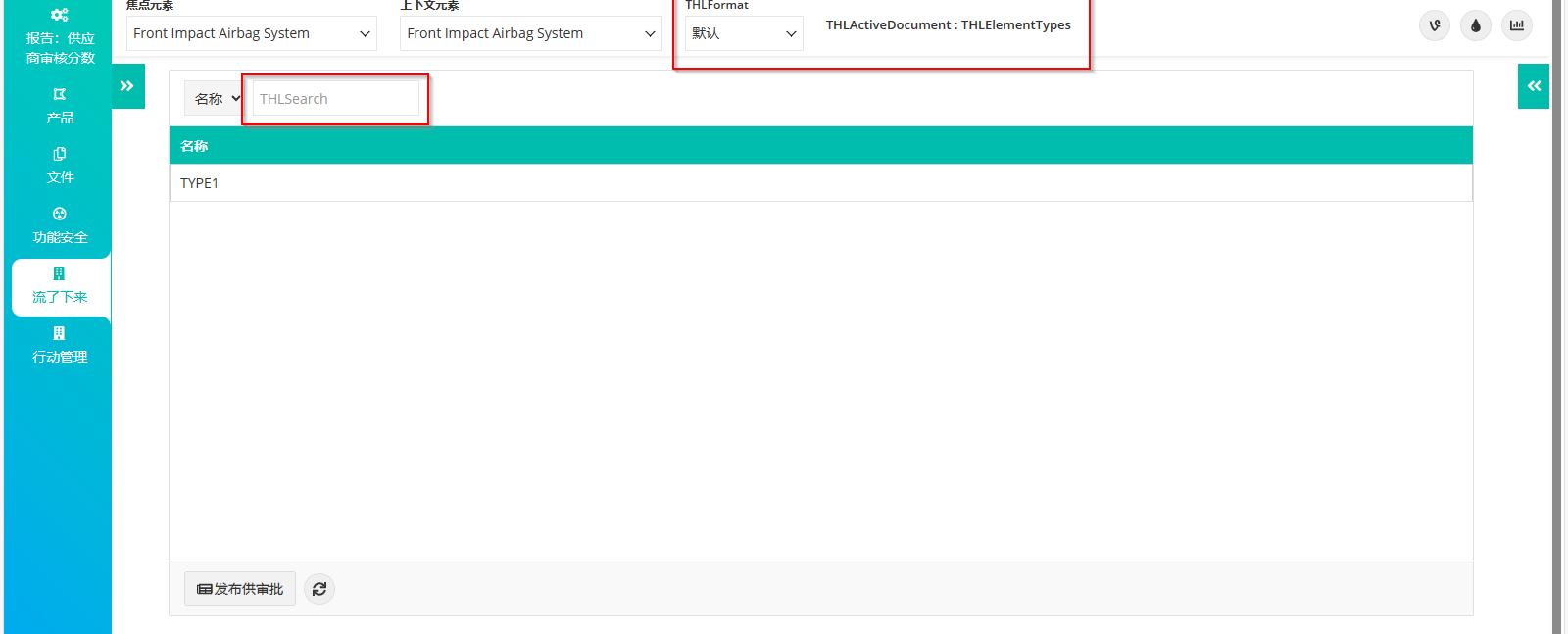
**Remove Existing Elements**

****

**Interface Types**

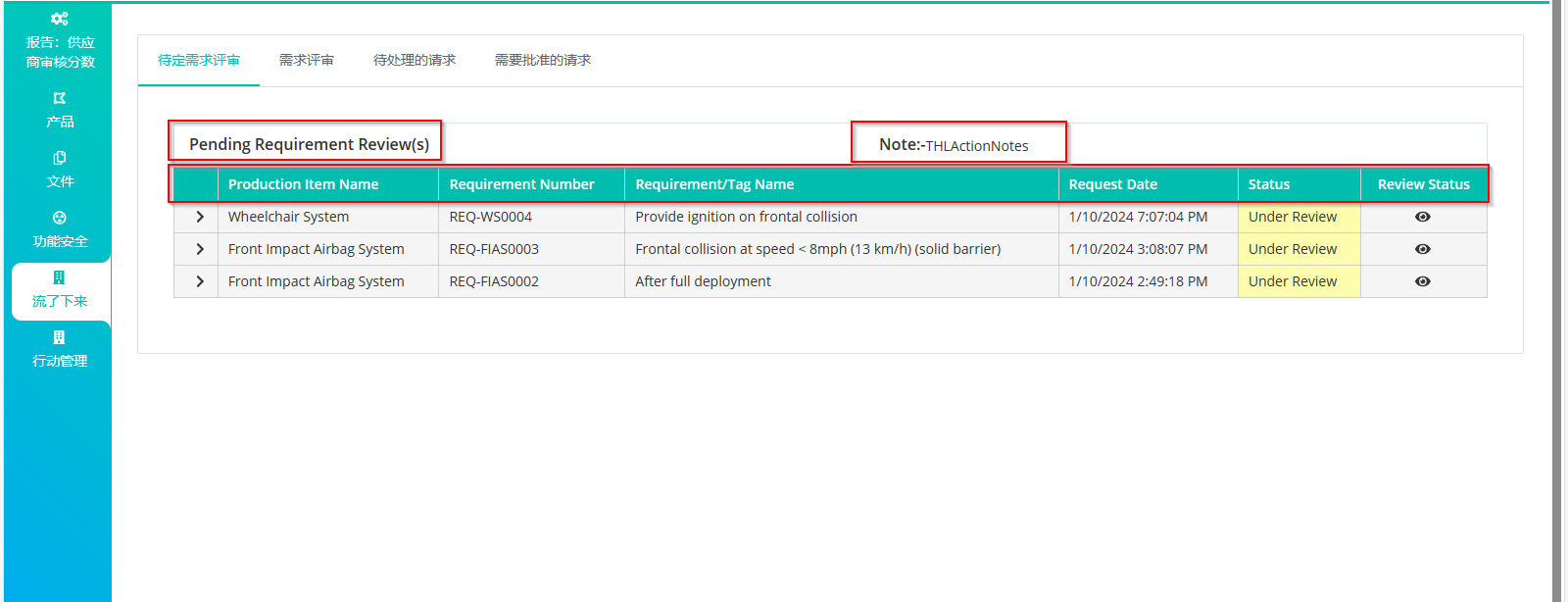
****

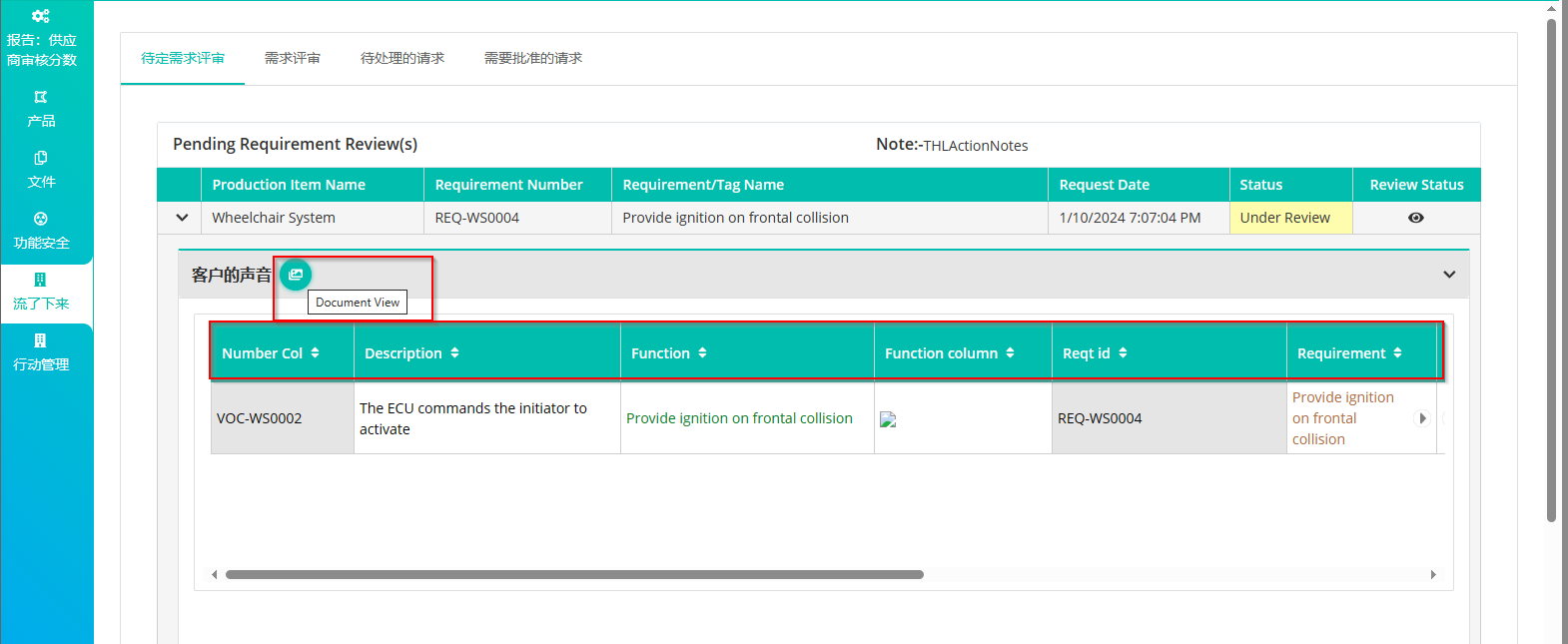
**Element Type**

****

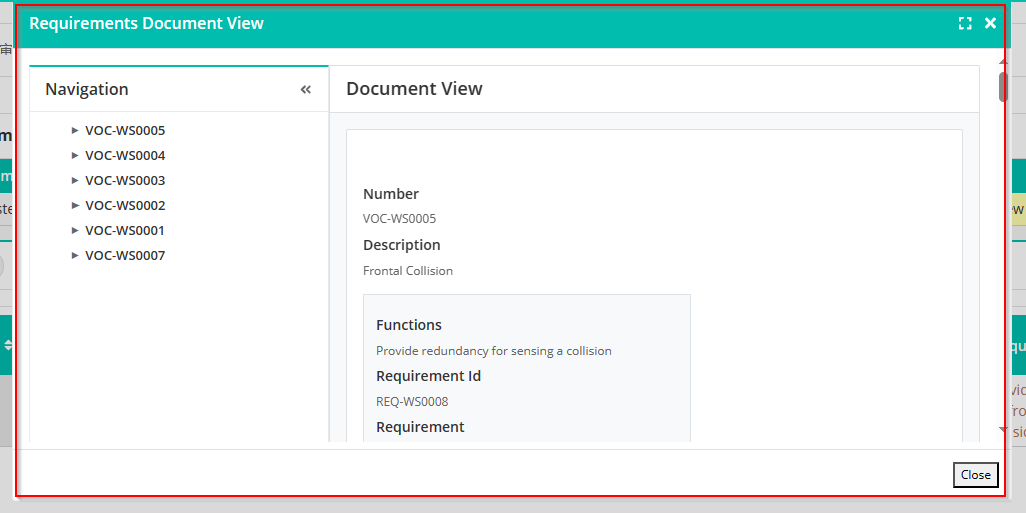
**Actions**

**Pending Requirements Review**

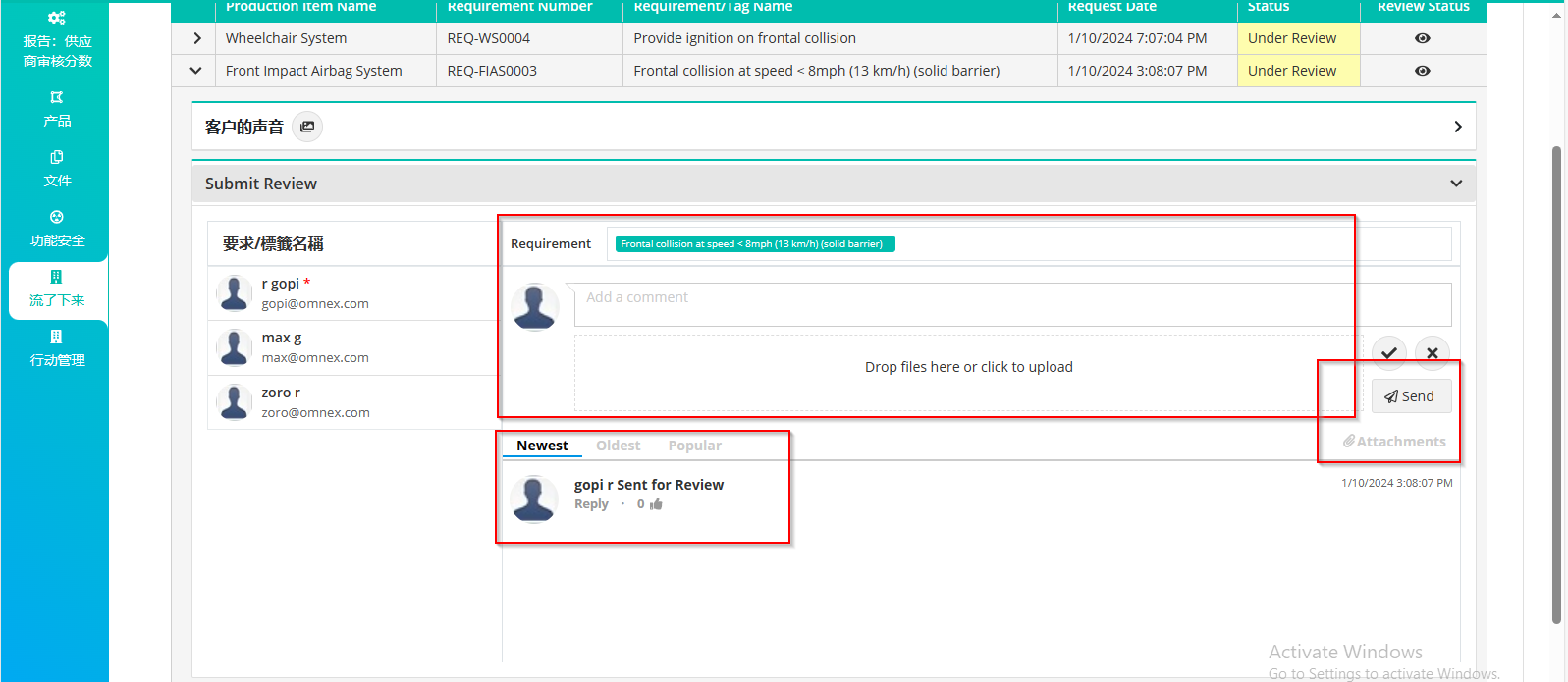
****

****

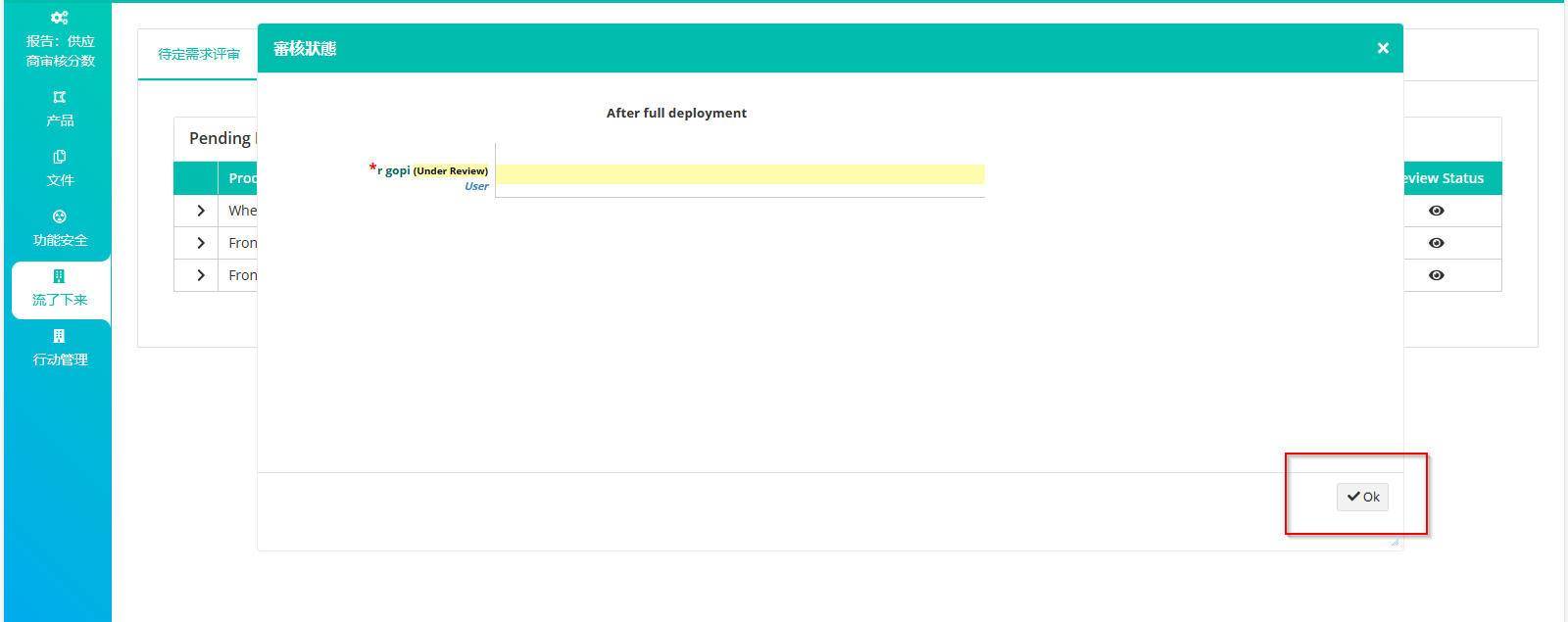
**Actions and Requirements Document Document View**

****

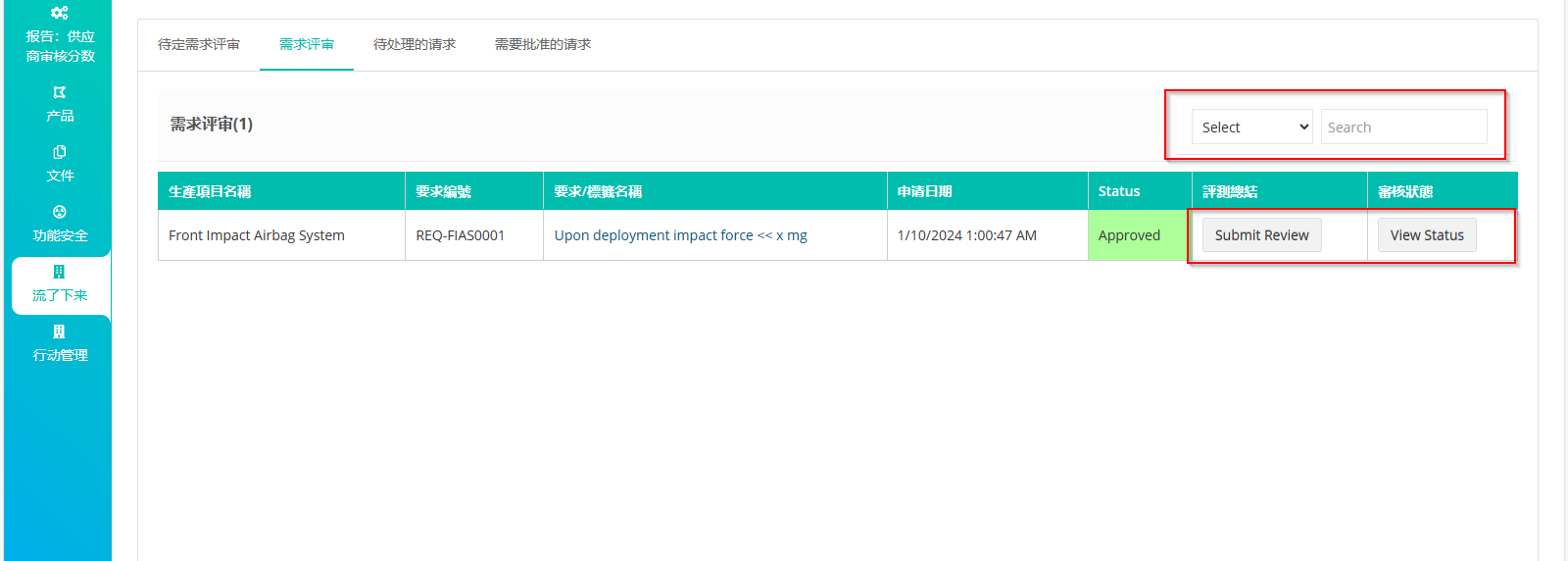
**Submit Review**

****

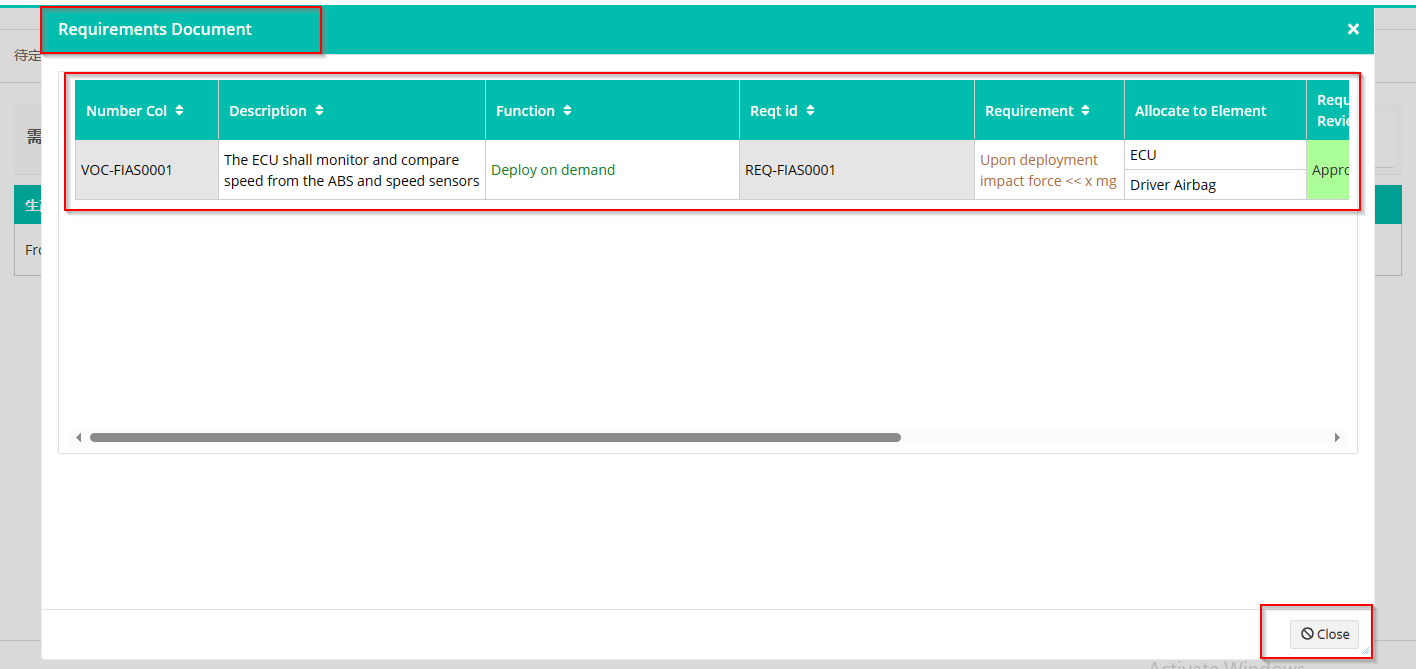
**Review status & View status**

****

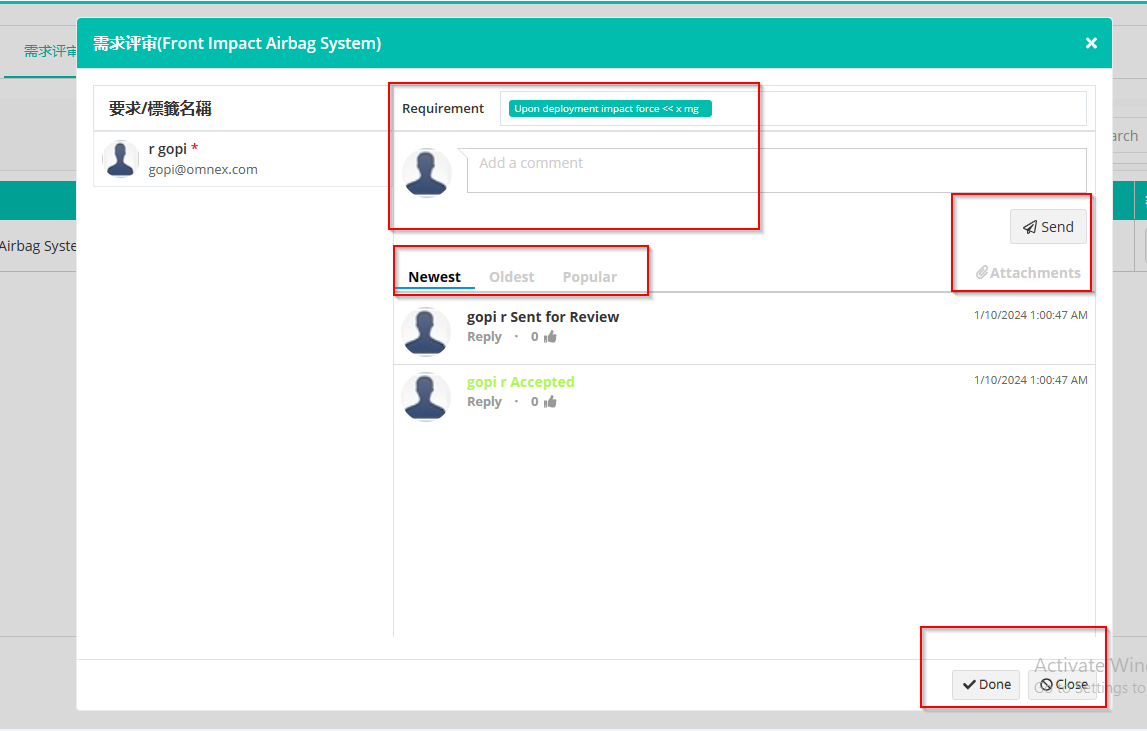
**Requirements Reviewed**

****

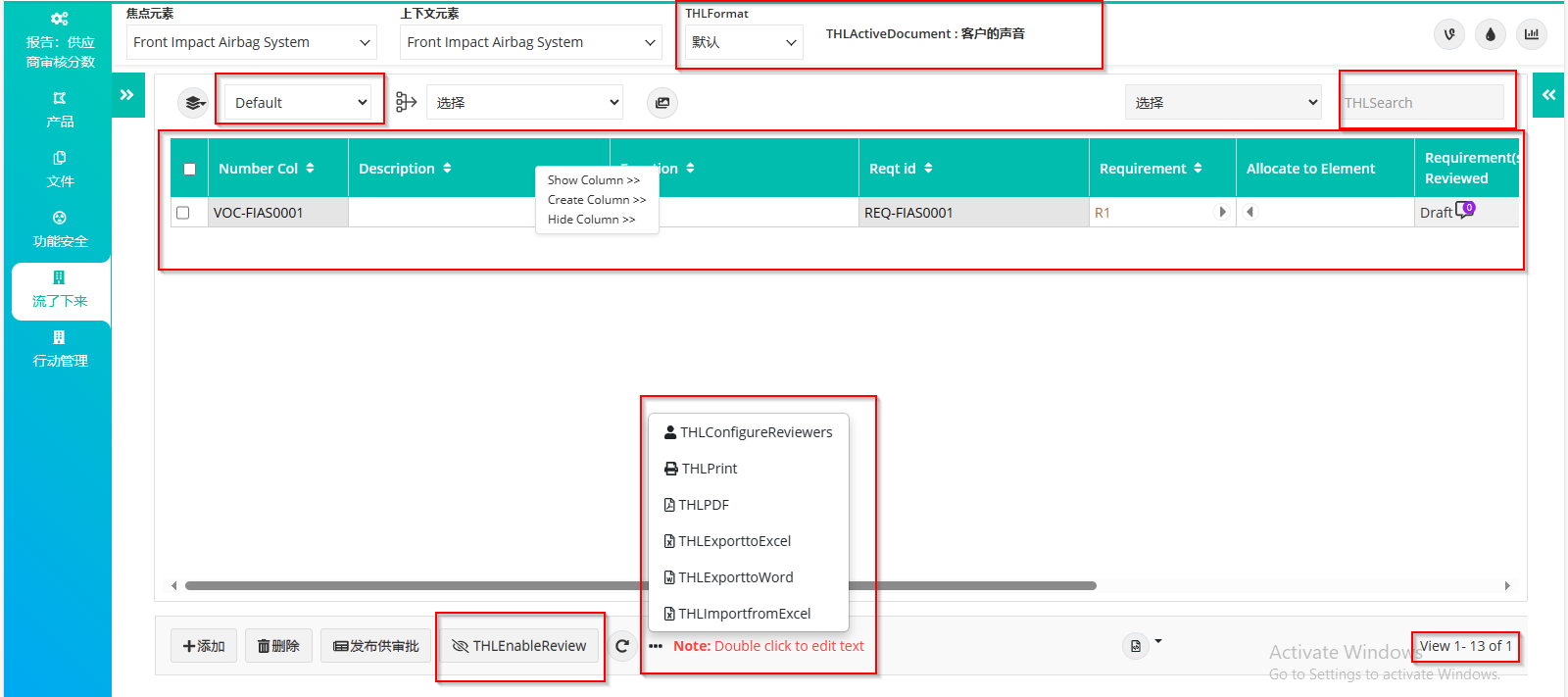
**Tag name**

****

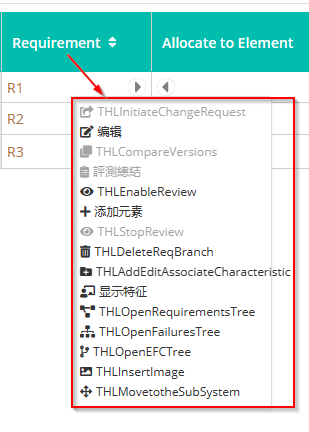
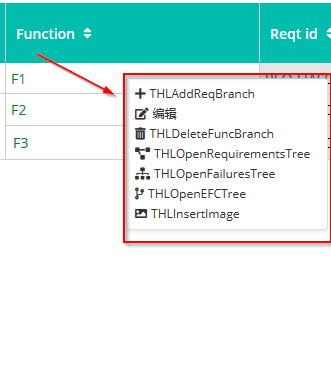
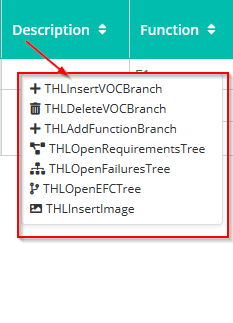
**Submit Review**

****

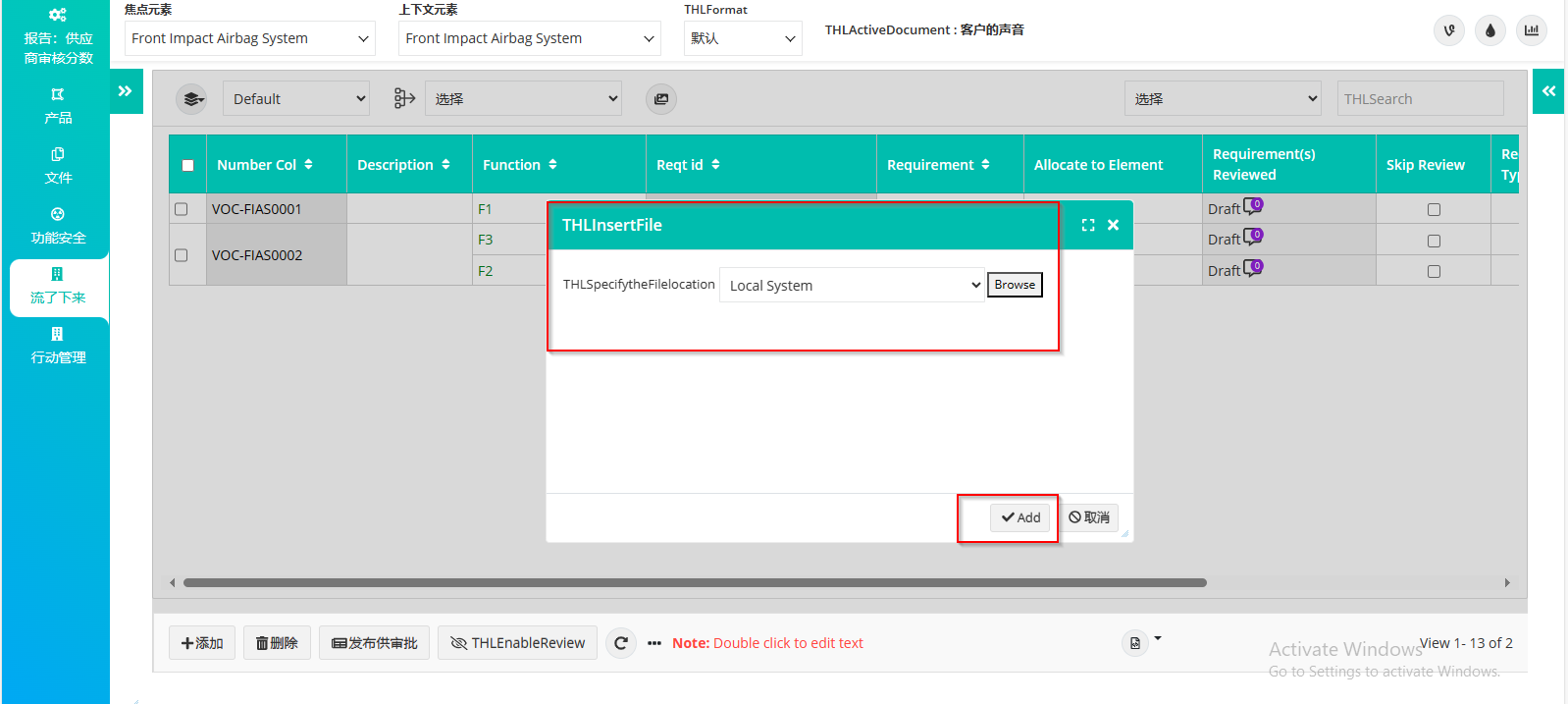
**Requirements Document**

****

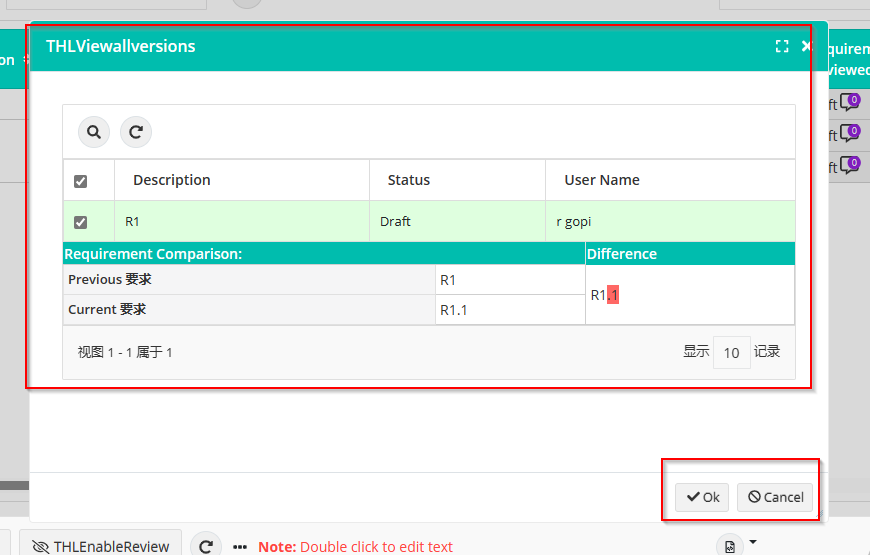
**Description Function Requirement**

****

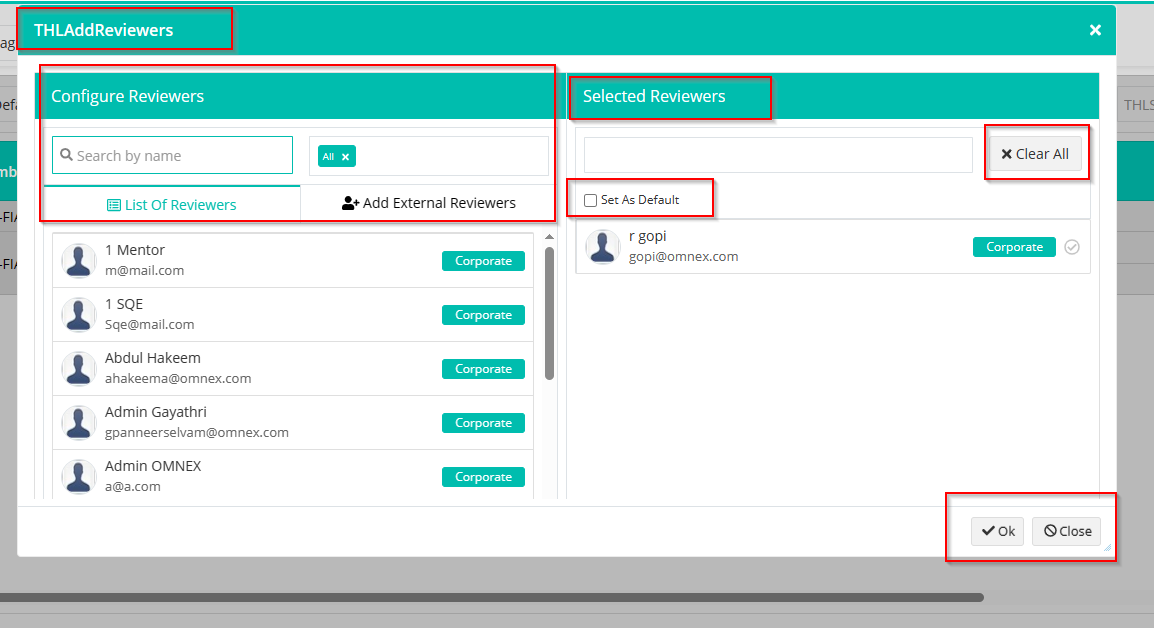
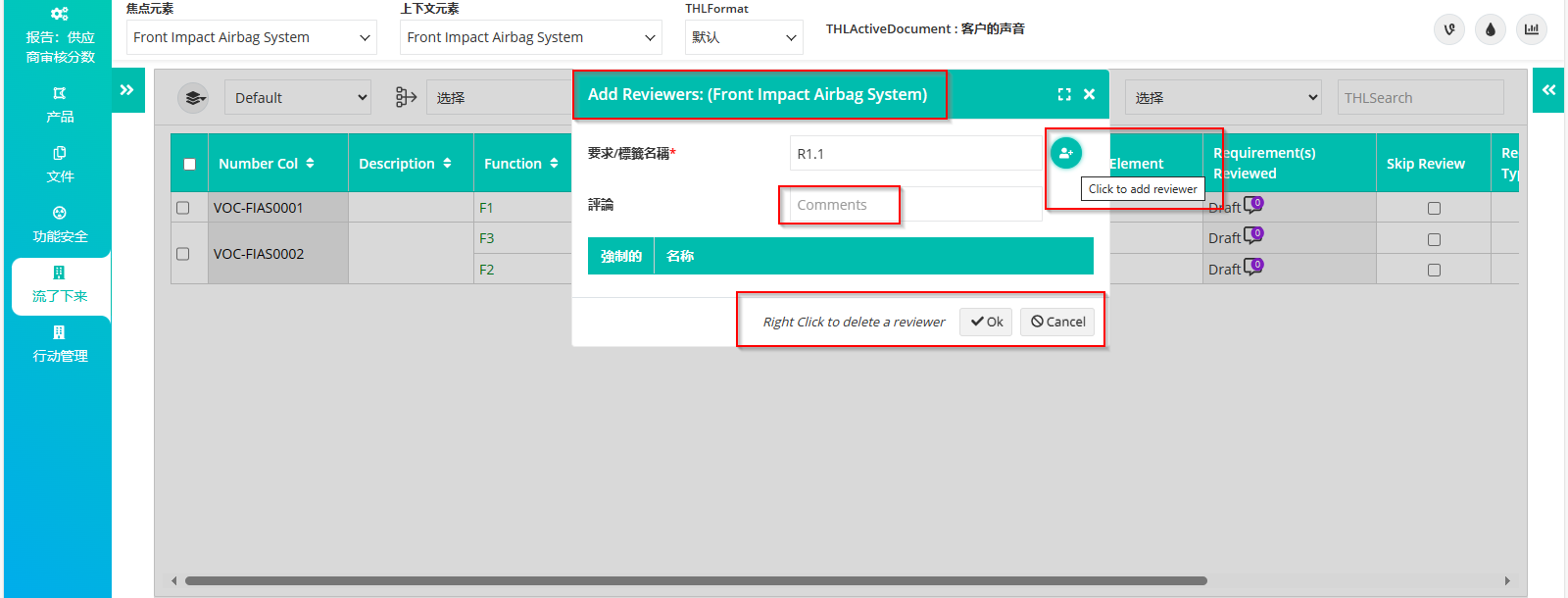
**Insert File**

****

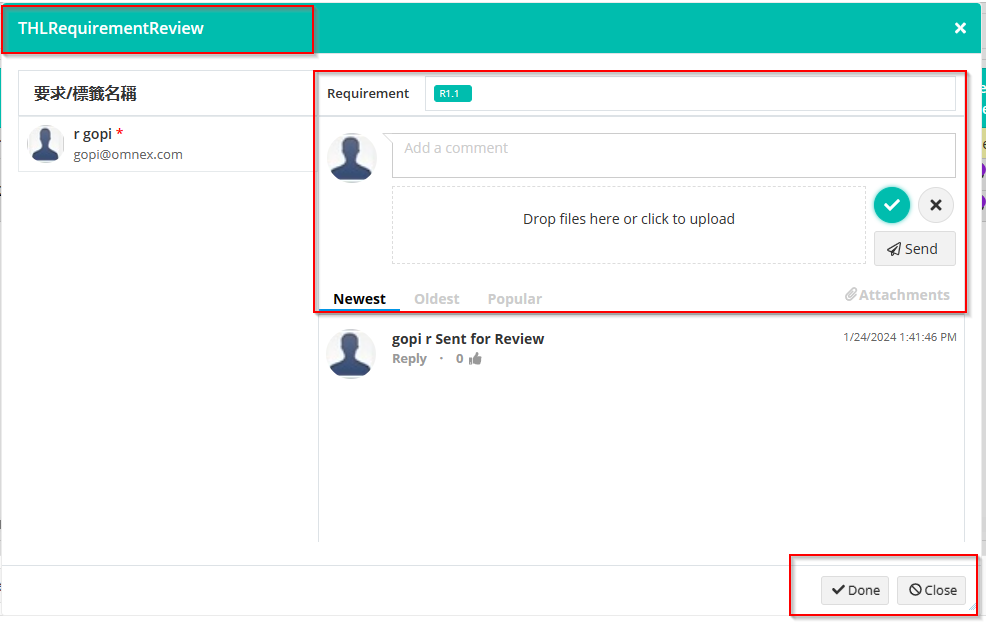
**Compare Version**

****

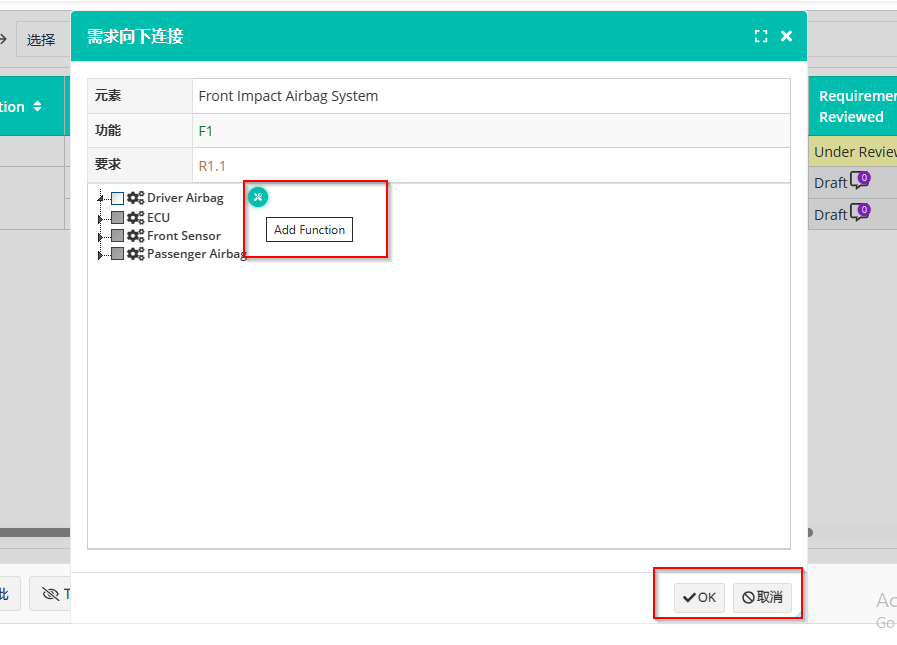
**Send For Review**

****

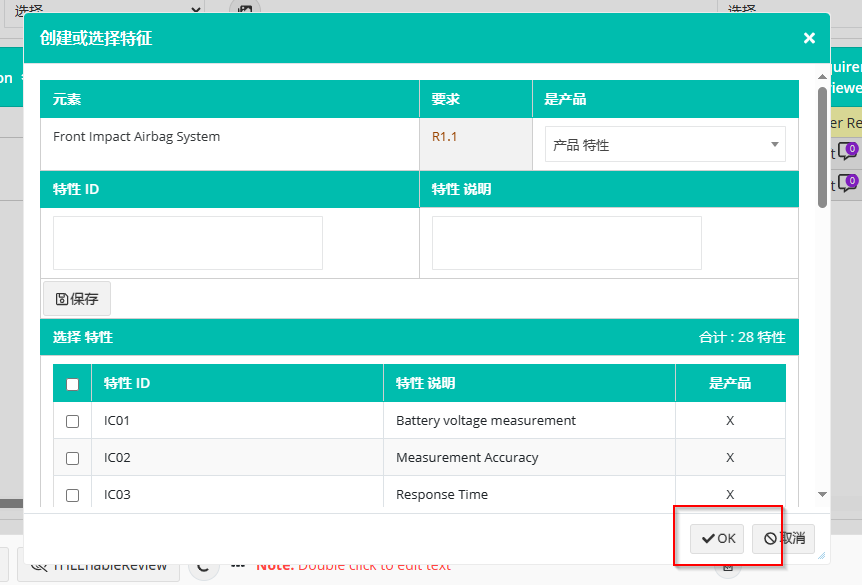
**Submit Review**

****

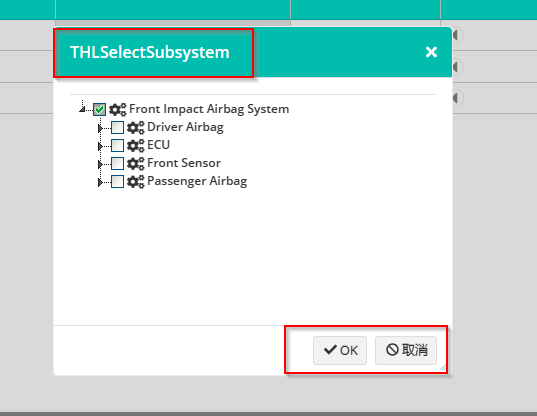
**Add Element**

****

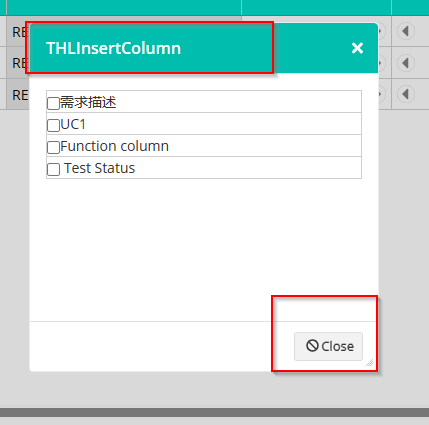
**Add Associate Characteristics**



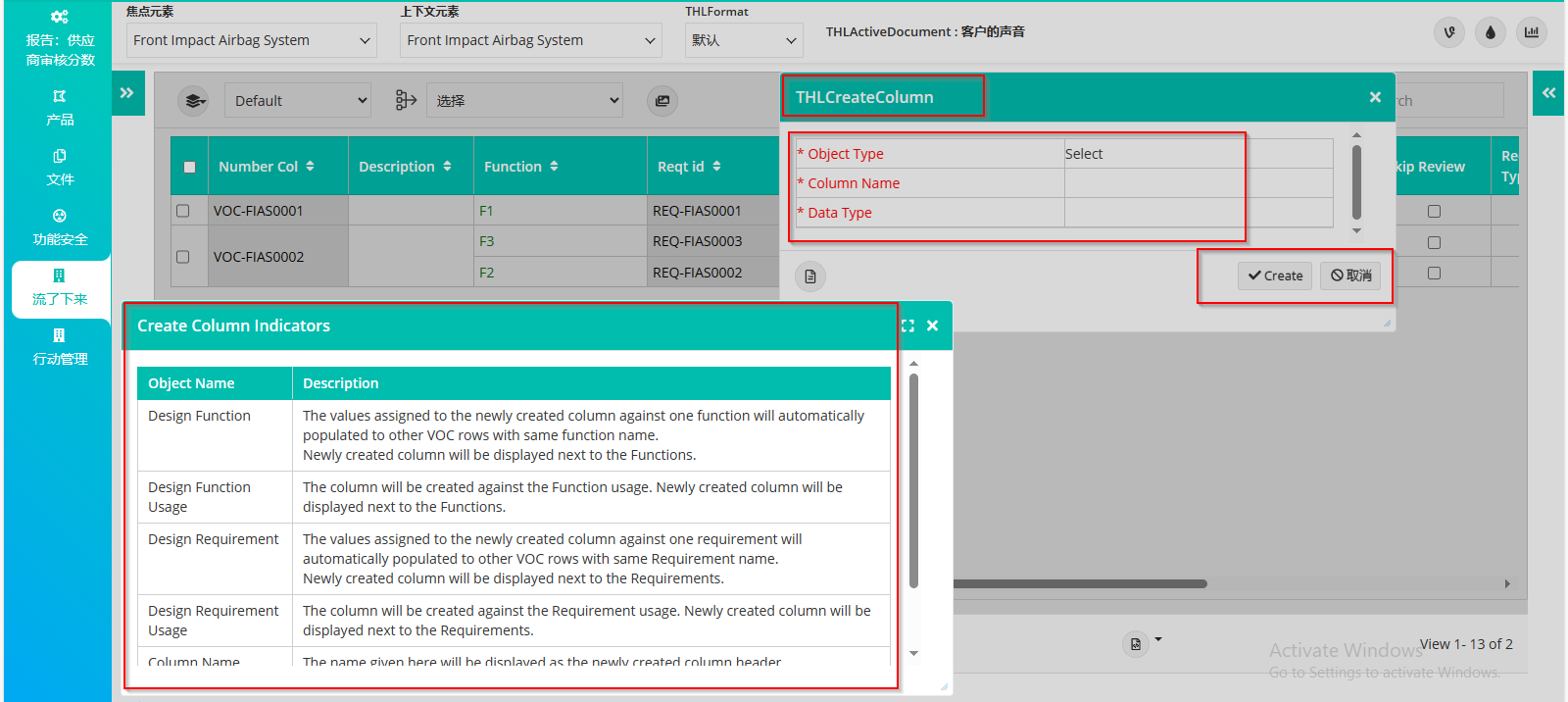
**Move to Sub system**

****

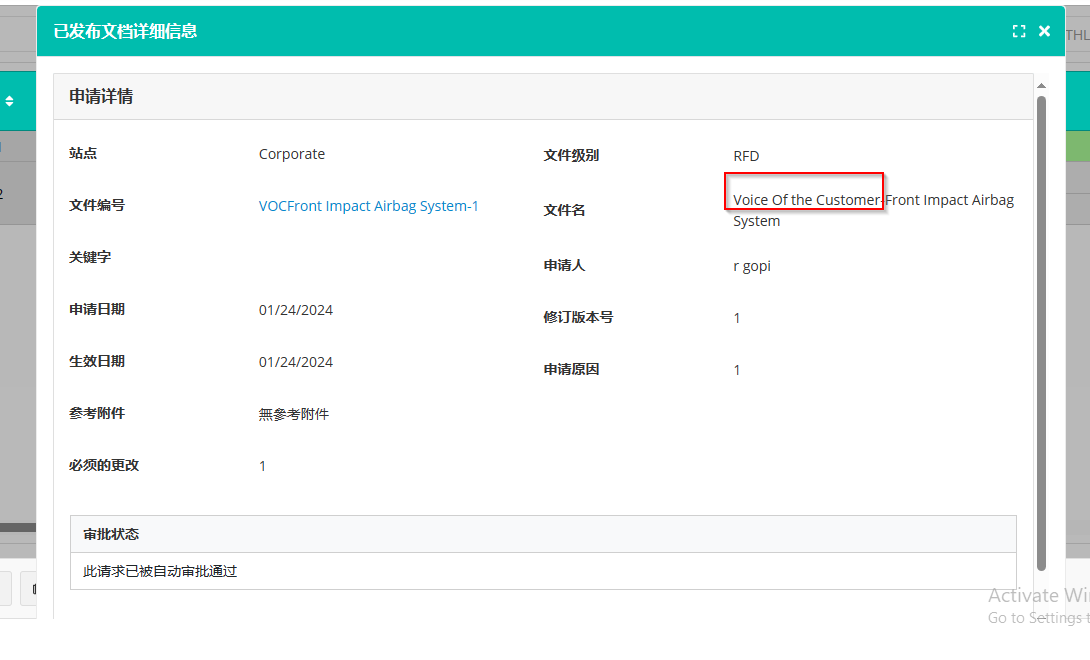
**Insert Column**

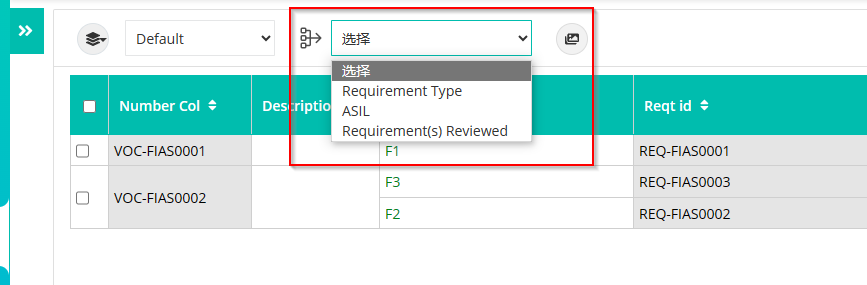
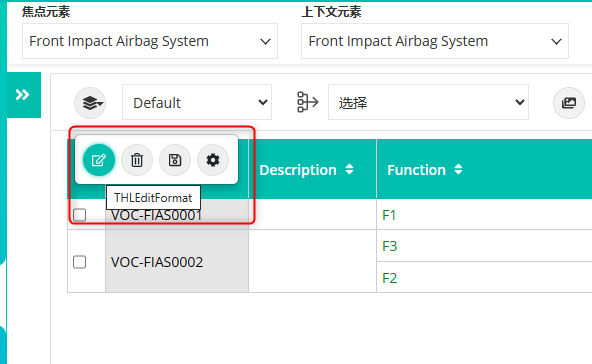
****

**Create column**

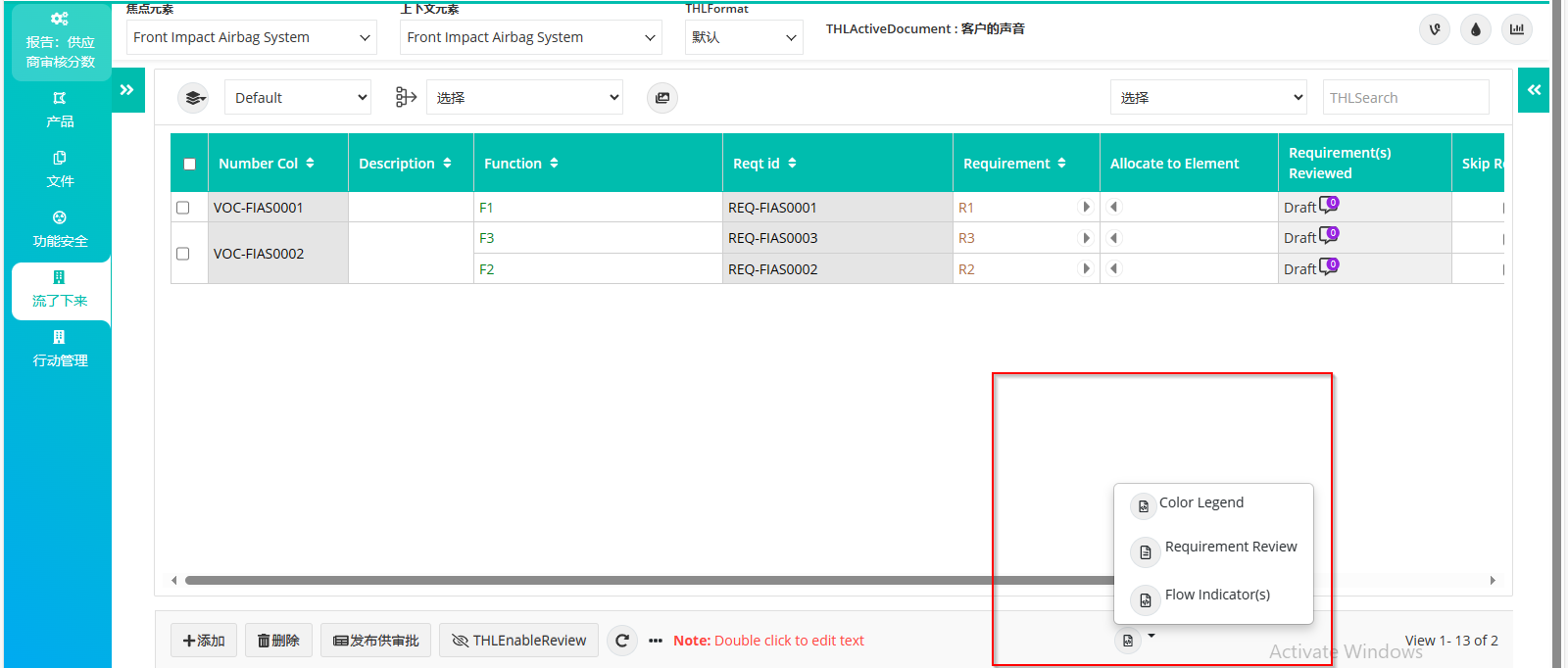
****

**Published Document**

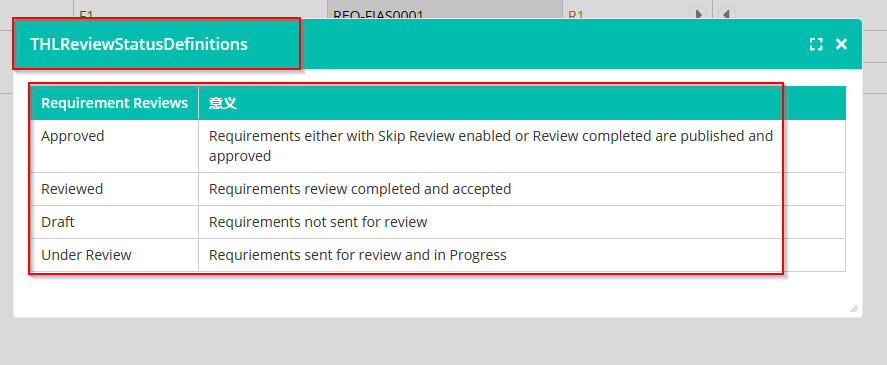
****

****

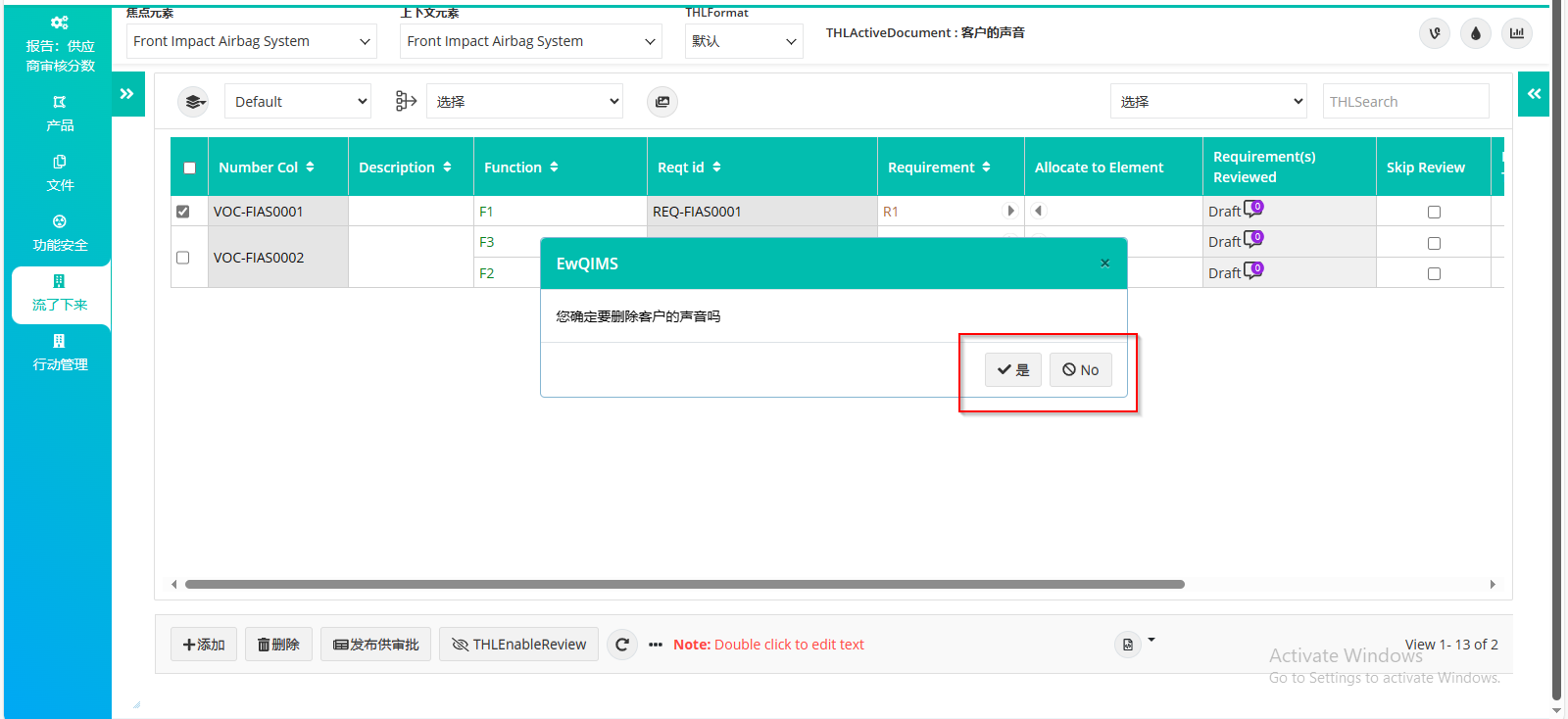
**Legends**

****

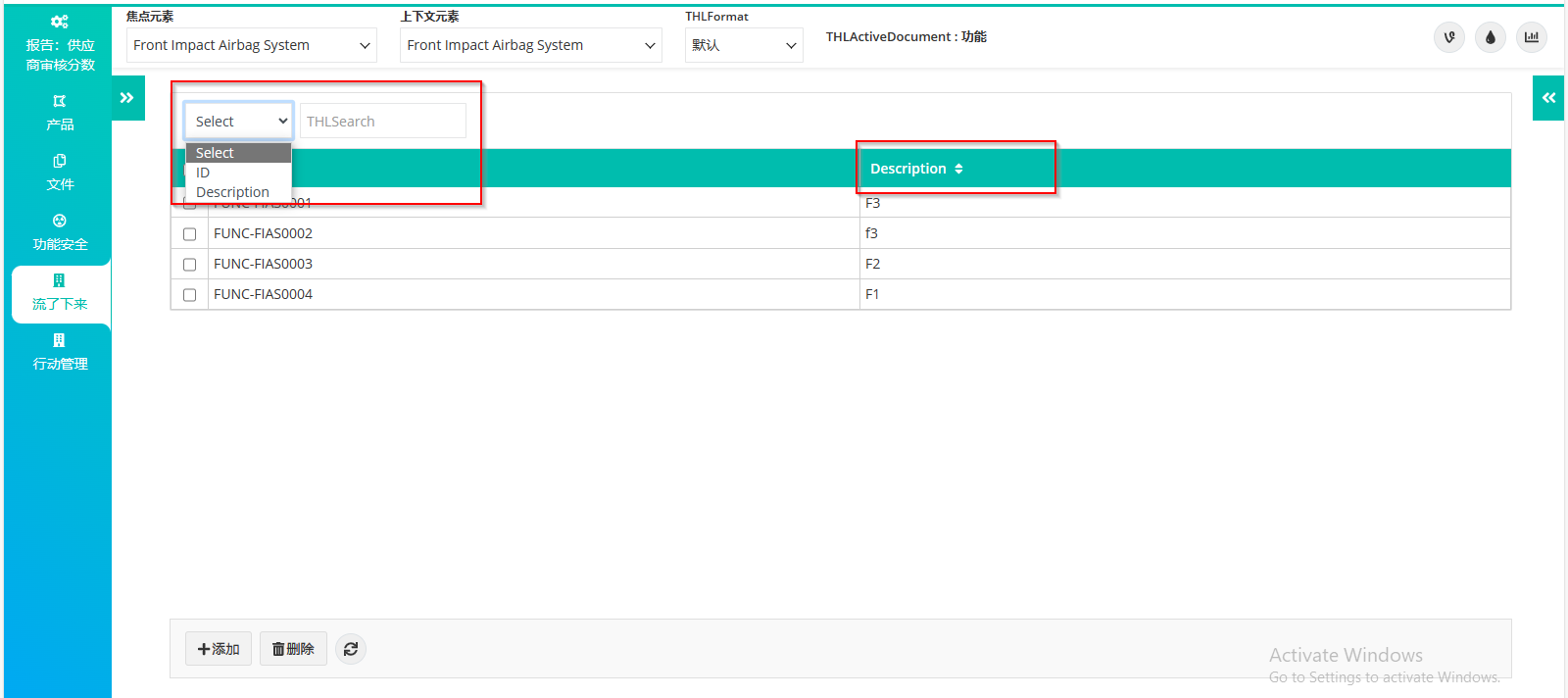
****

****

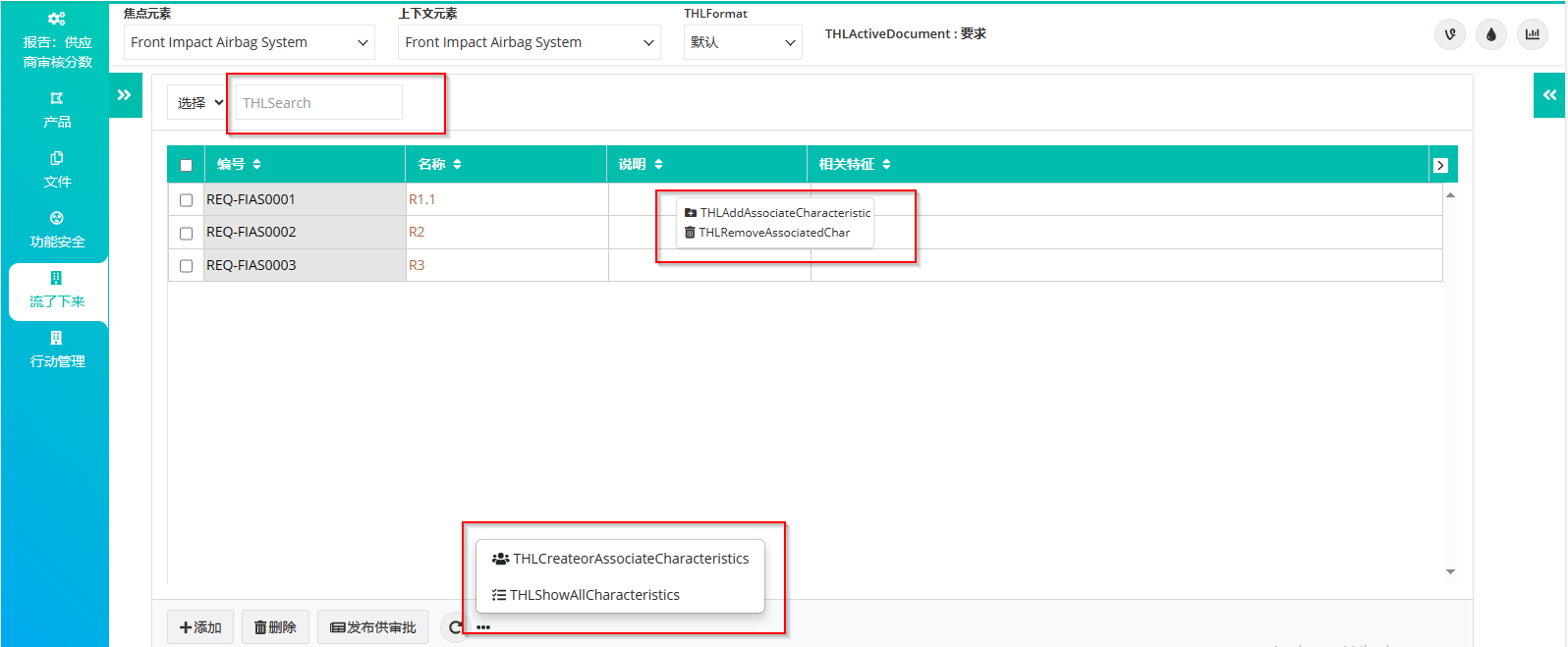
**Delete VOC**

****

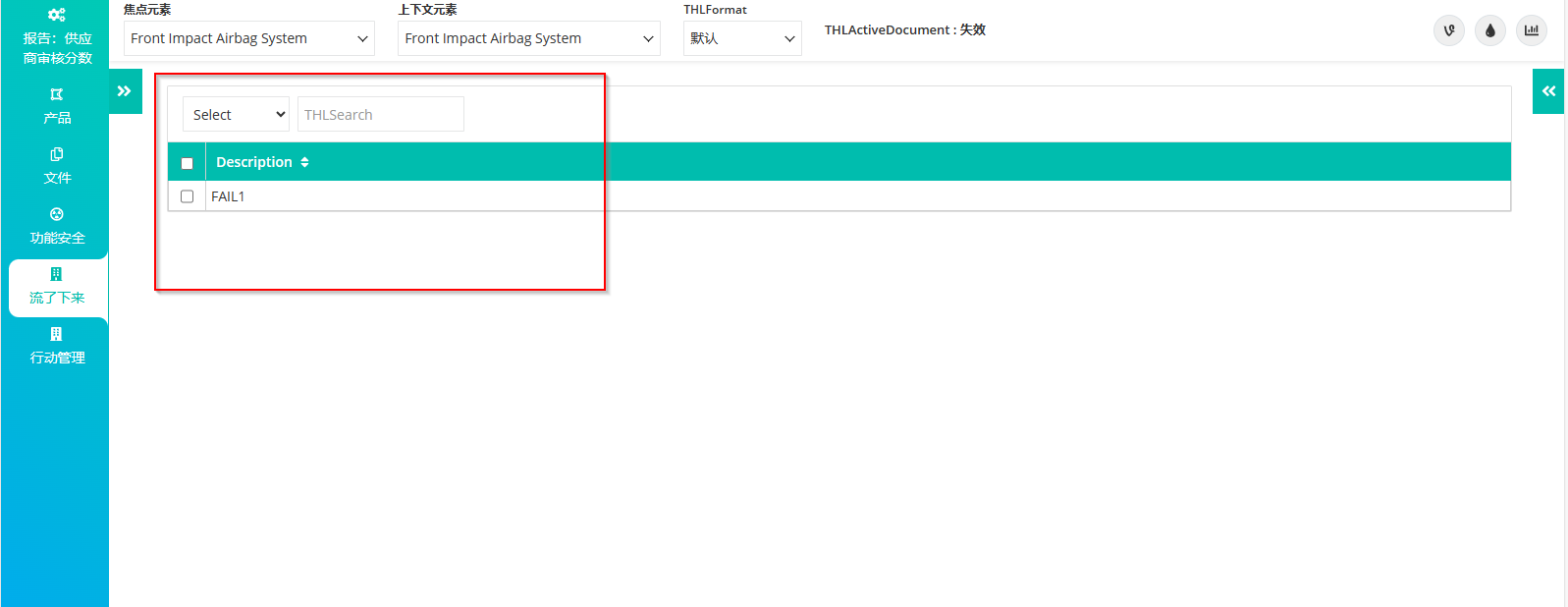
**Functions**

****

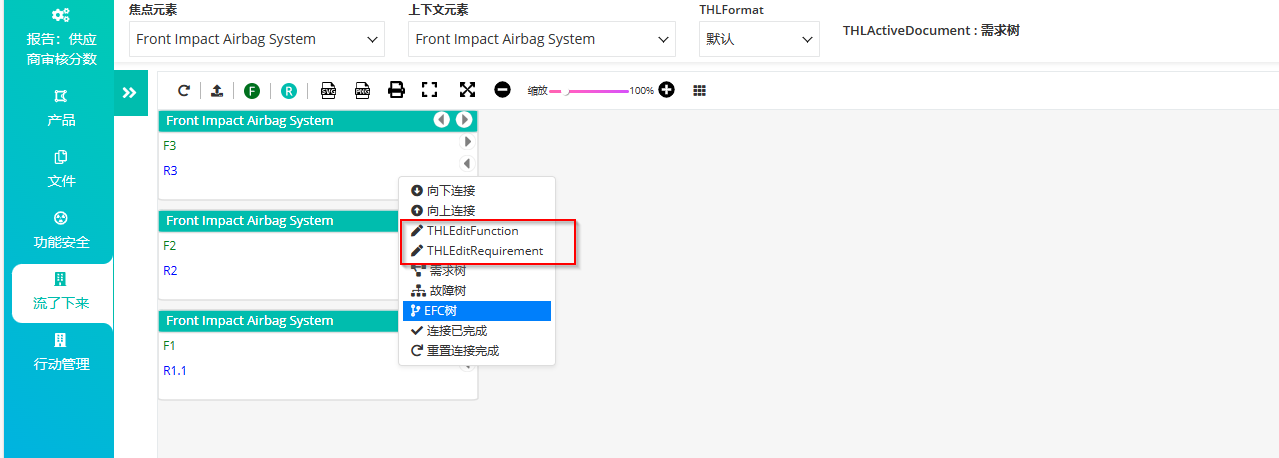
**Requirements**

****

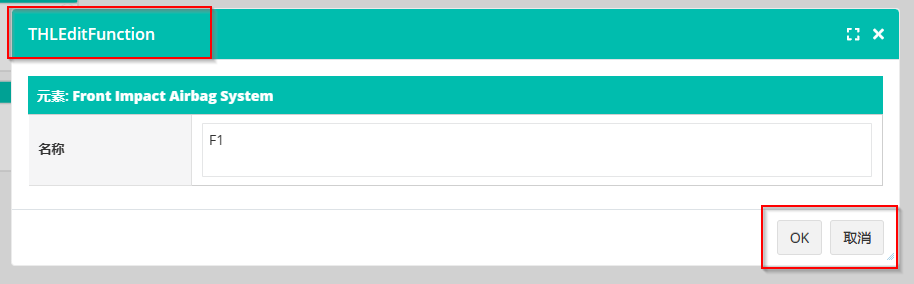
**Failures**

****

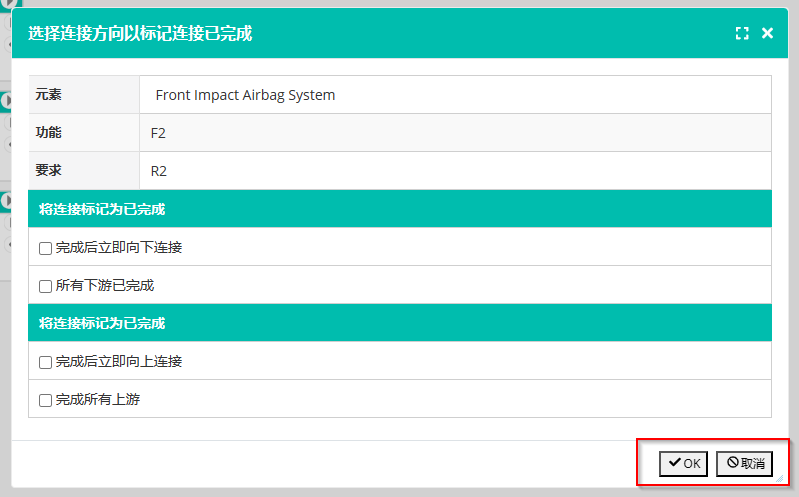
**Requirements Tree**

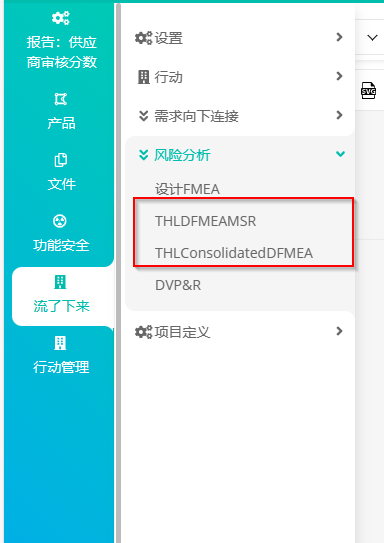
****

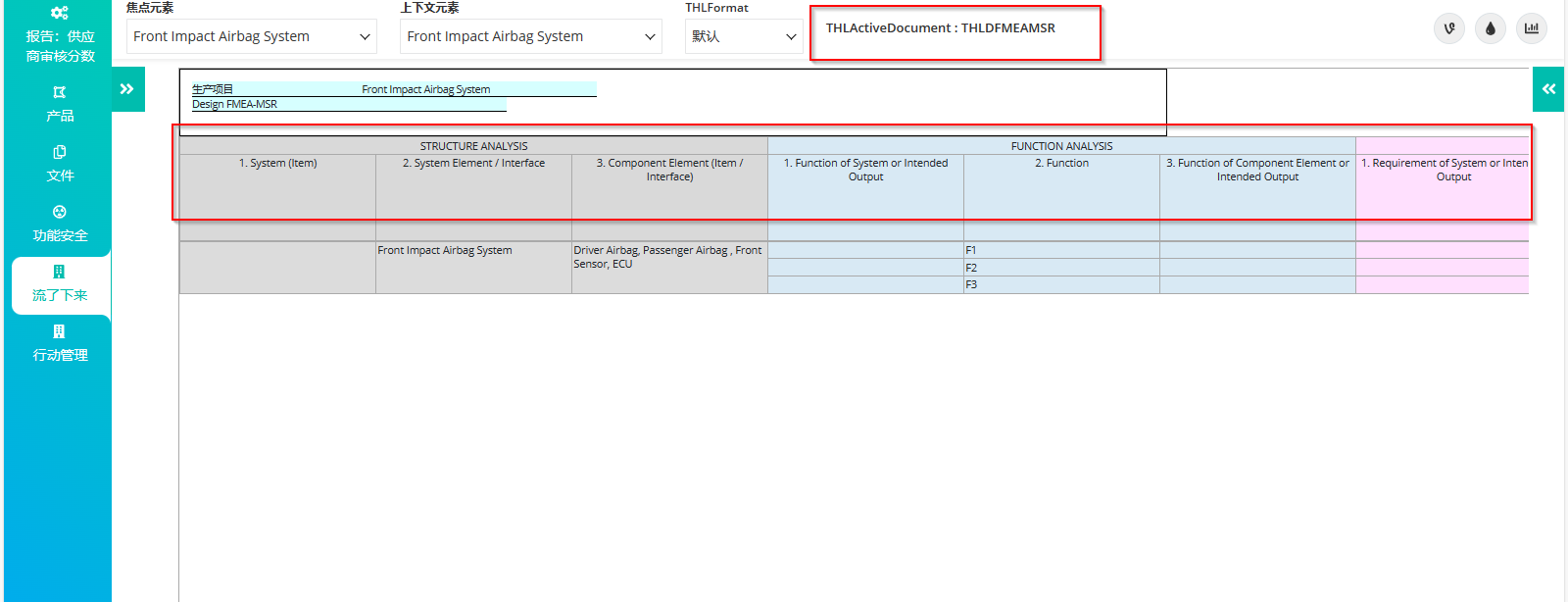
**Edit Function**

****

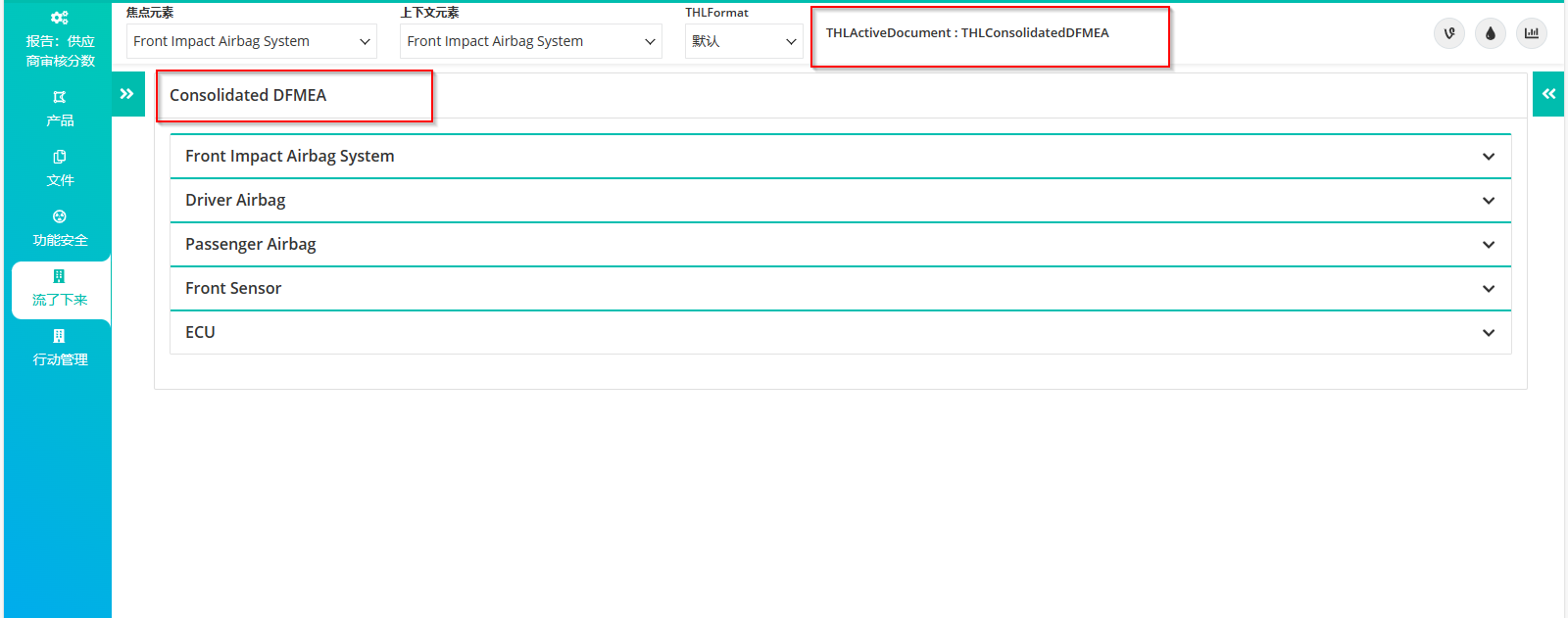
**Flows Done & Rest Flows Done**

****

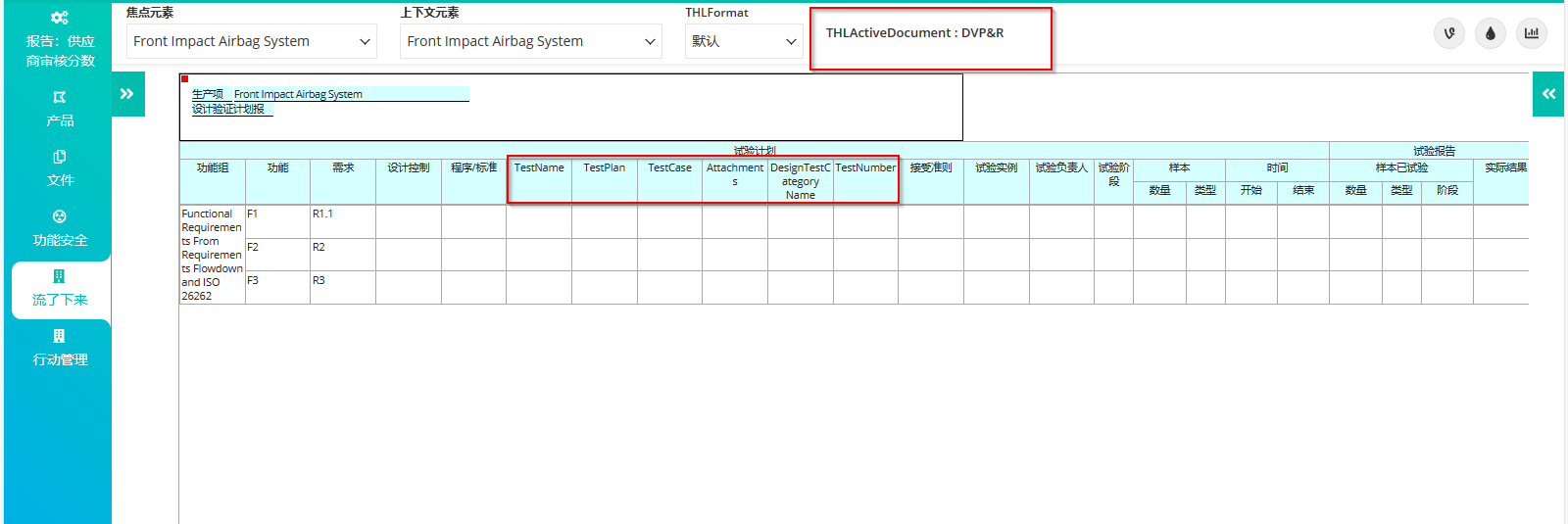
****

****

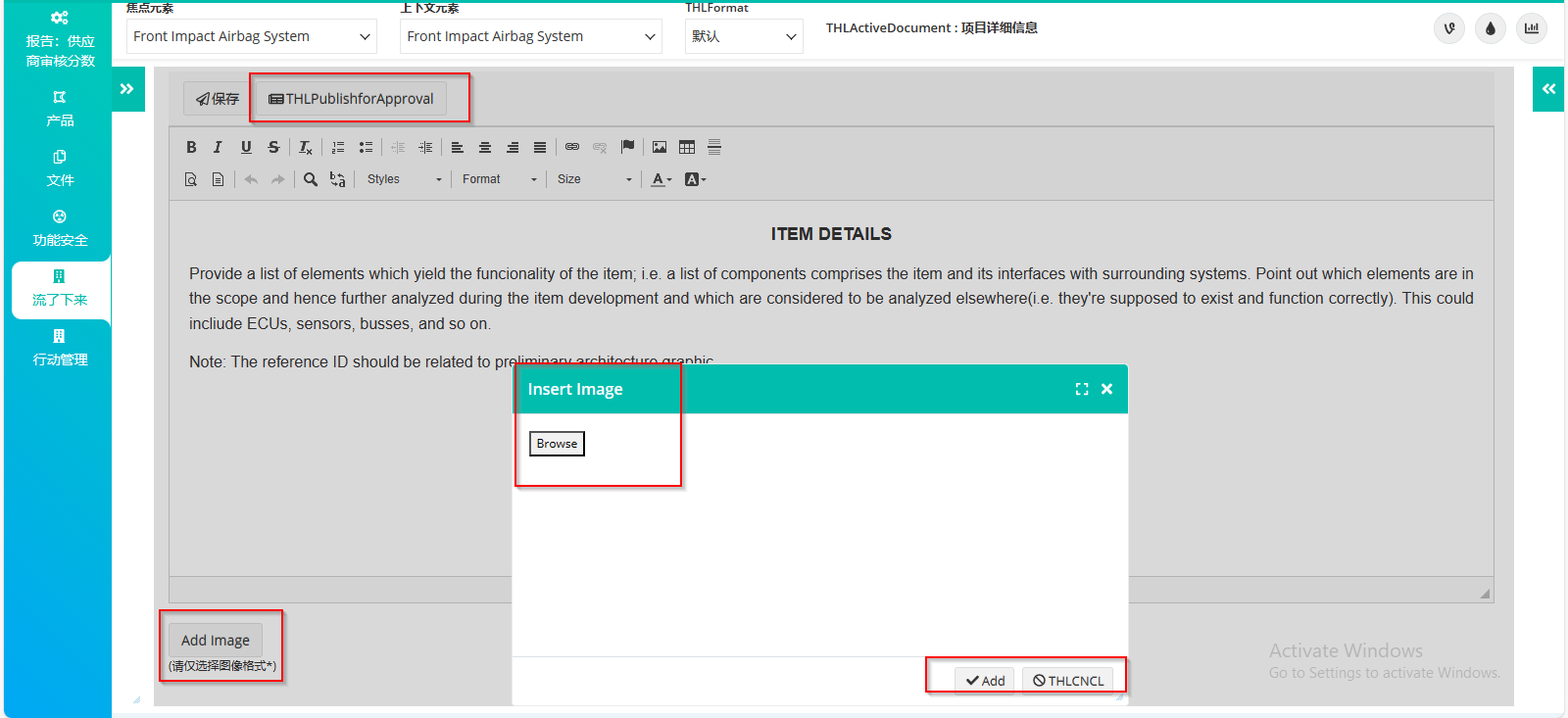
**Consolidated DFMEA**

****

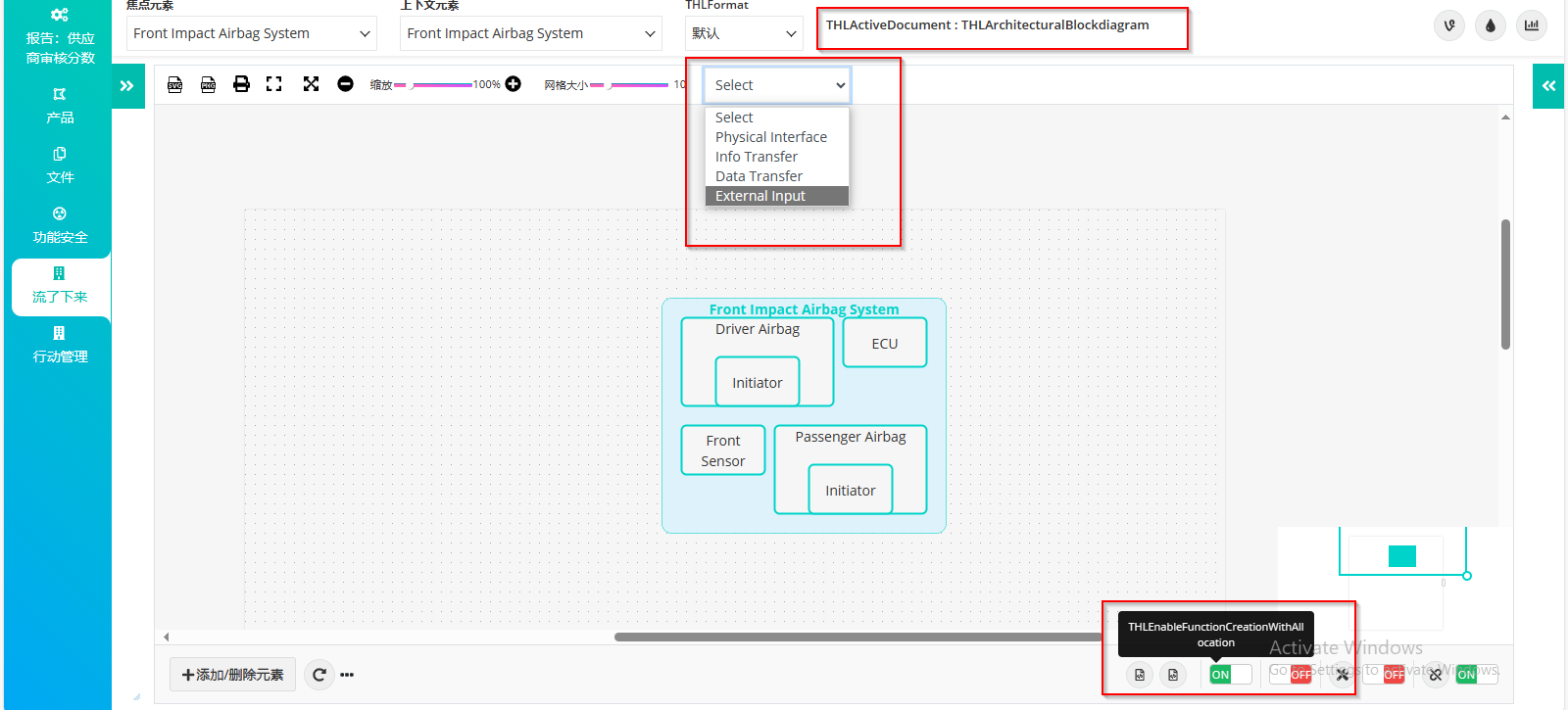
**DVP&R**

****

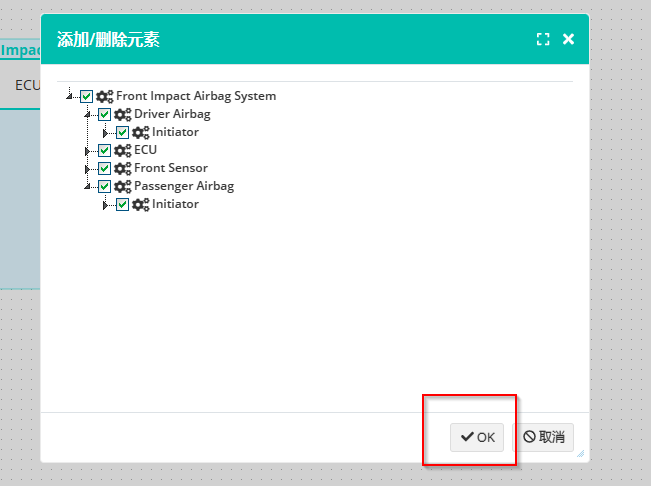
**Item Details**

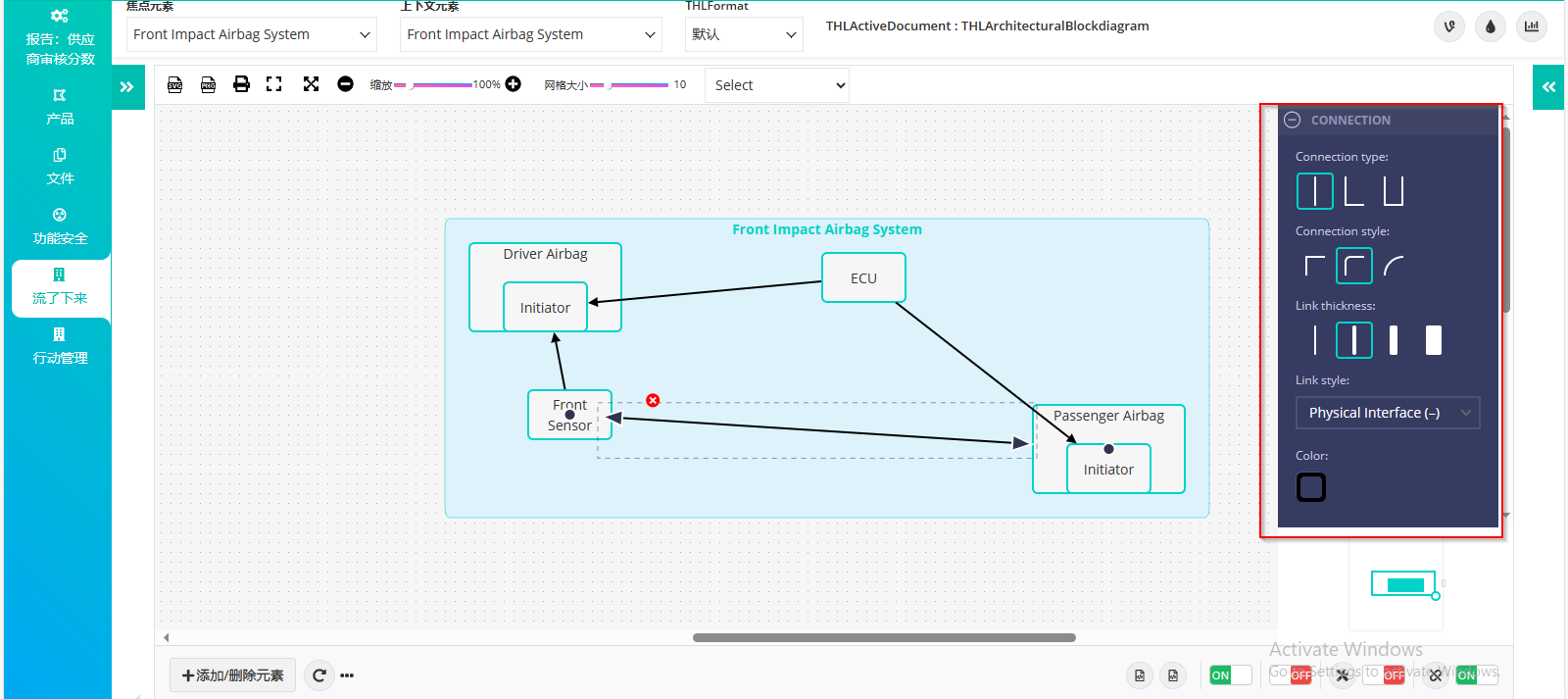
****

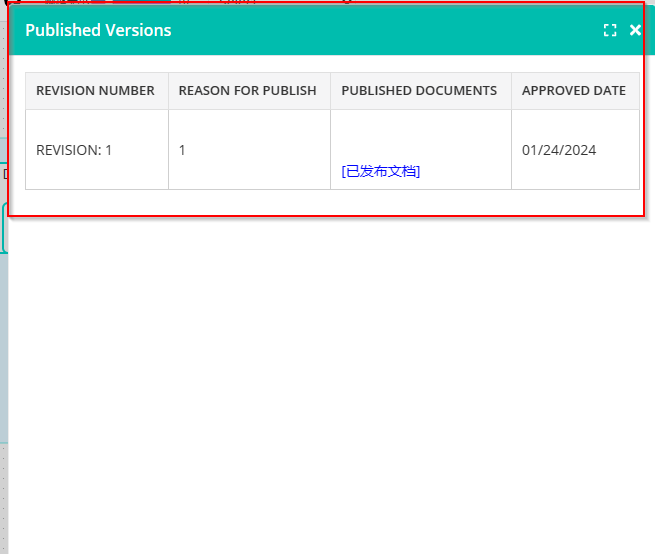
**Block Diagram**

****

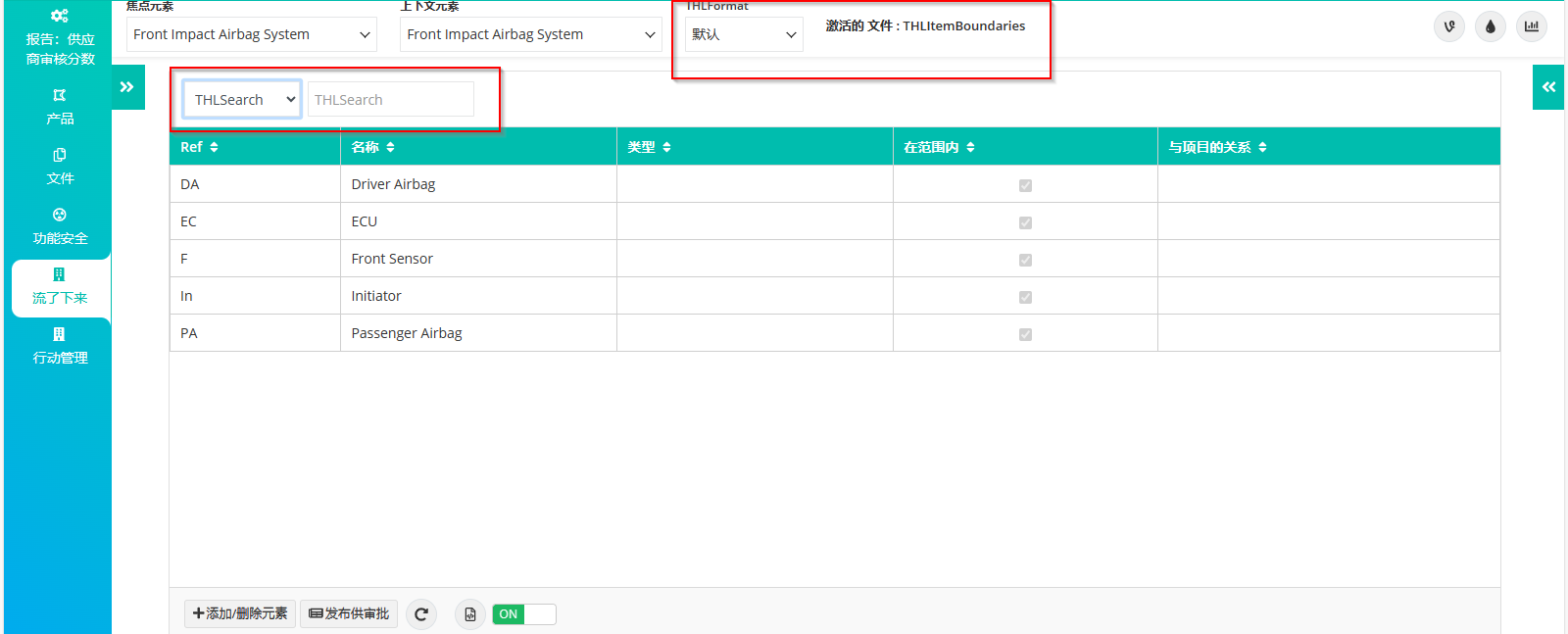
**Add/Remove Elements**

****

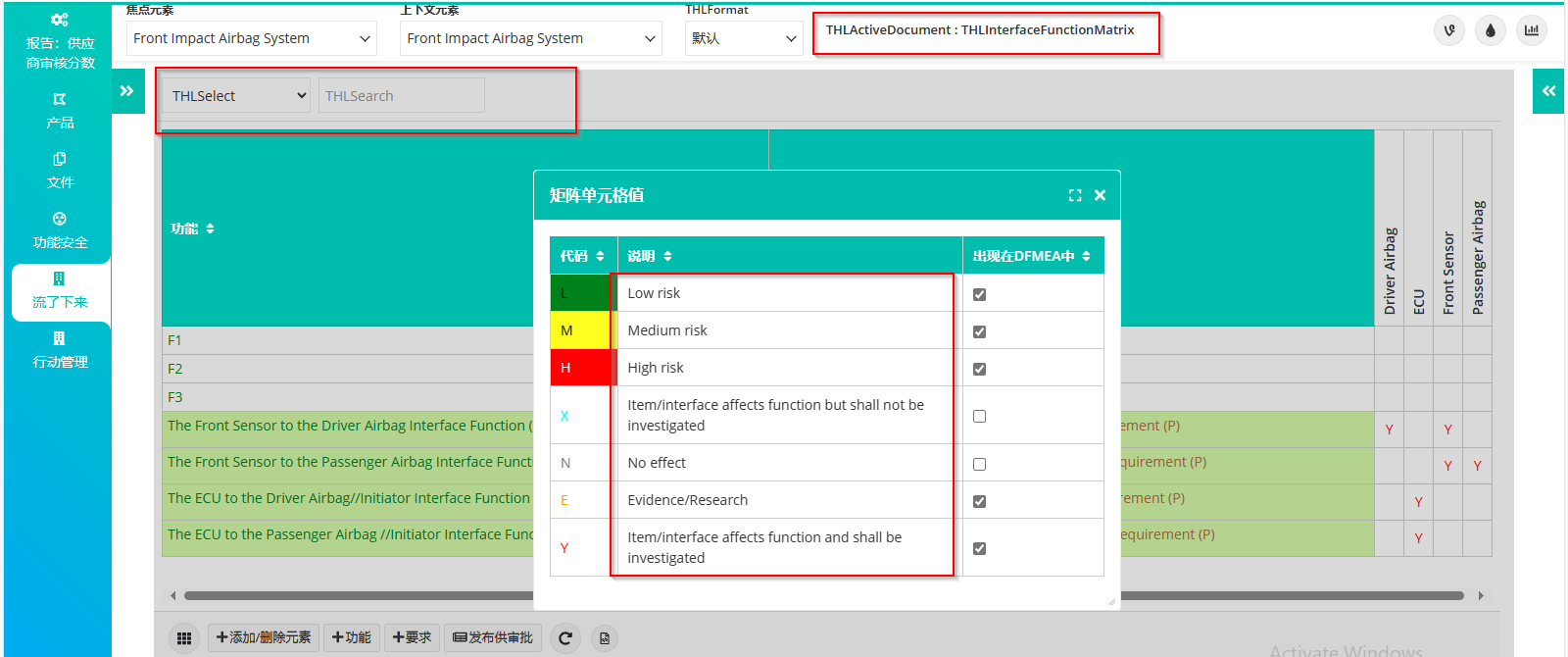
****

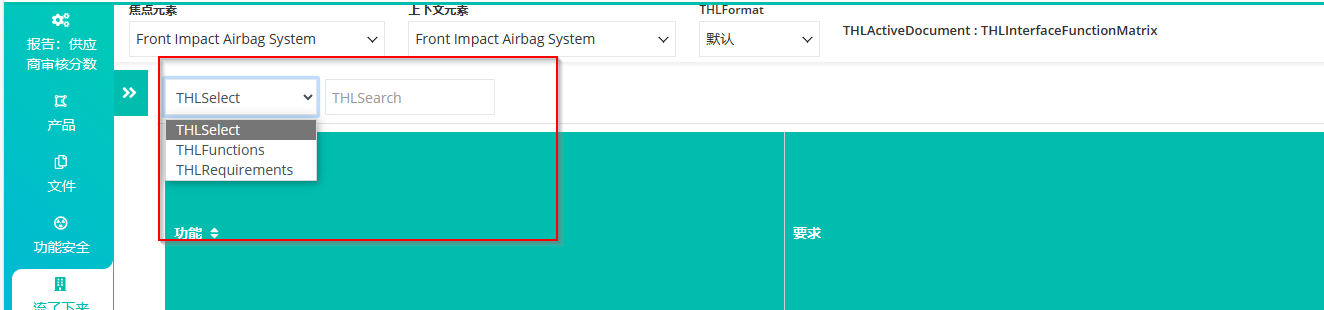
****

**Item Boundaries**

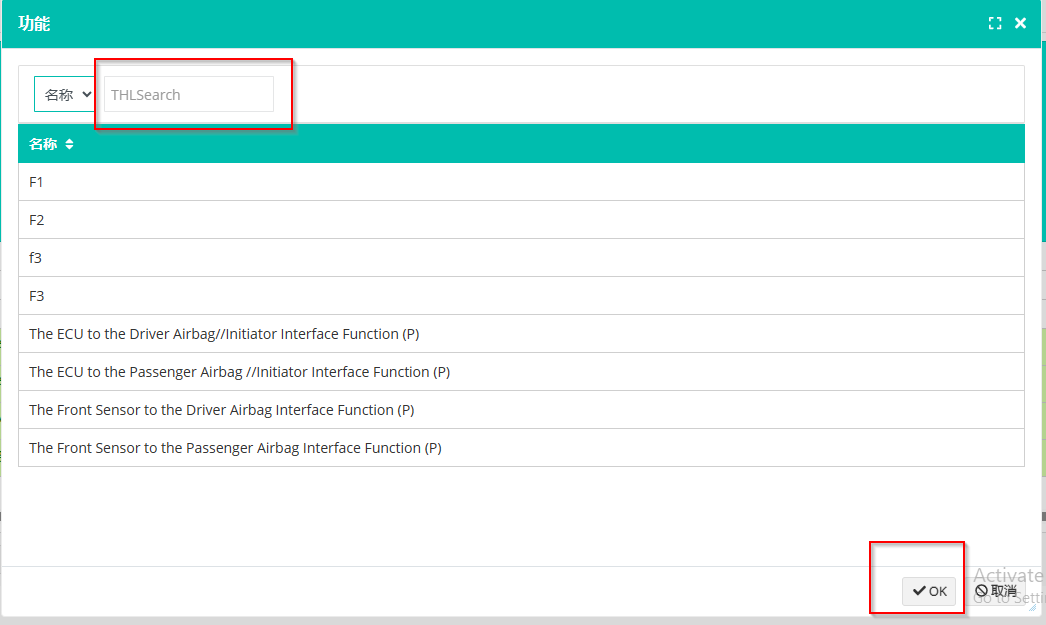
****

**IFM**

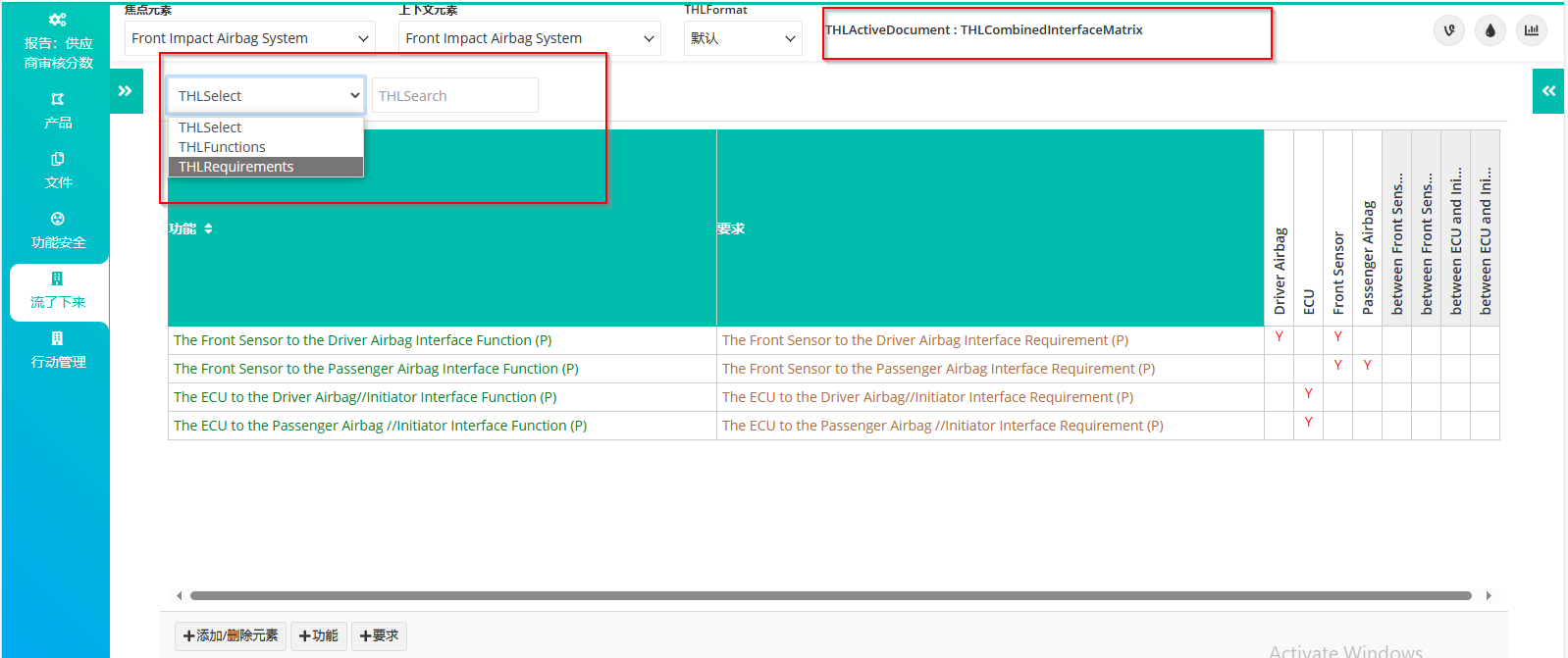
****

****

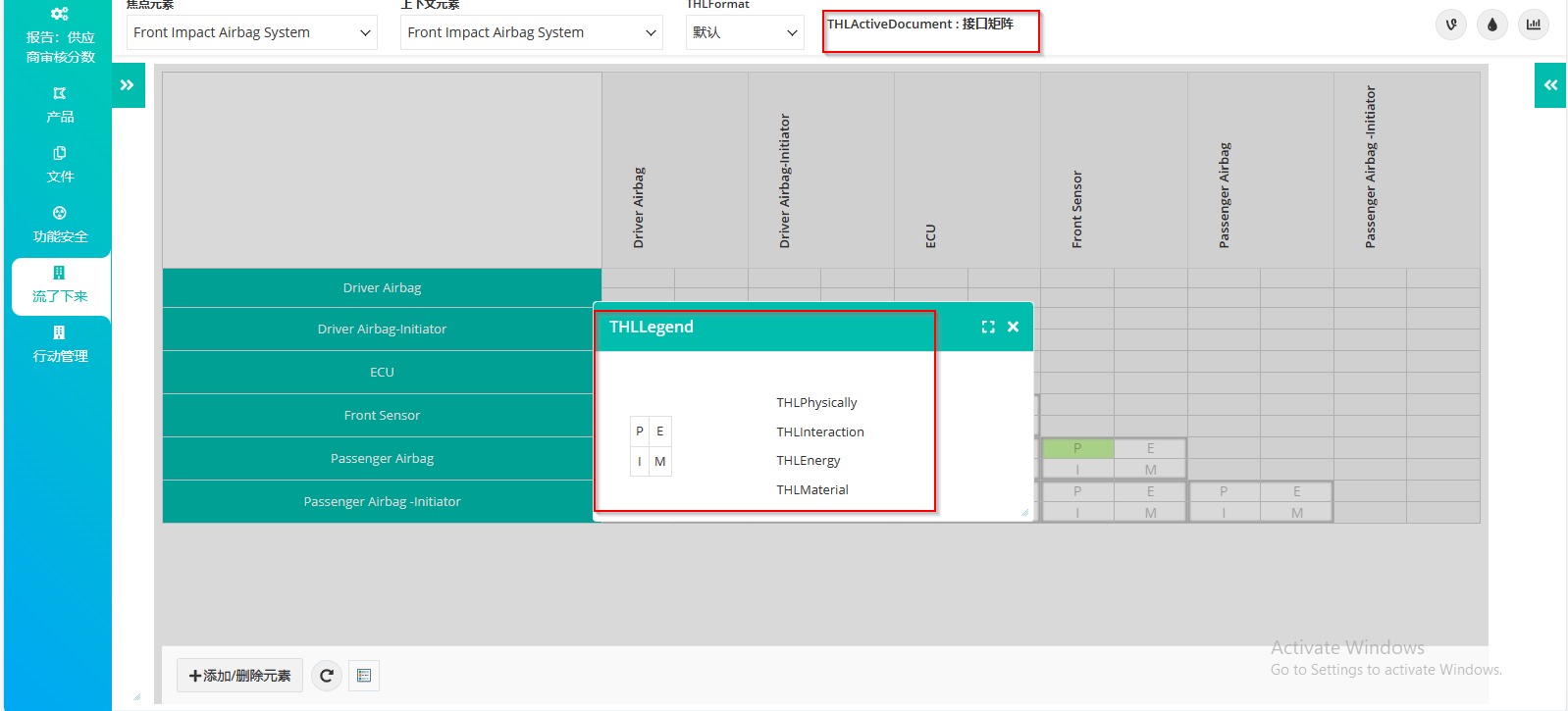
**+Functions and +Requirements**

****

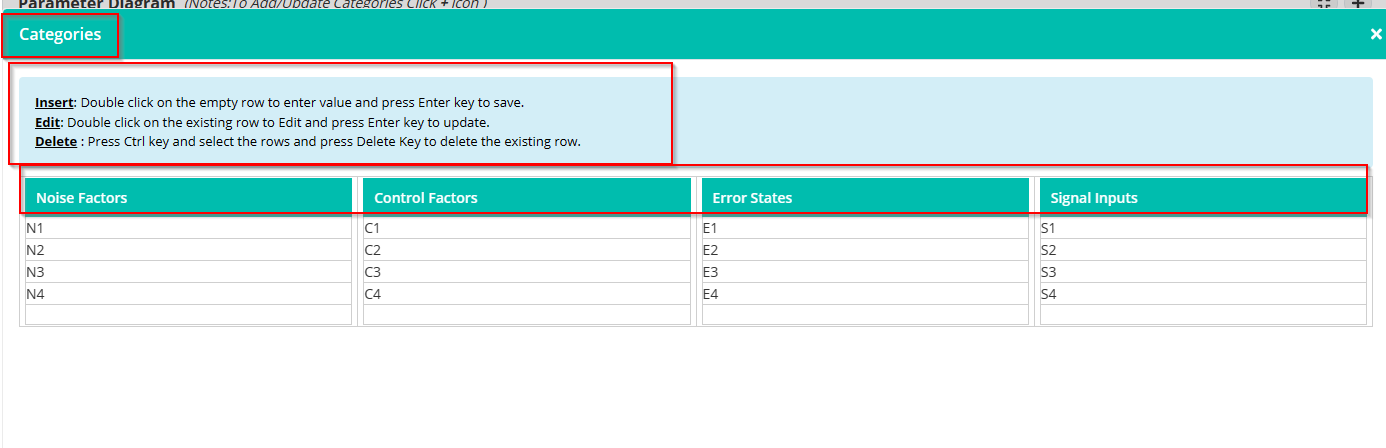
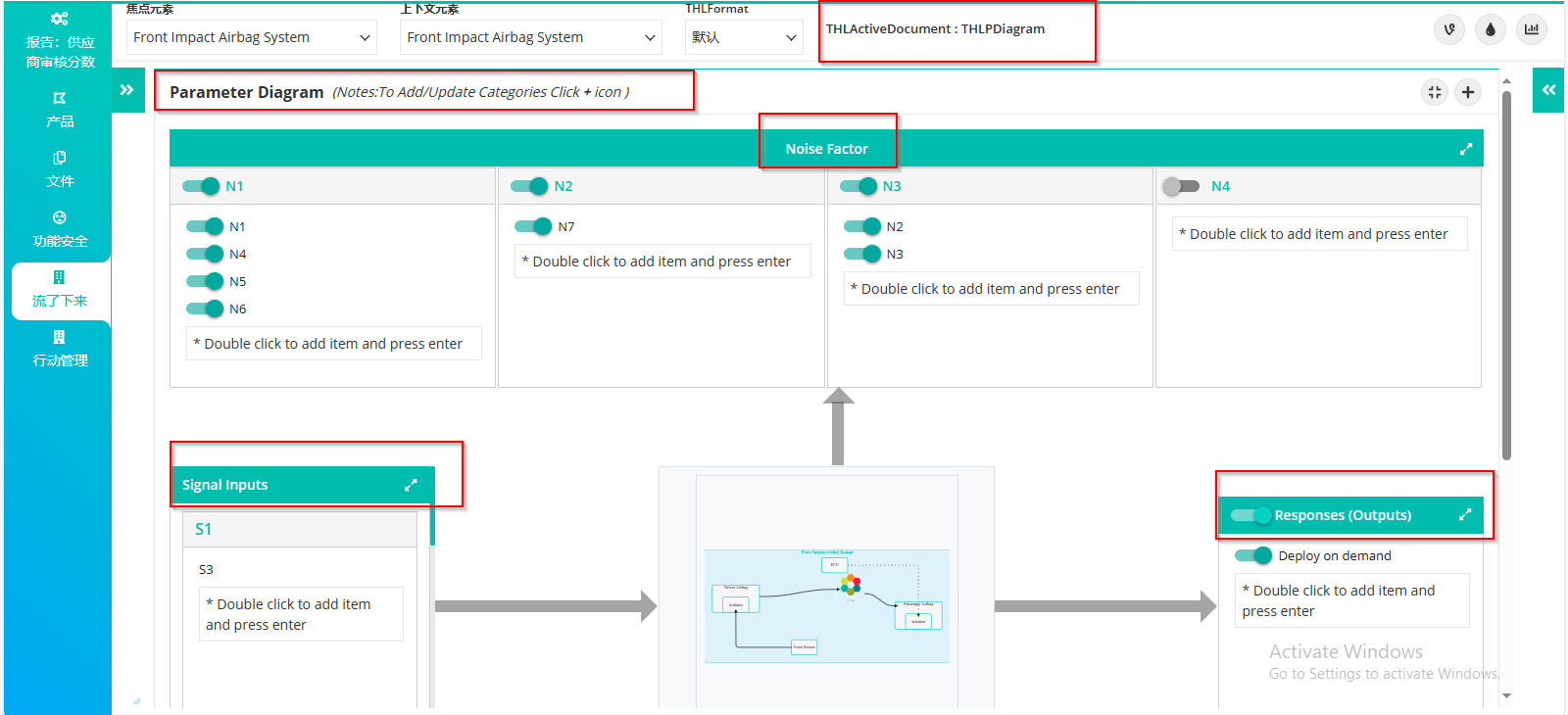
**Combined Interface Matrix**

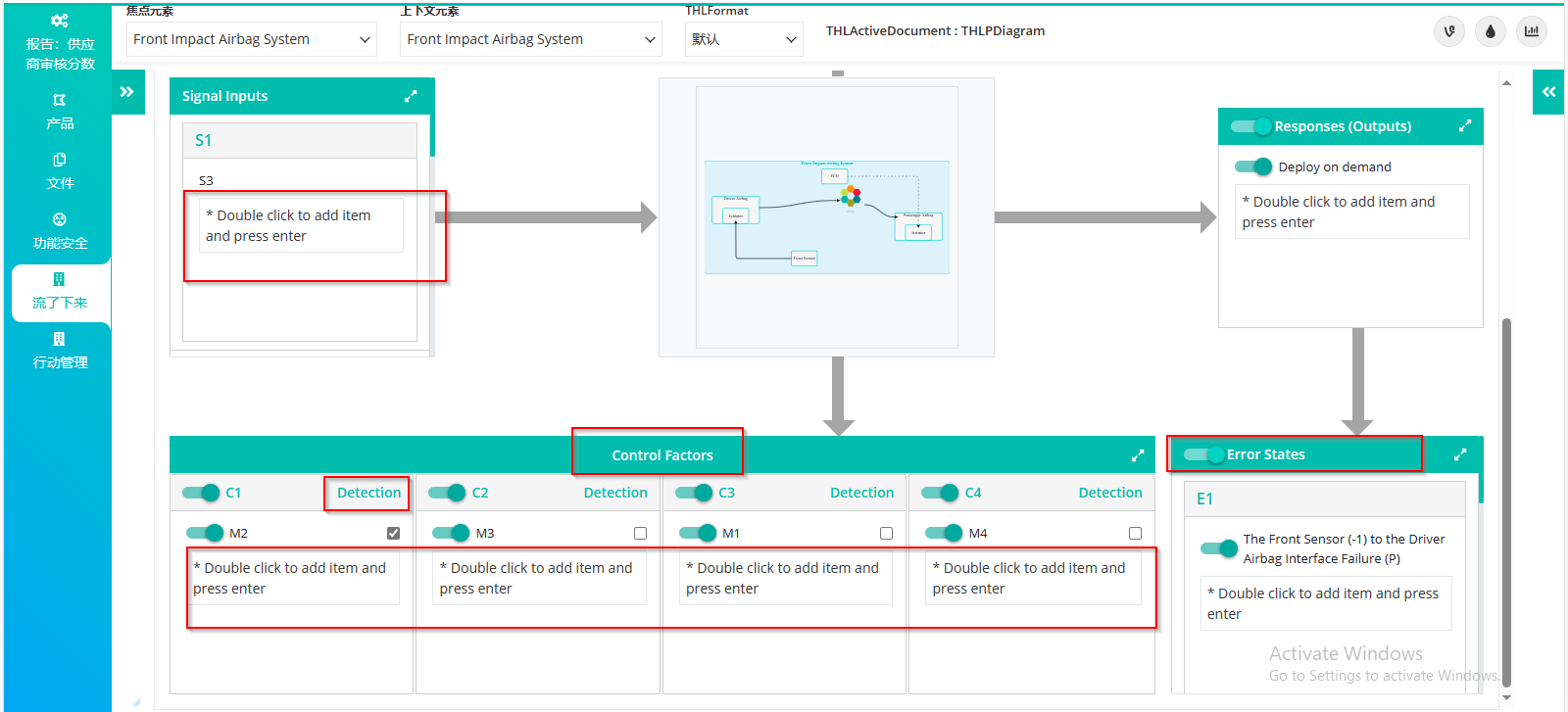
****

**Interface Matrix**

****

**P Diagram**

****

****