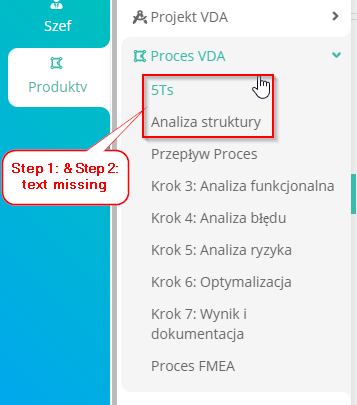
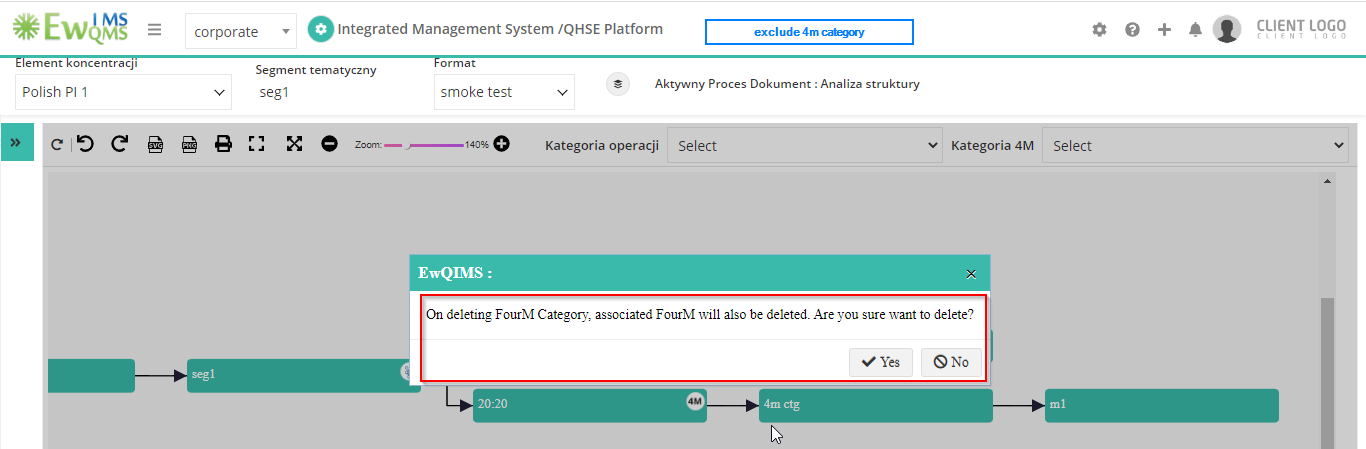
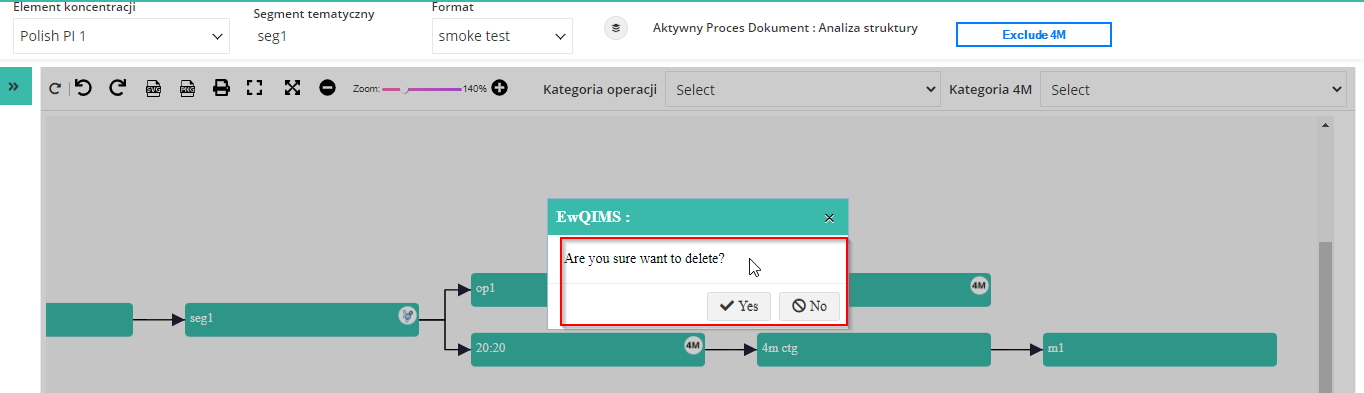
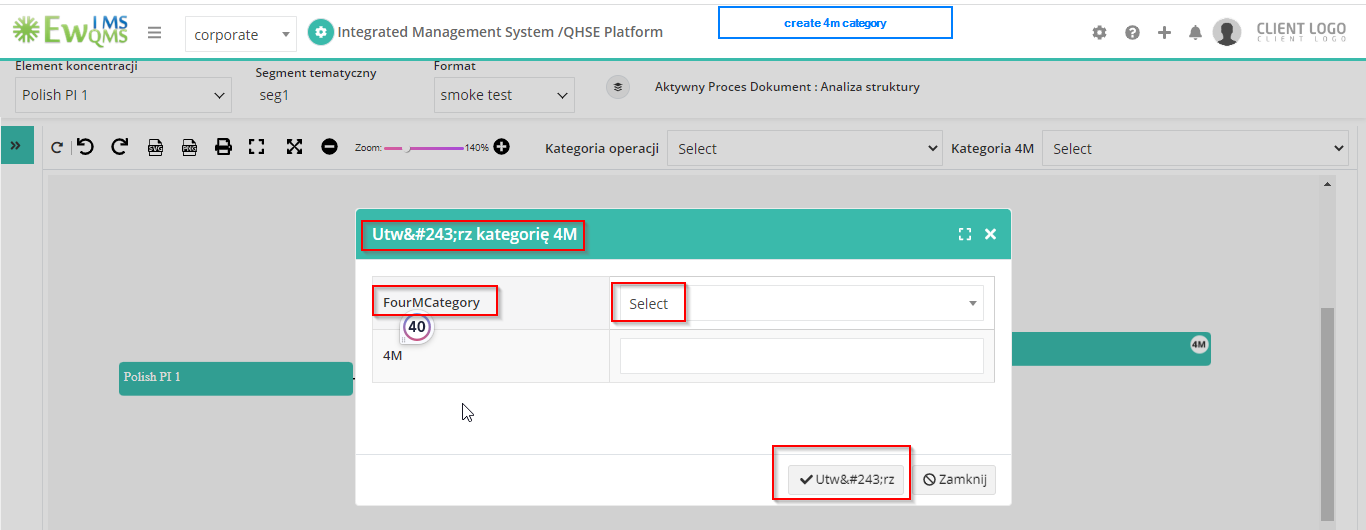
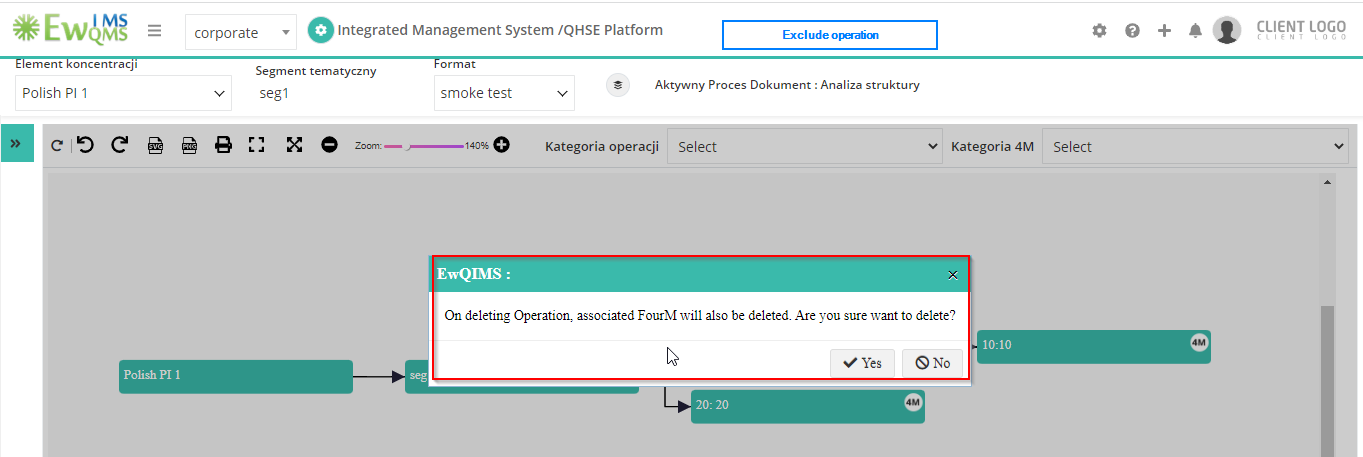
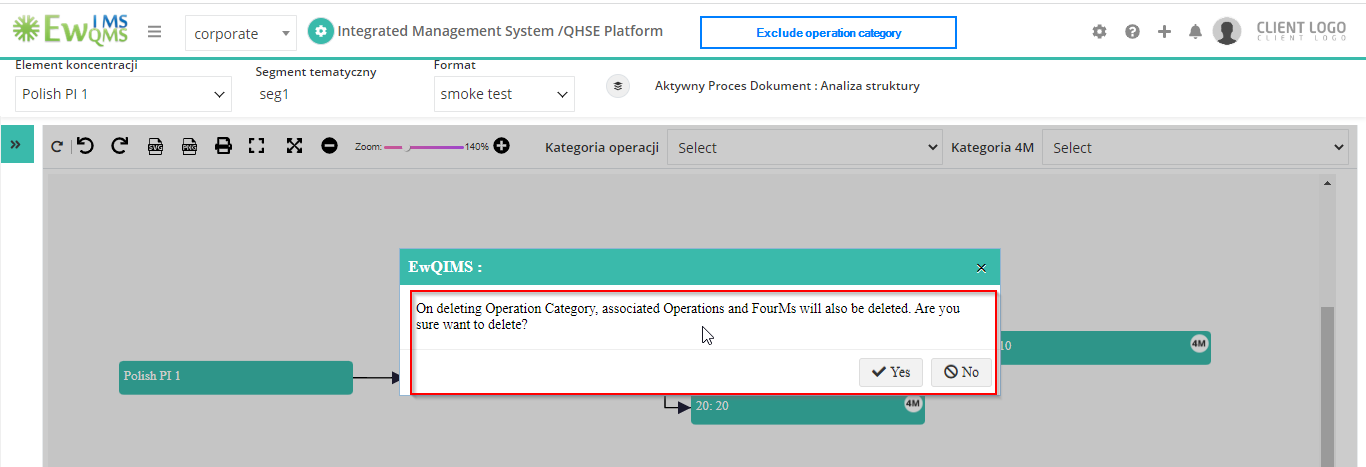
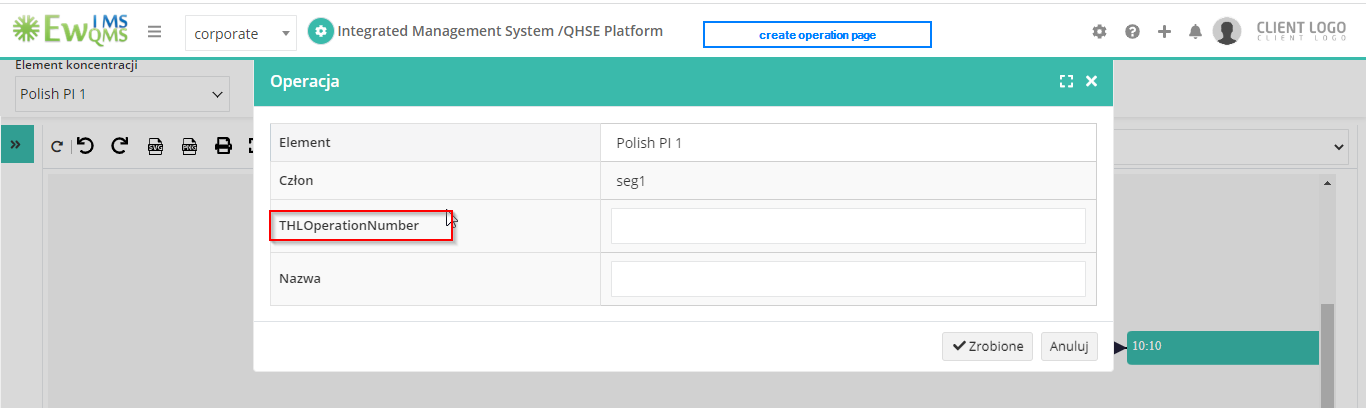
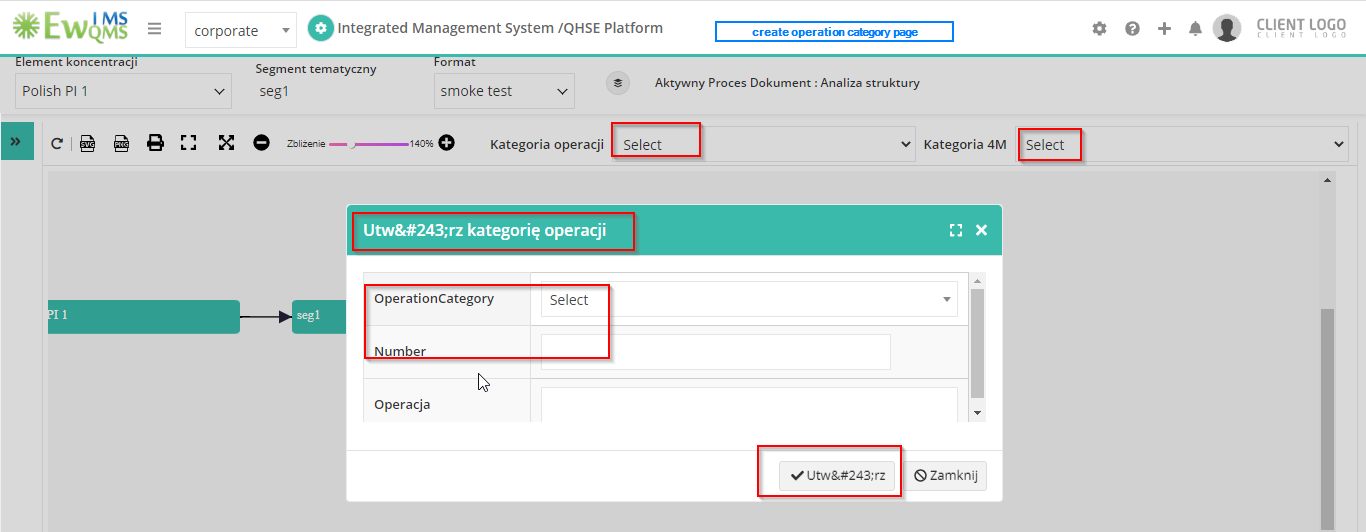
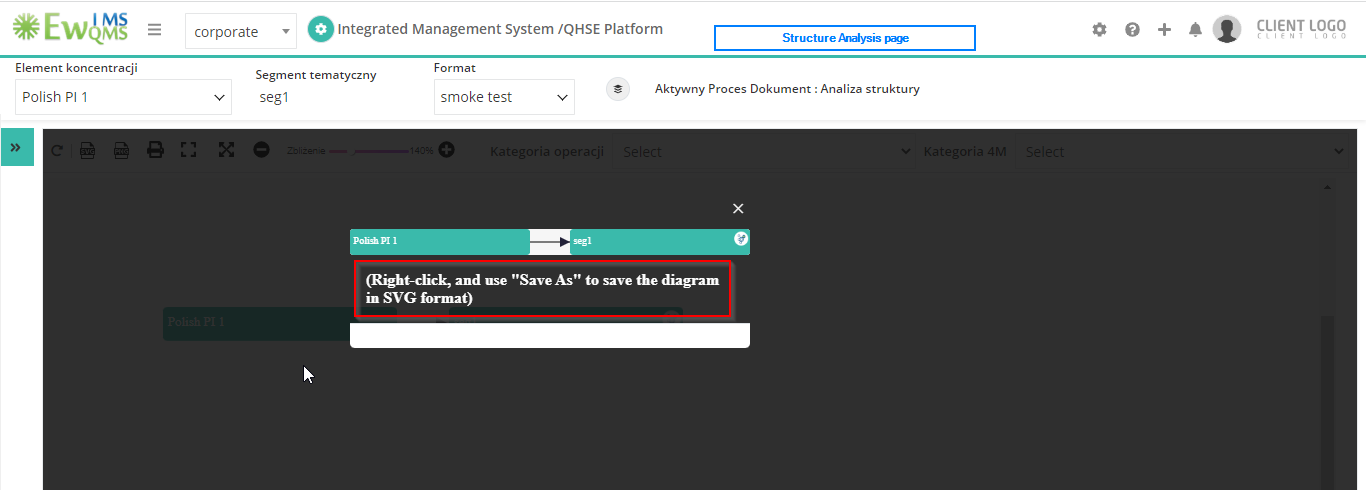
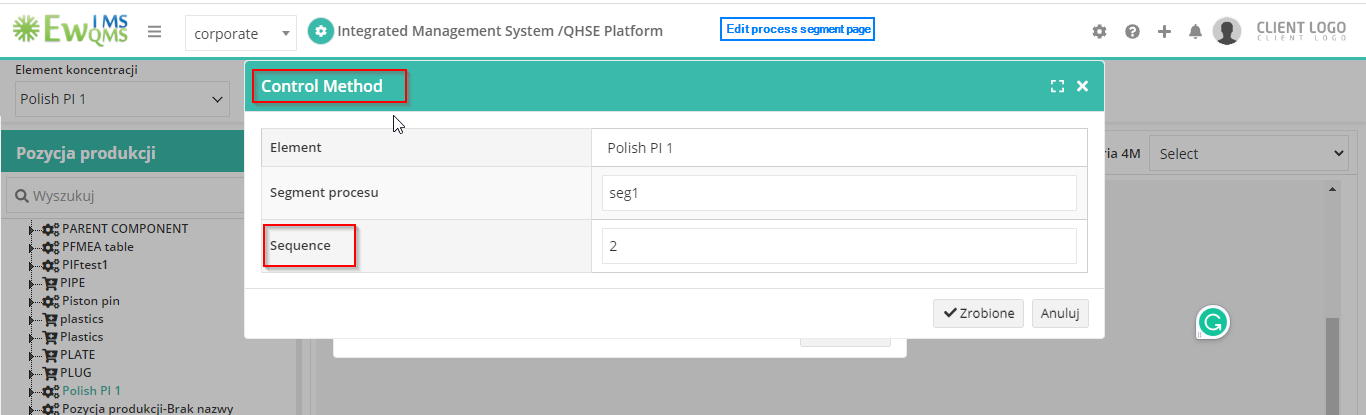
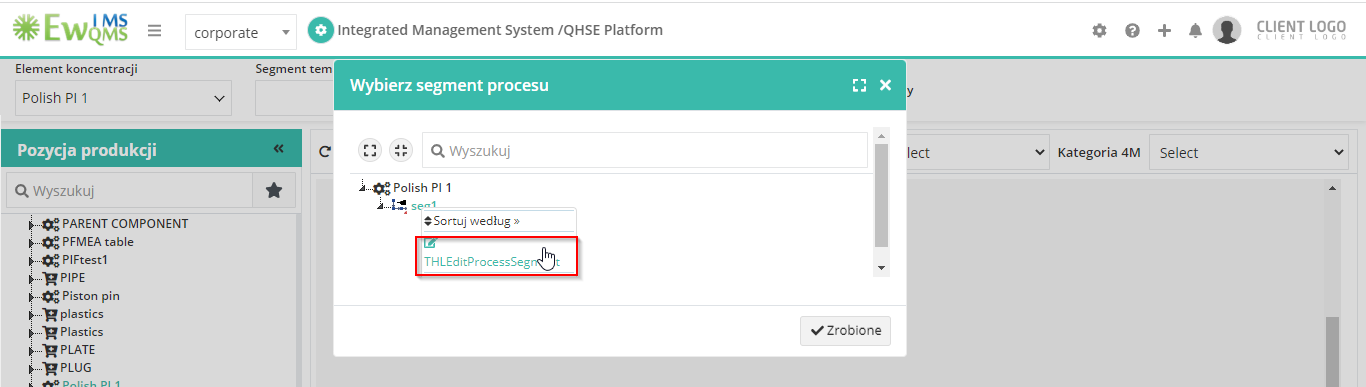
**Polish language verification**

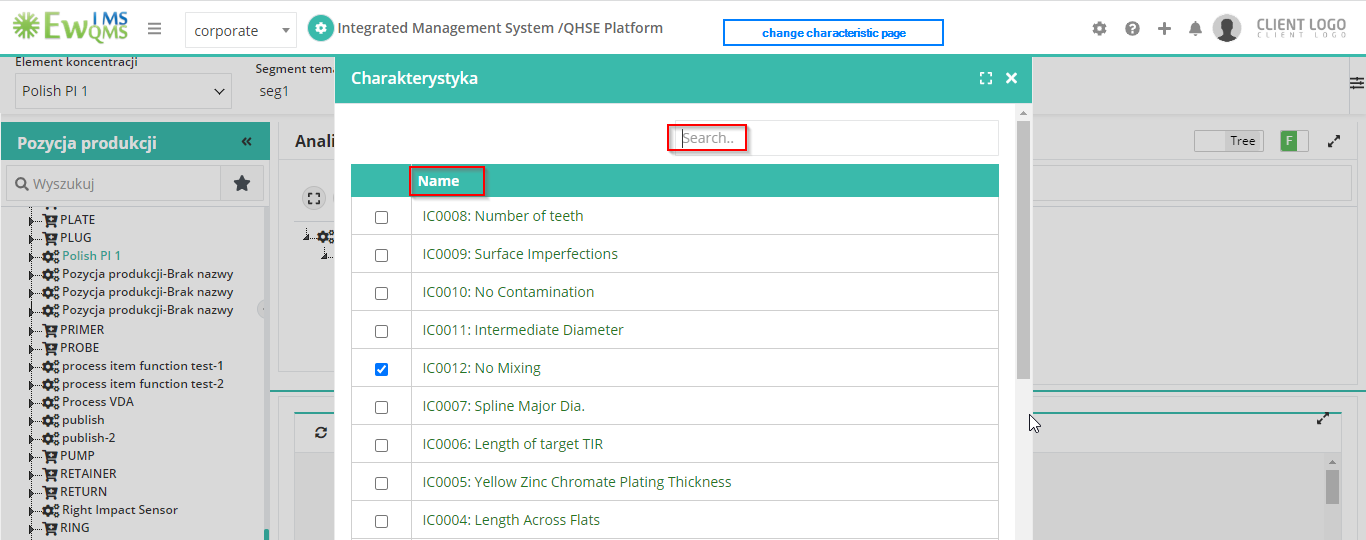
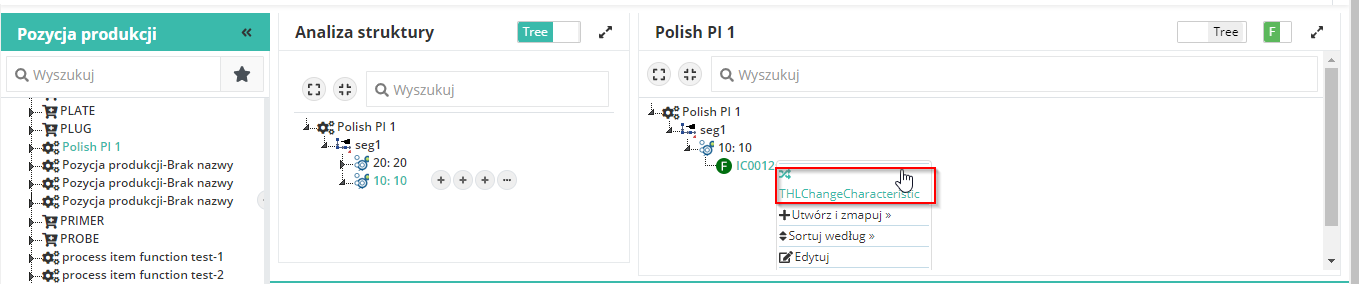
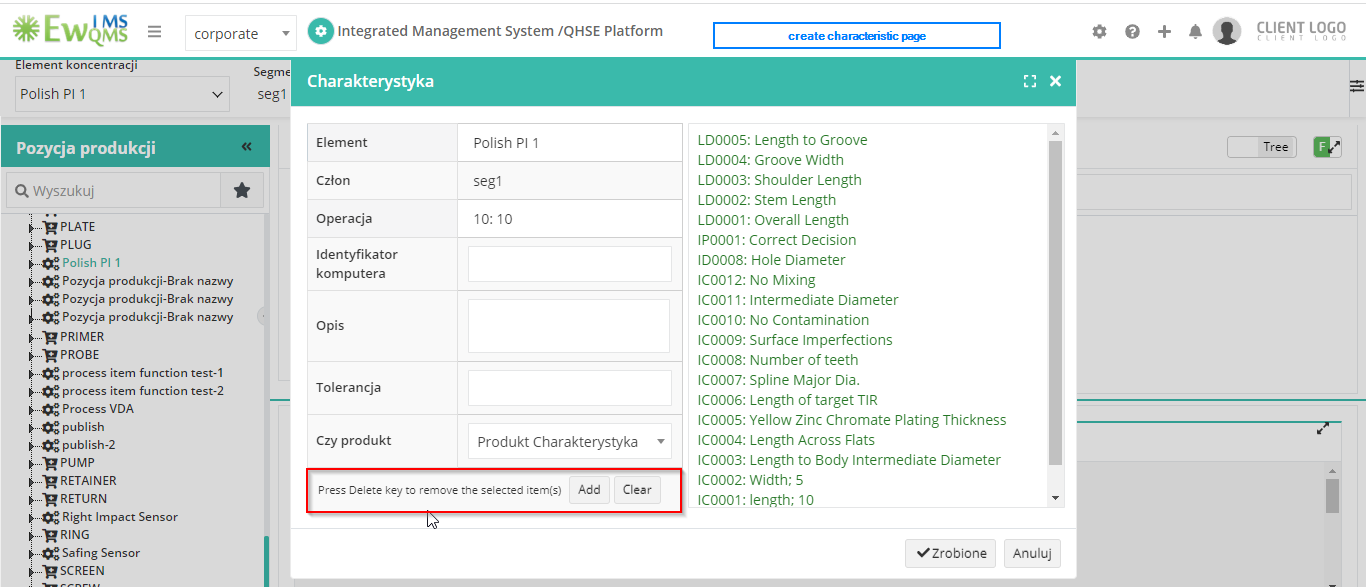
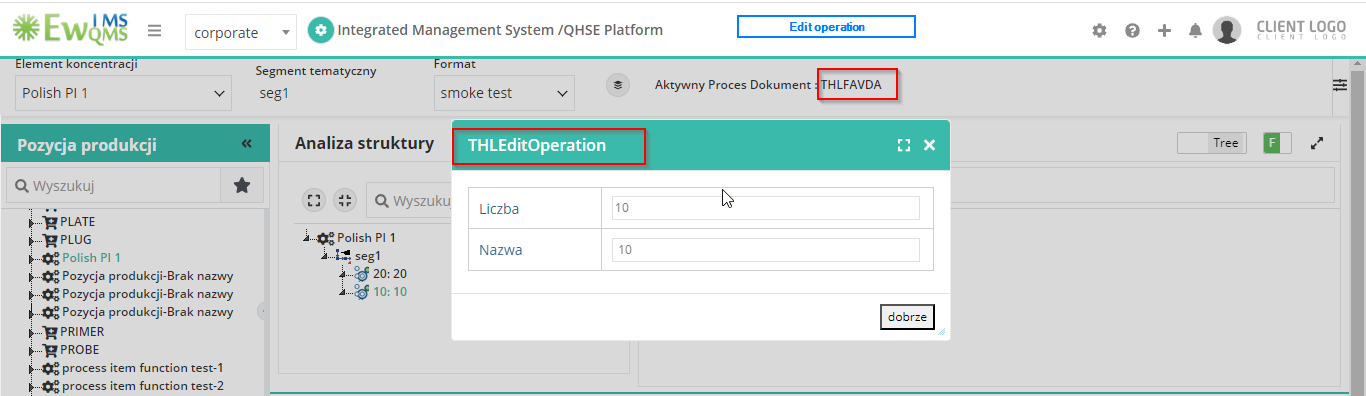
**Process VDA**

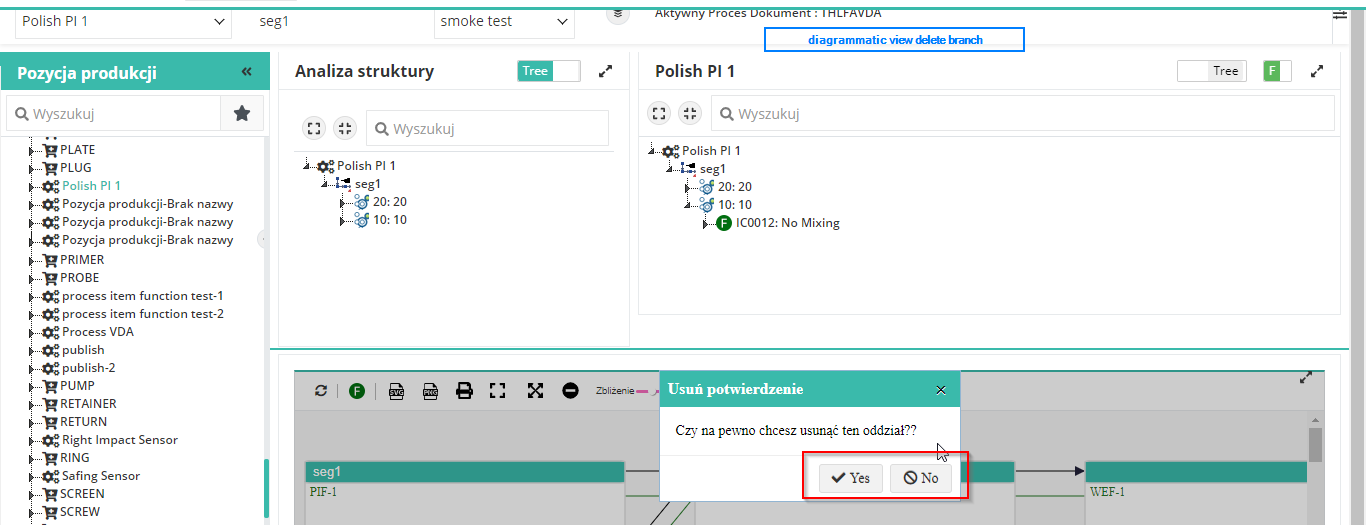
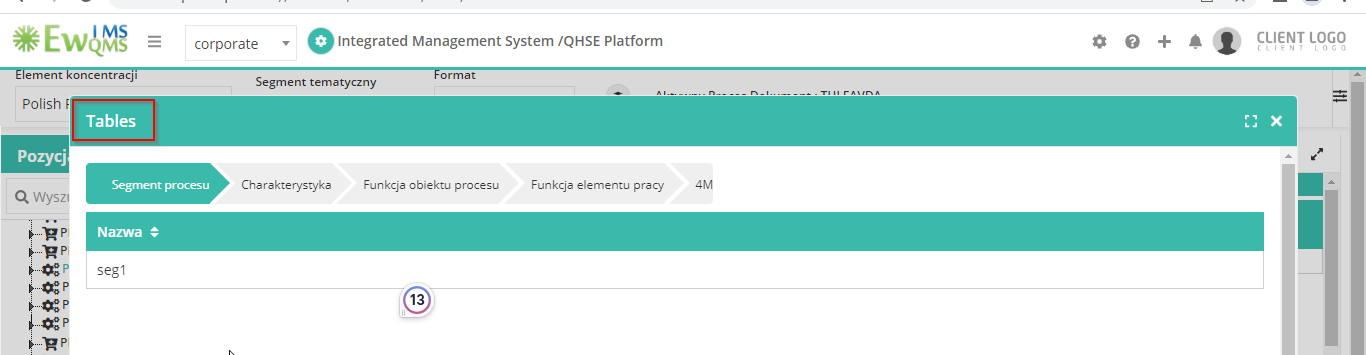
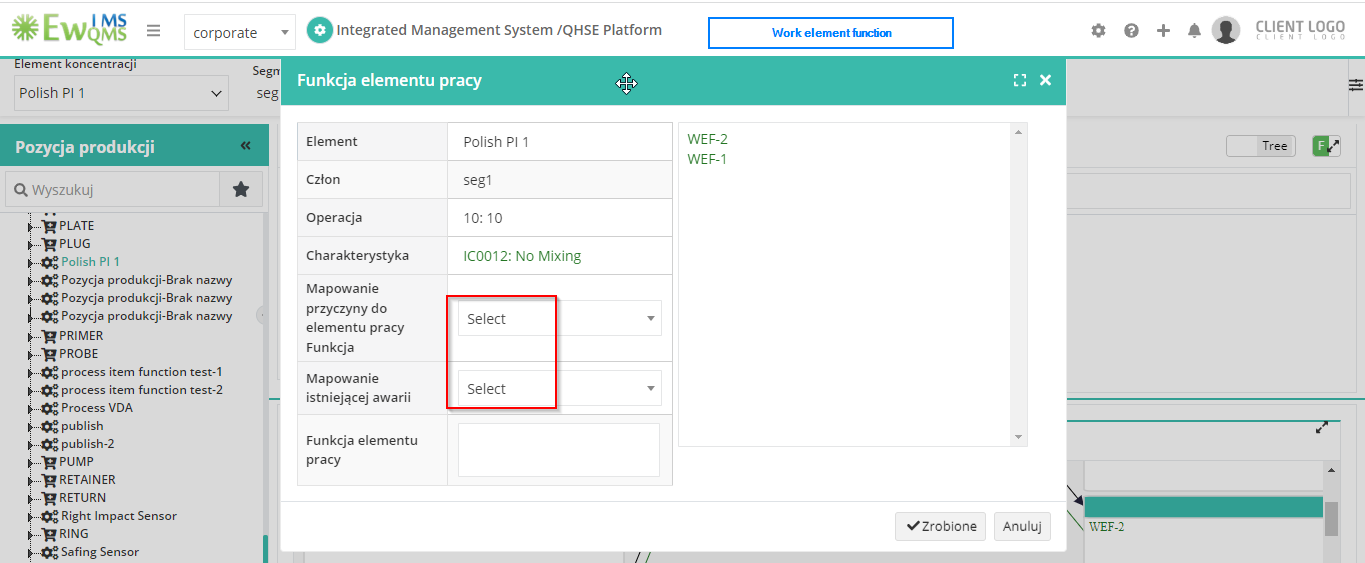
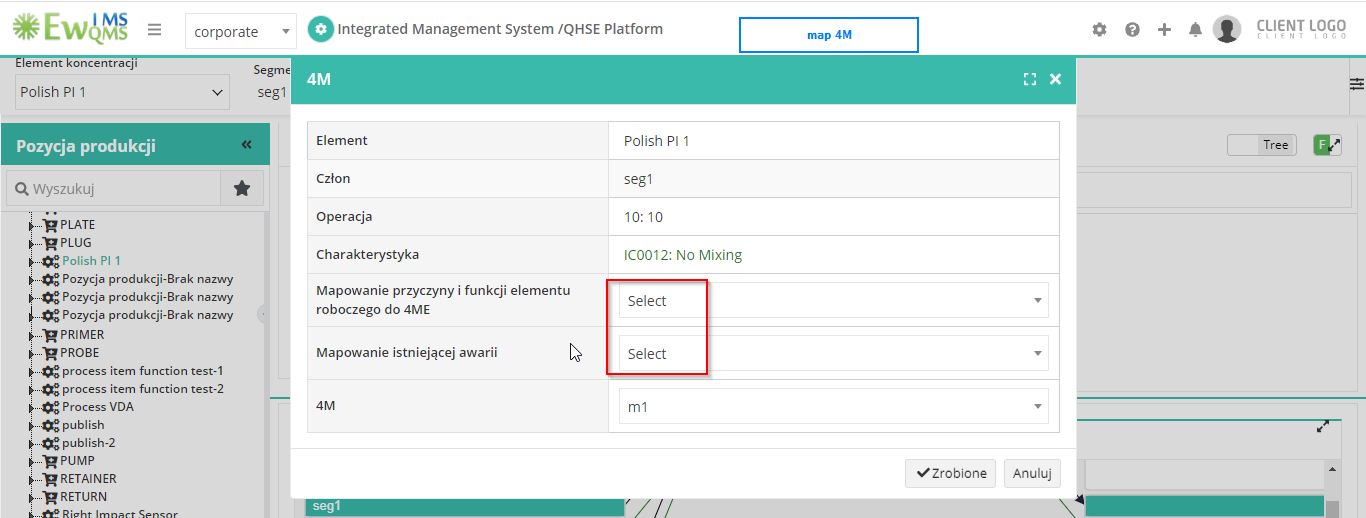


Edit process segment



Function Analysis





Failure analysis

