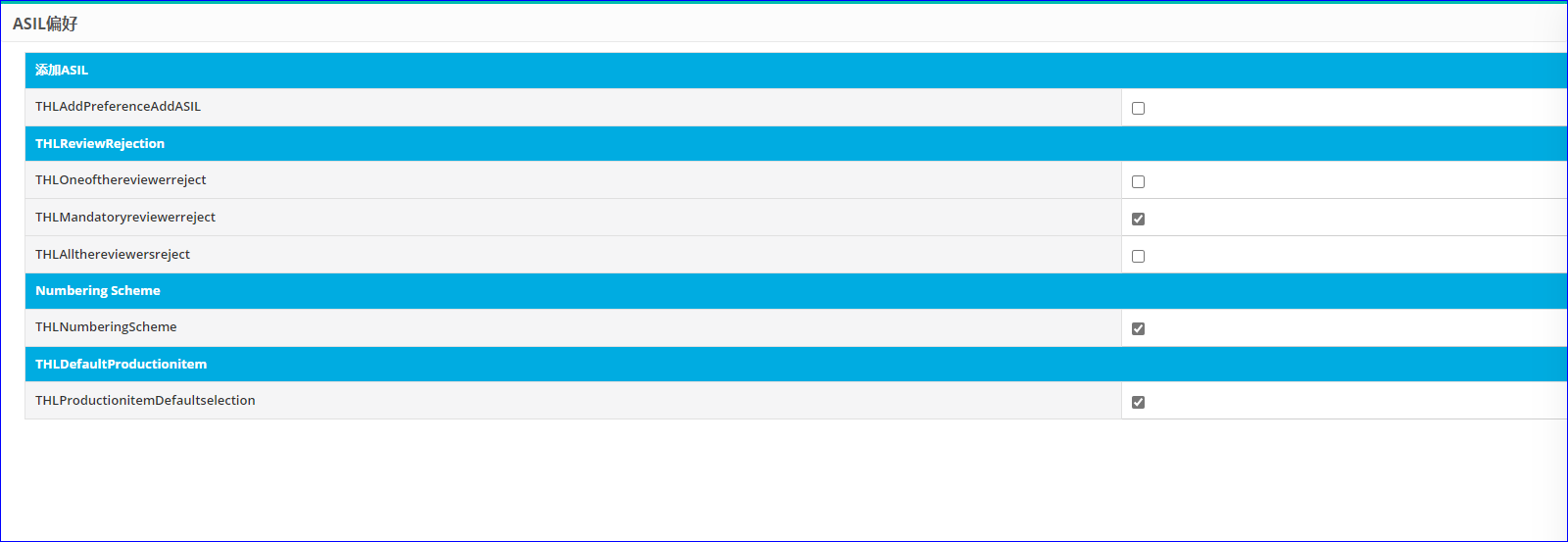
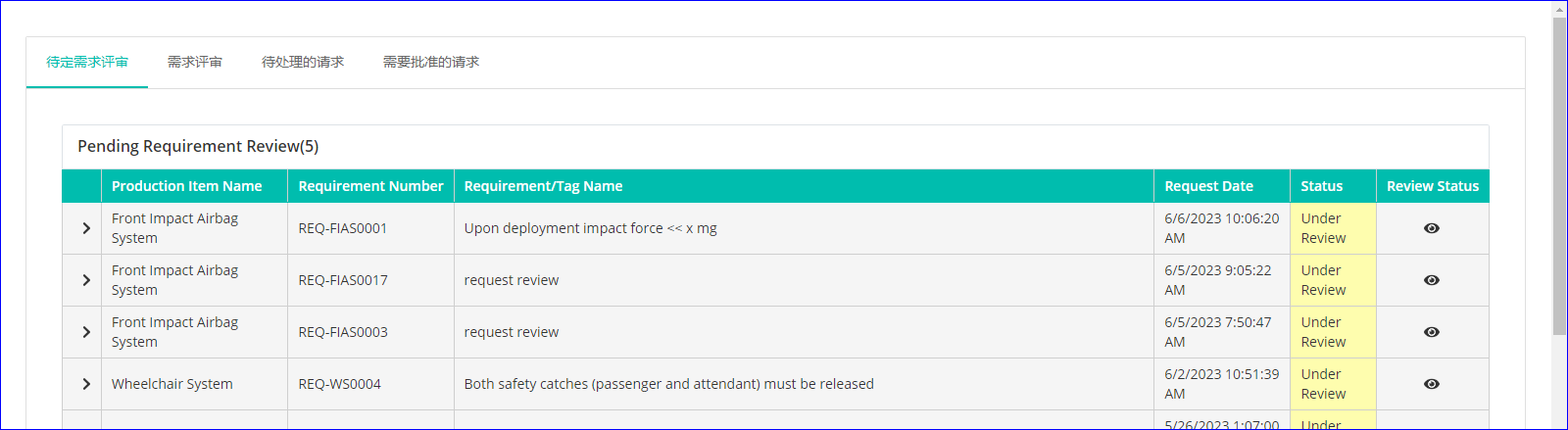
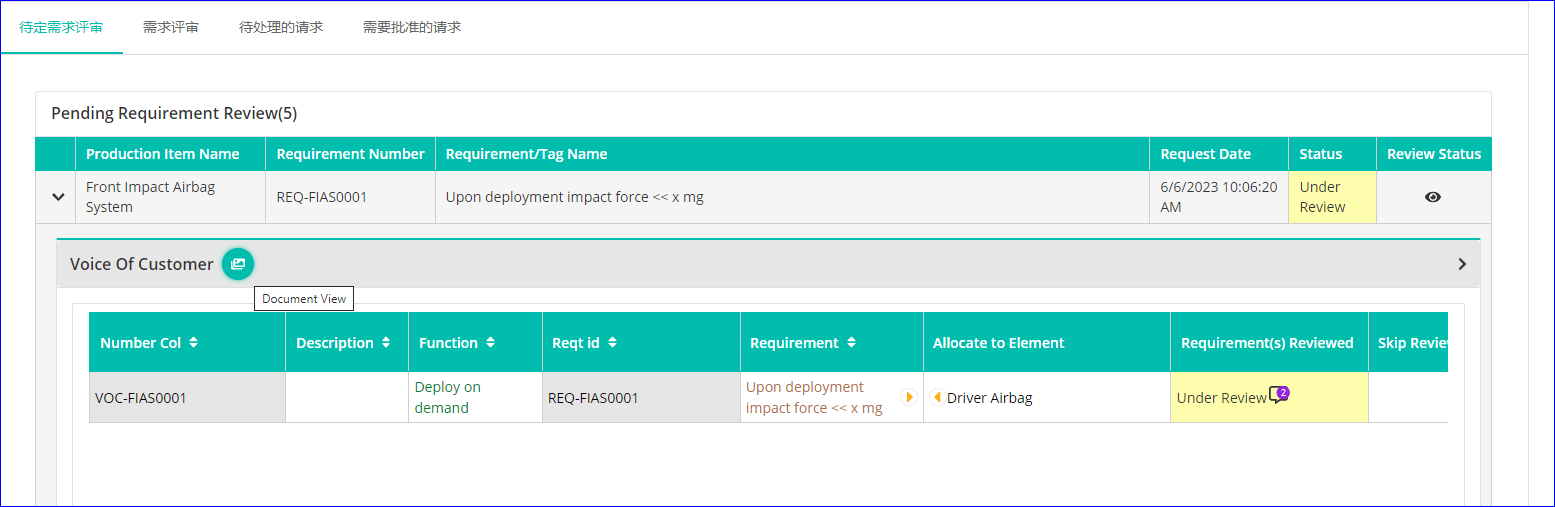
**Preference**



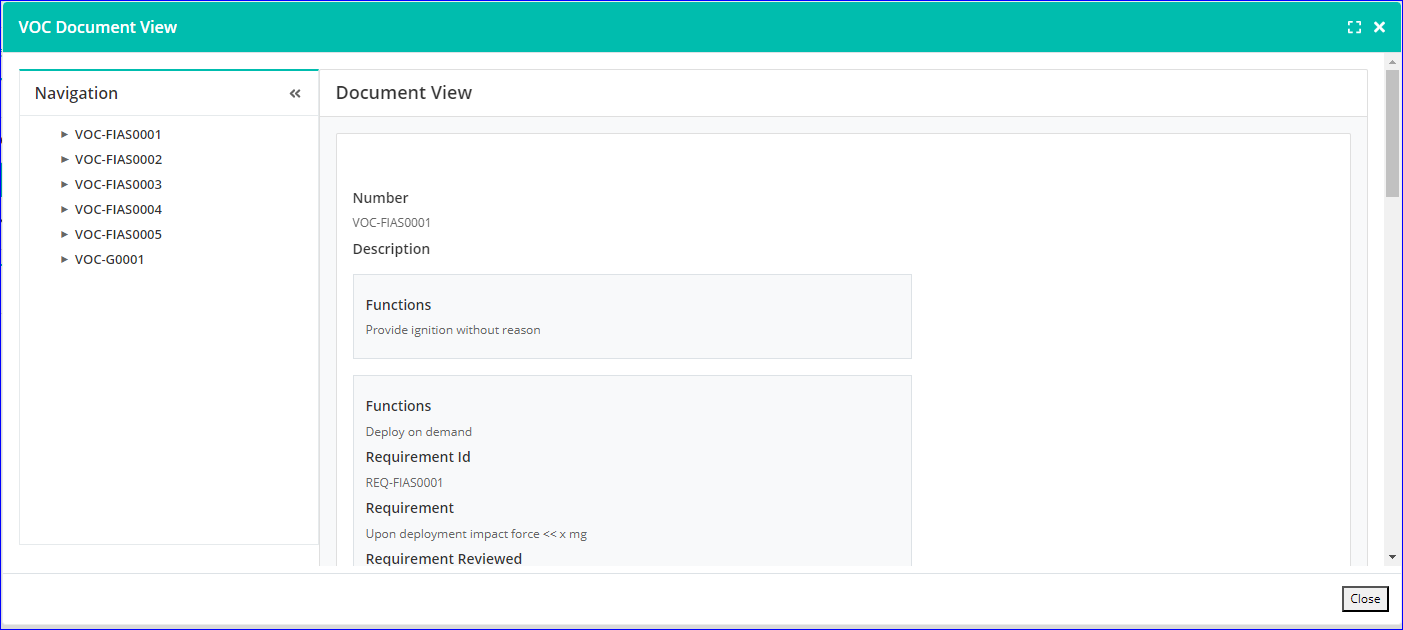
**Actions**

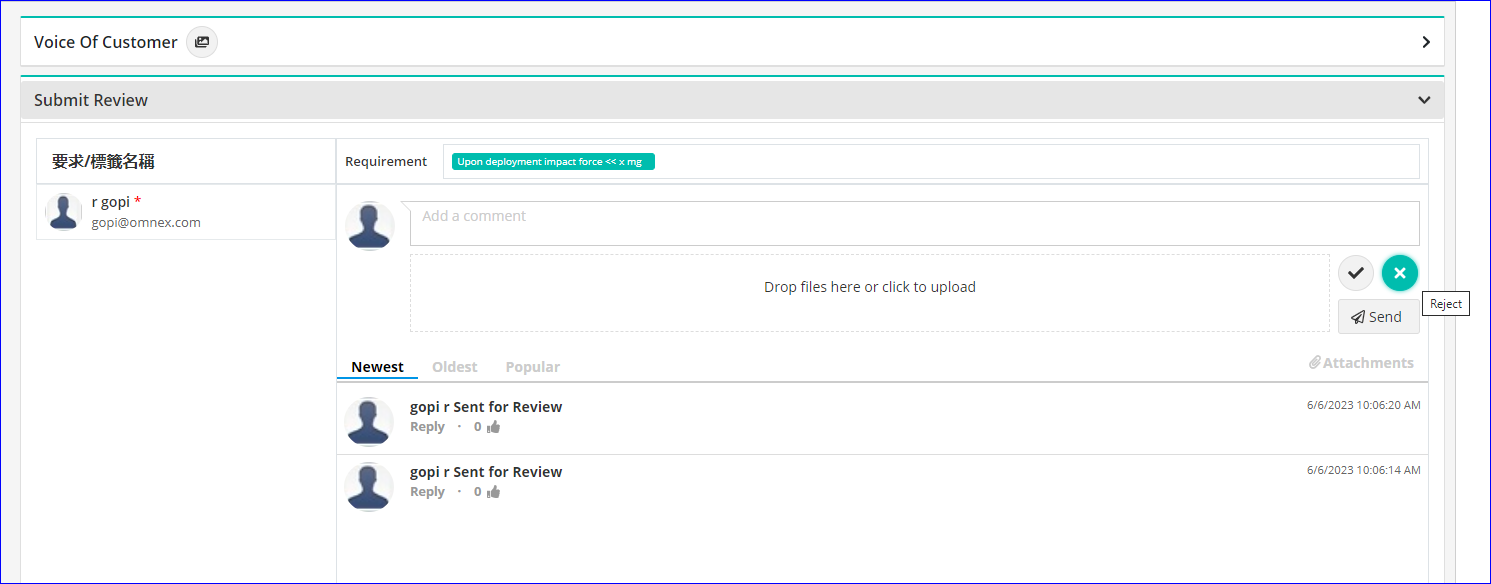
**Pending Requirement Review**



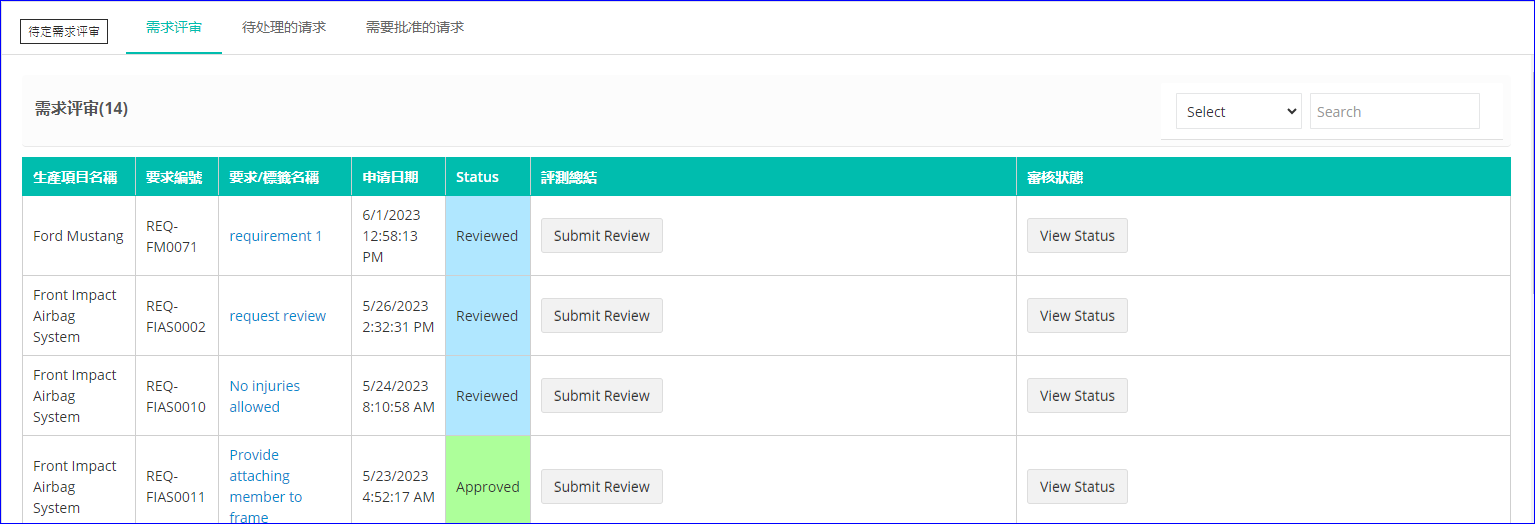


**Document view – Actions & VOC**

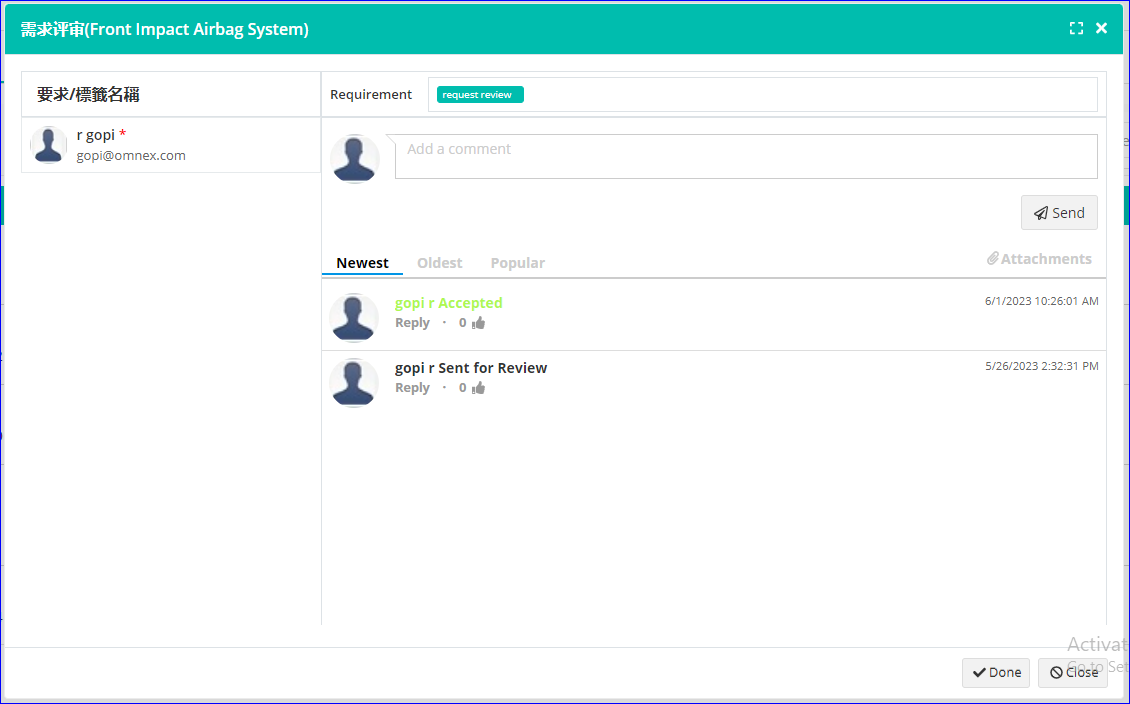




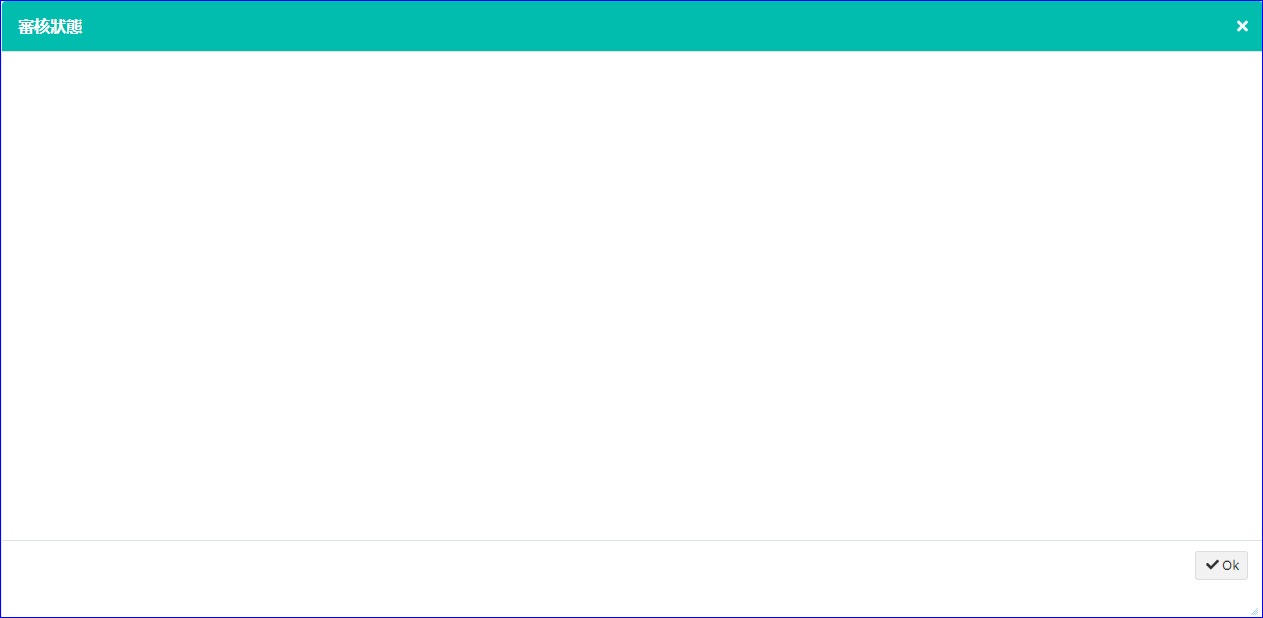
**Requirements Reviewed**



**Submit Review**



**Review status**

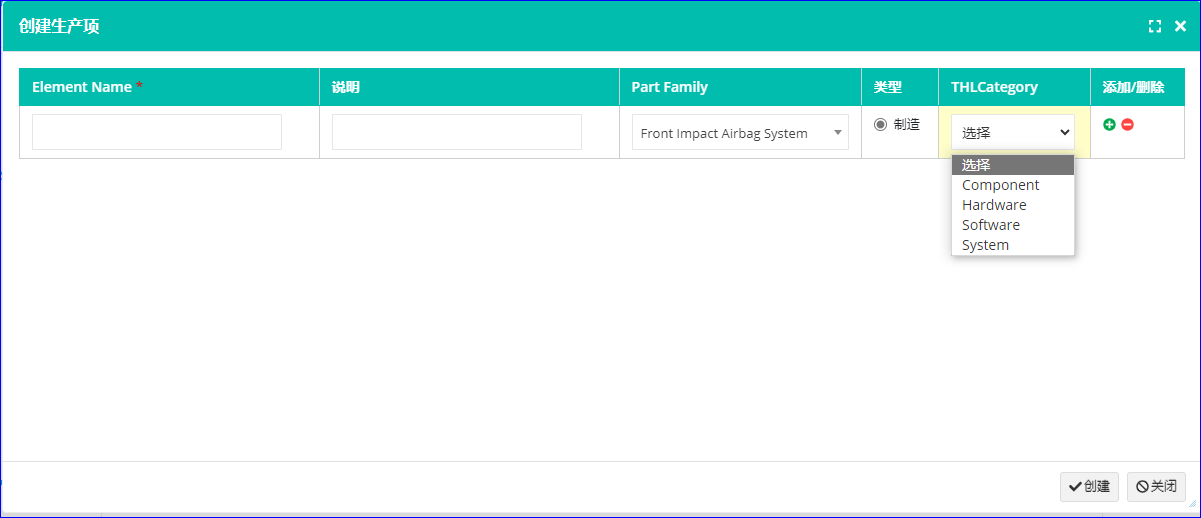


**VOC**

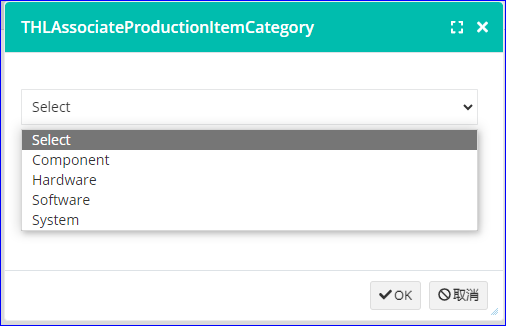
**PI Tree**



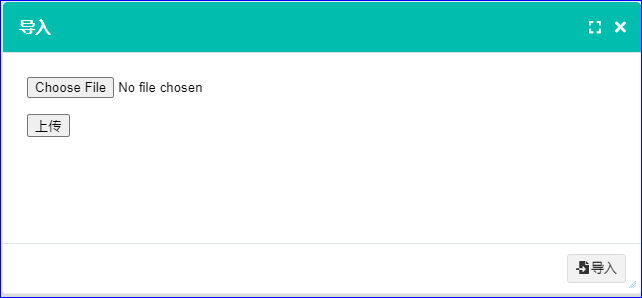
**Create PI**



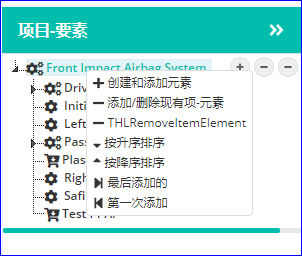
**Associate category**



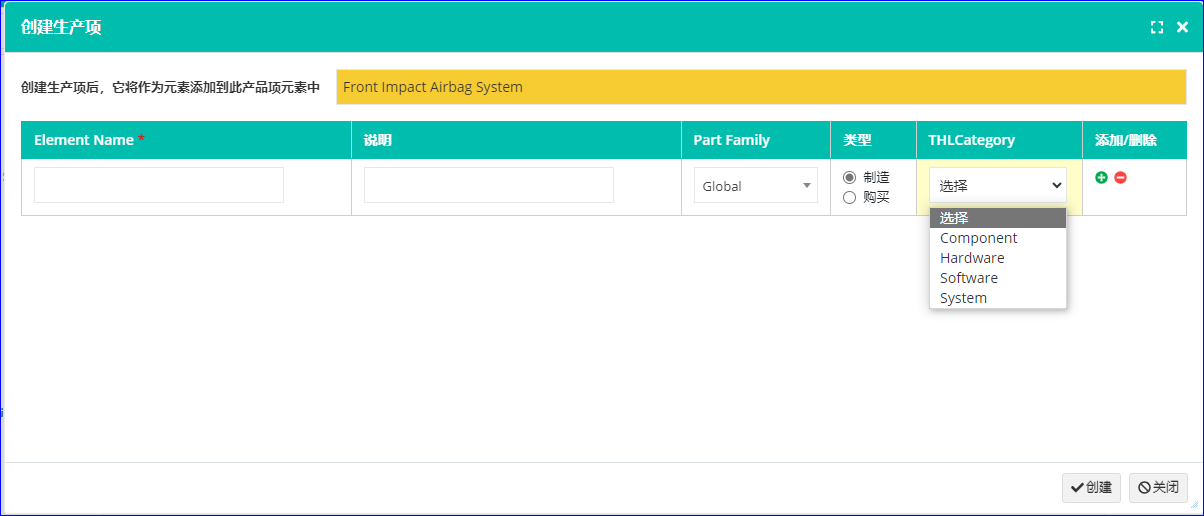
**Import**



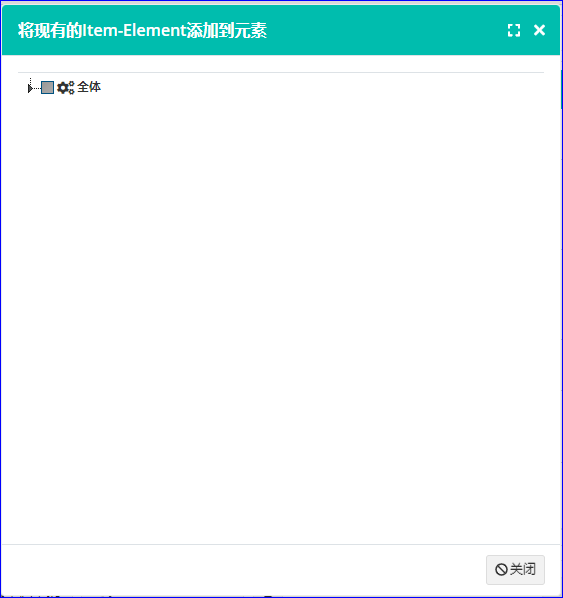
**BOM Tree**



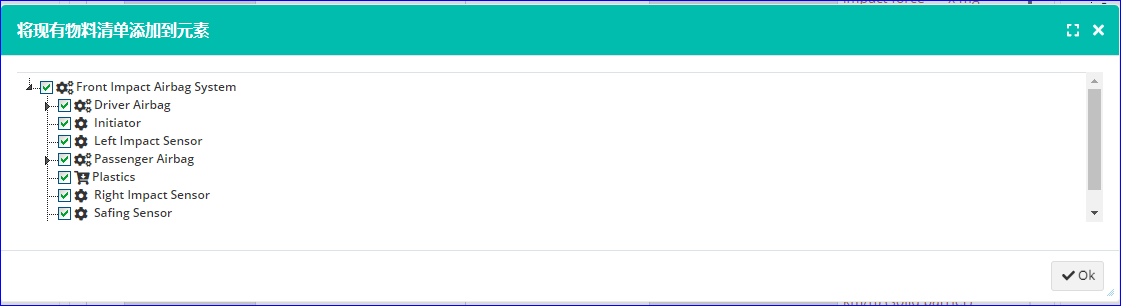
**Create BOM**

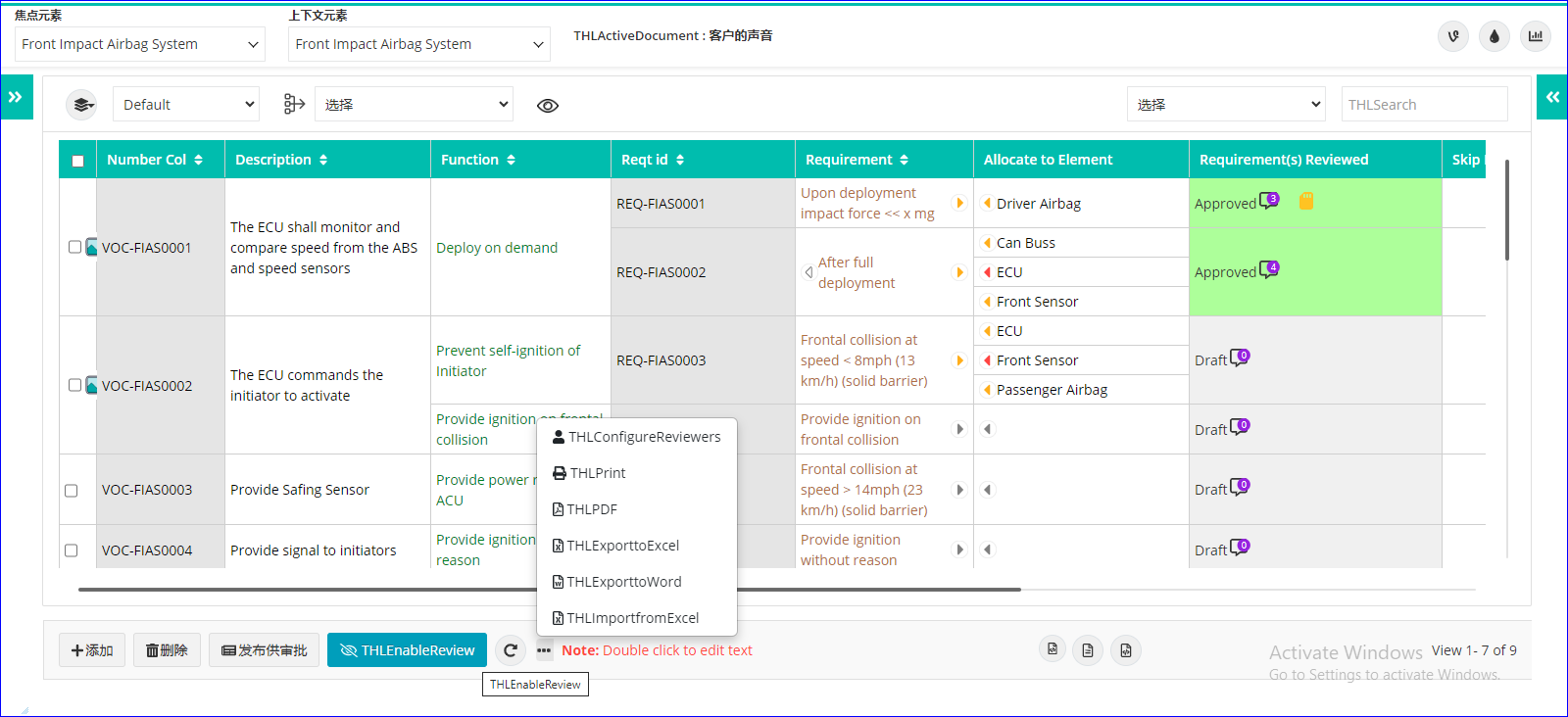


**Add existing PI**

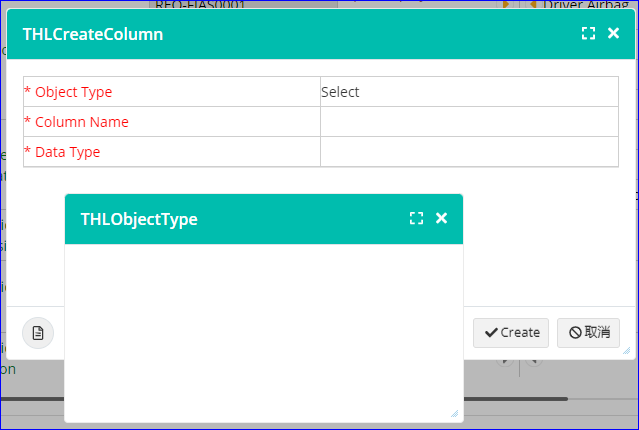


**Remove Existing PI**

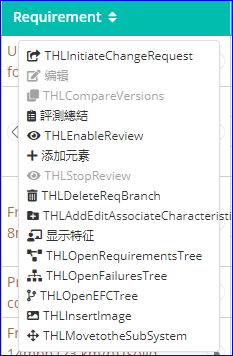
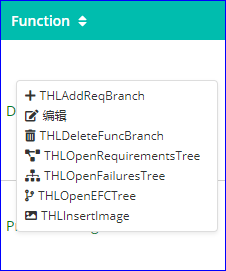
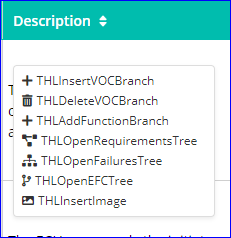




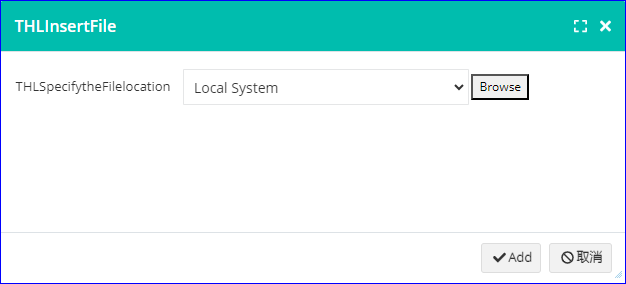
**Show column Create Column**



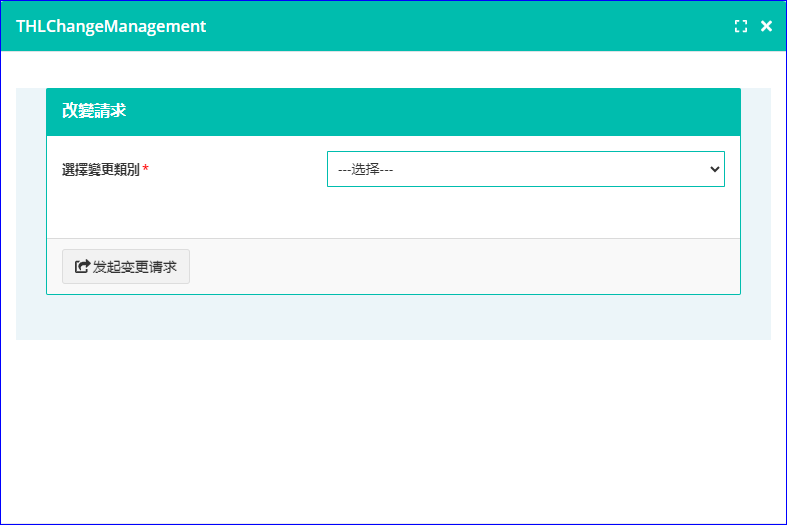
**Context menus**



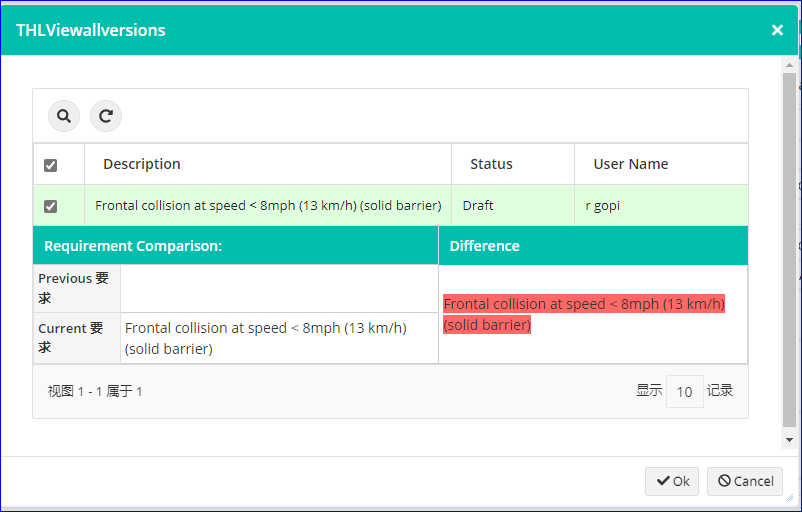
**Insert Image**



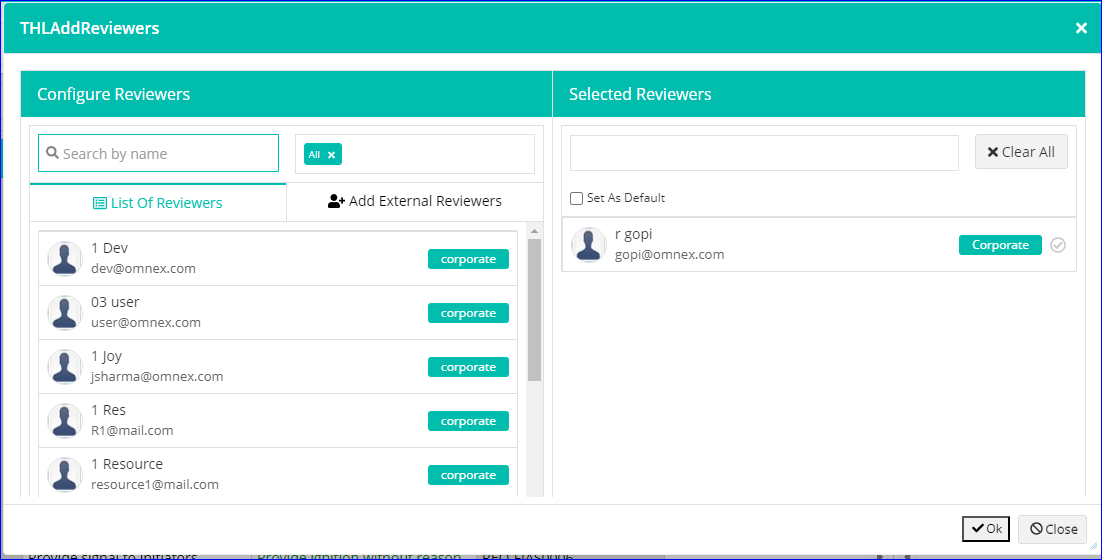
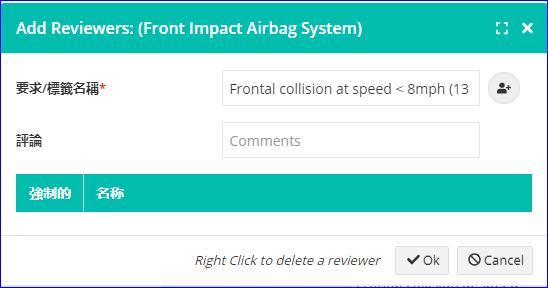
**Initiate change request**



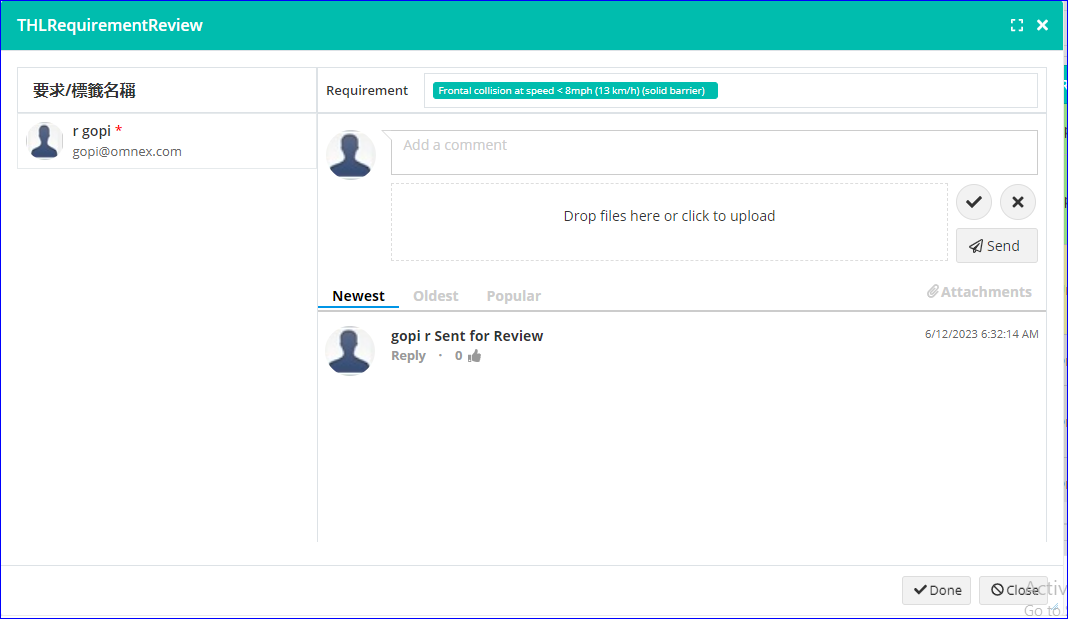
**Compare Versions**



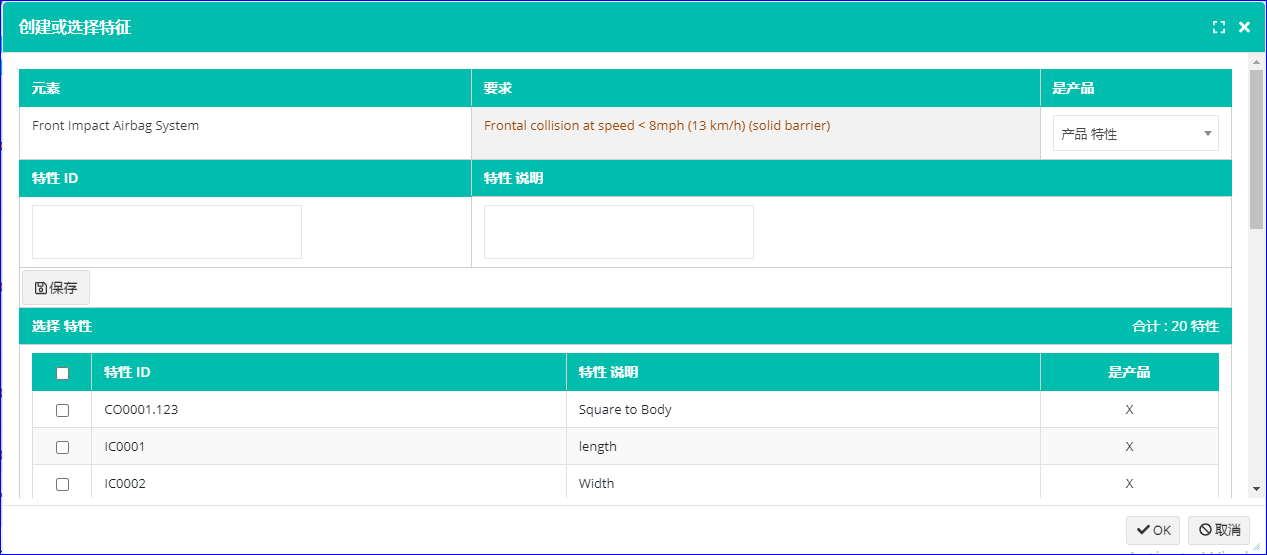
**Enable Review**



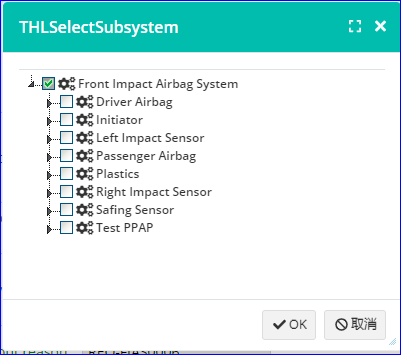
**Submit review**



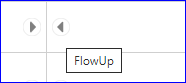
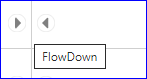
**Add Characteristic**

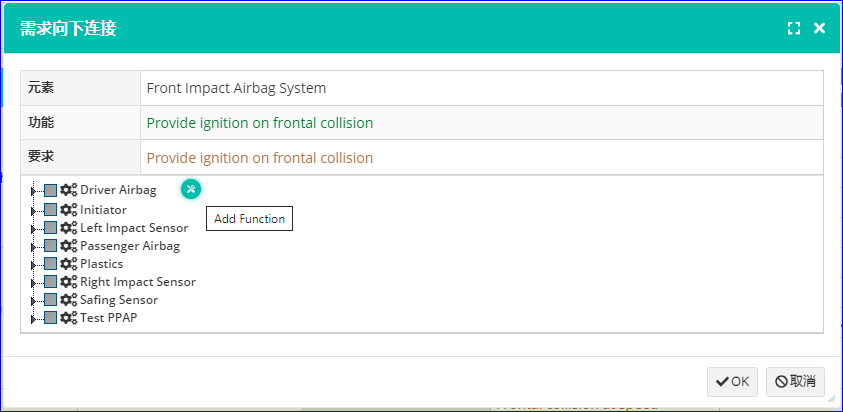


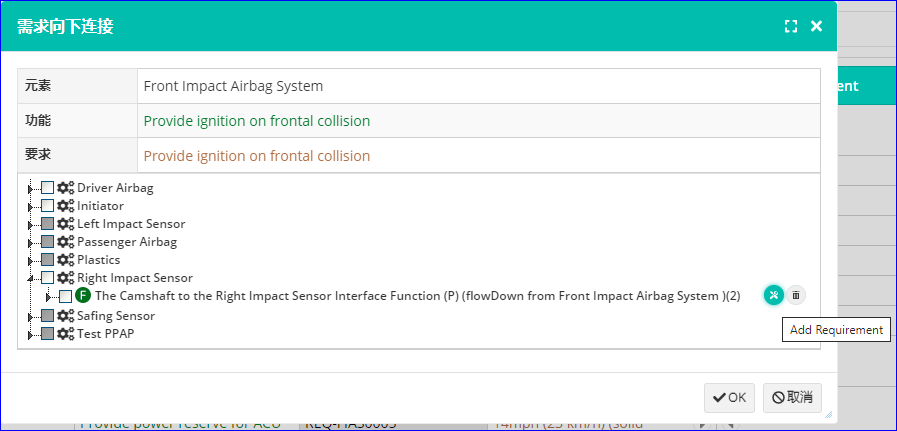
**Move to subsystem**



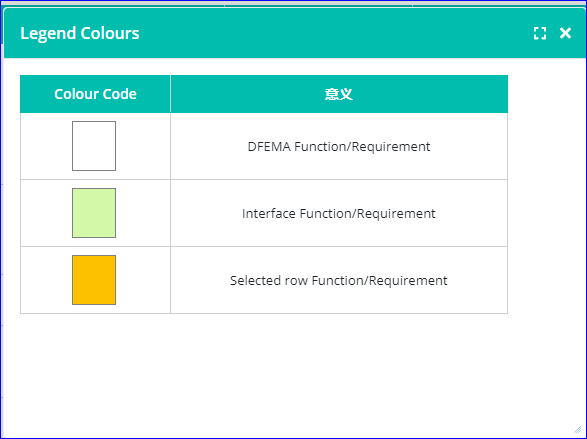
**Flowdown**



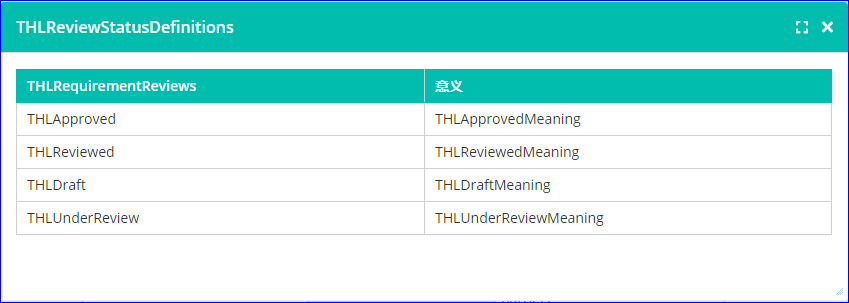




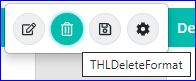
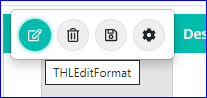
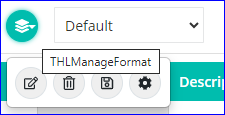
**Color Legend**

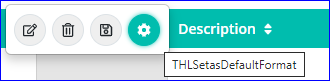
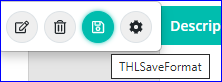


**Review Status**



**Manage Format**

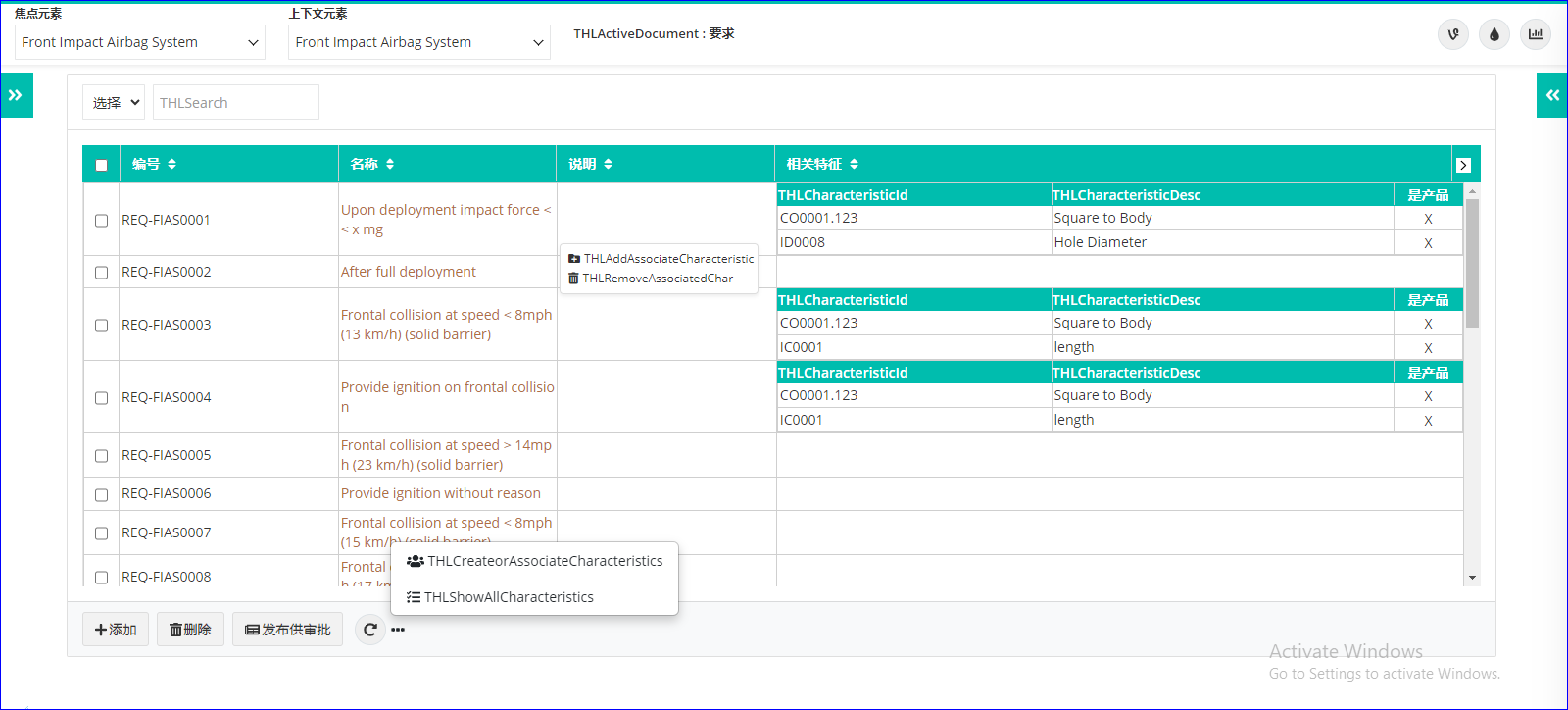




**Import**

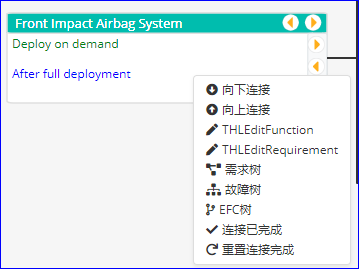


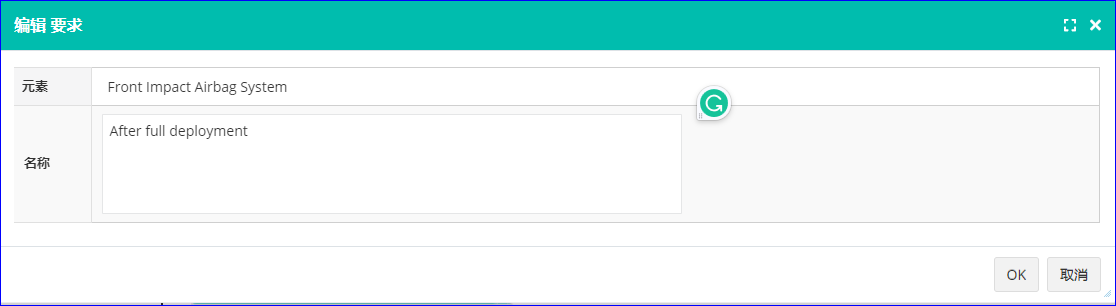
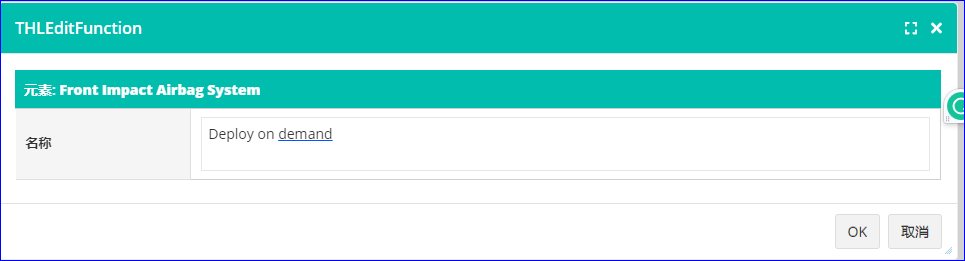
**Requirements**

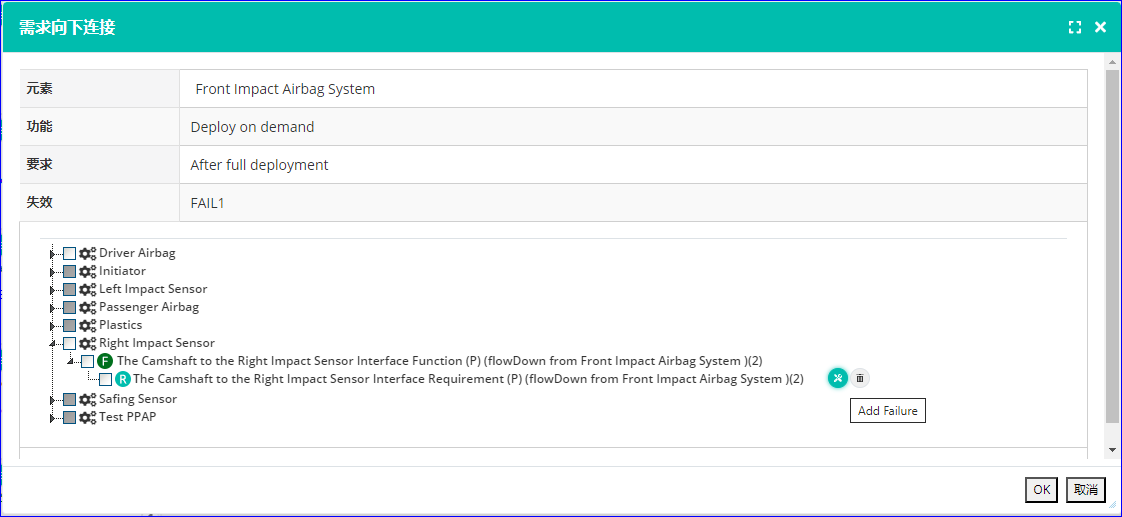




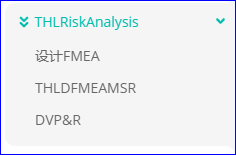
**Requirements tree**



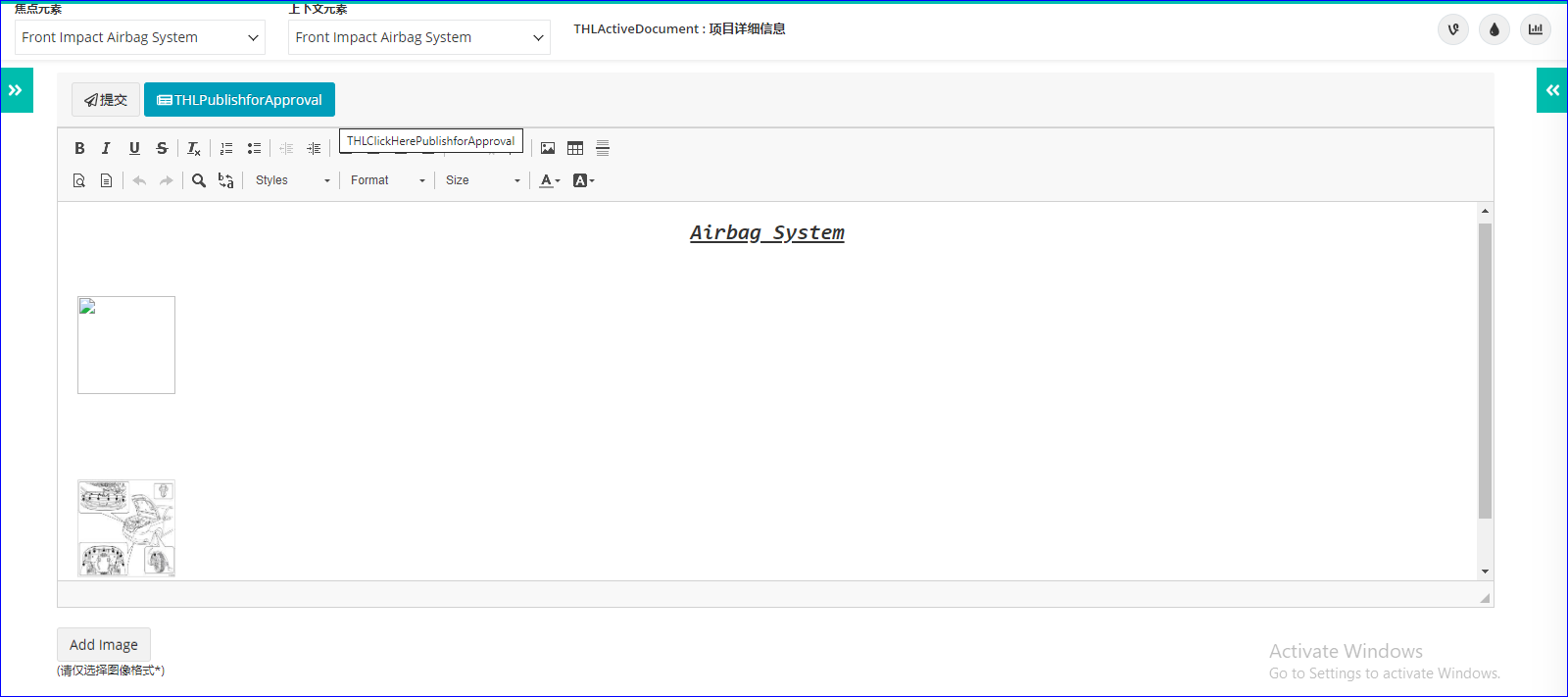


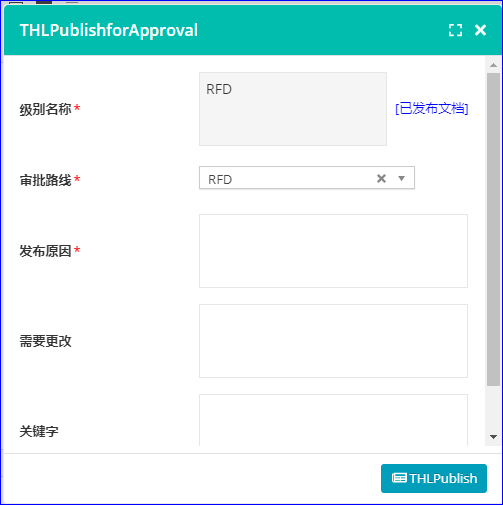


**Risk Analysis**



**Item Details & Impact Analysis**

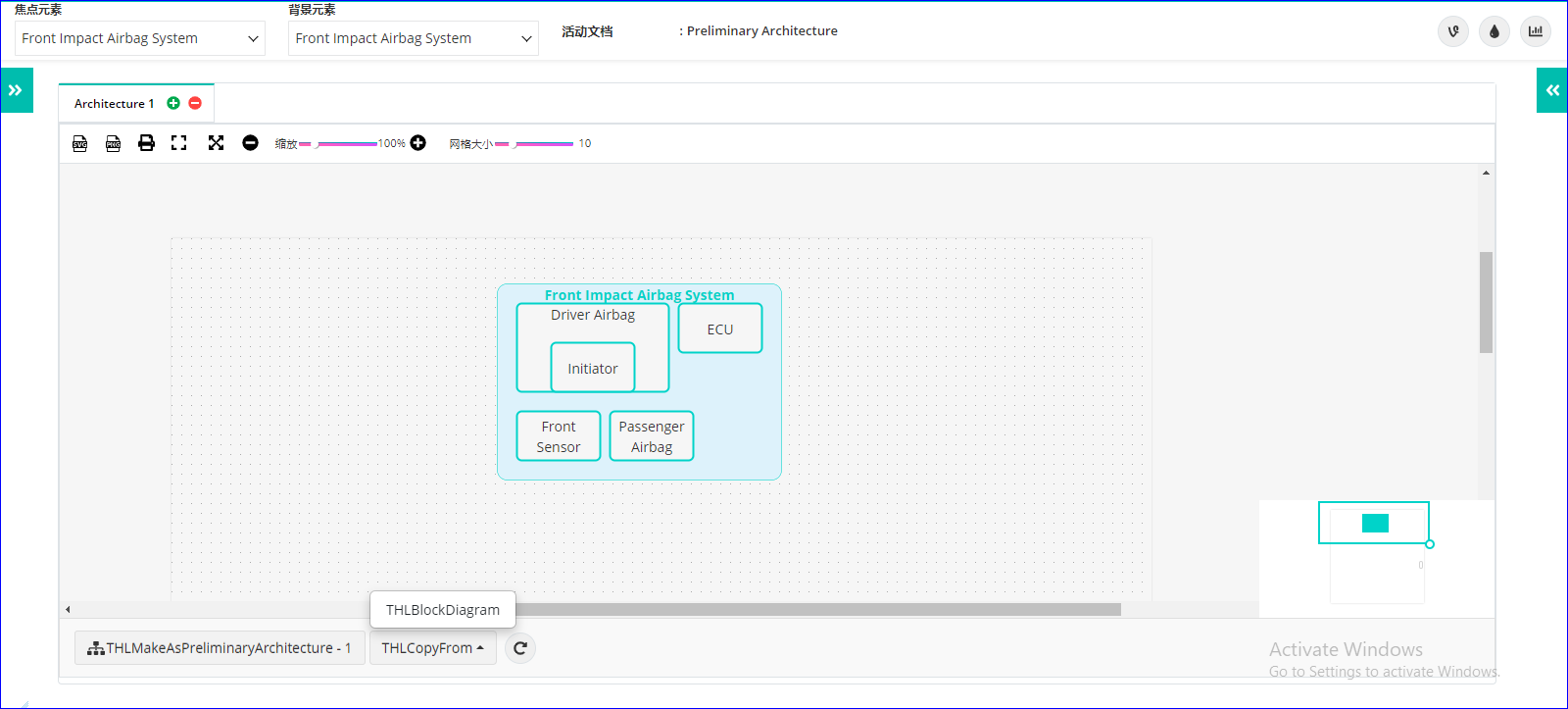




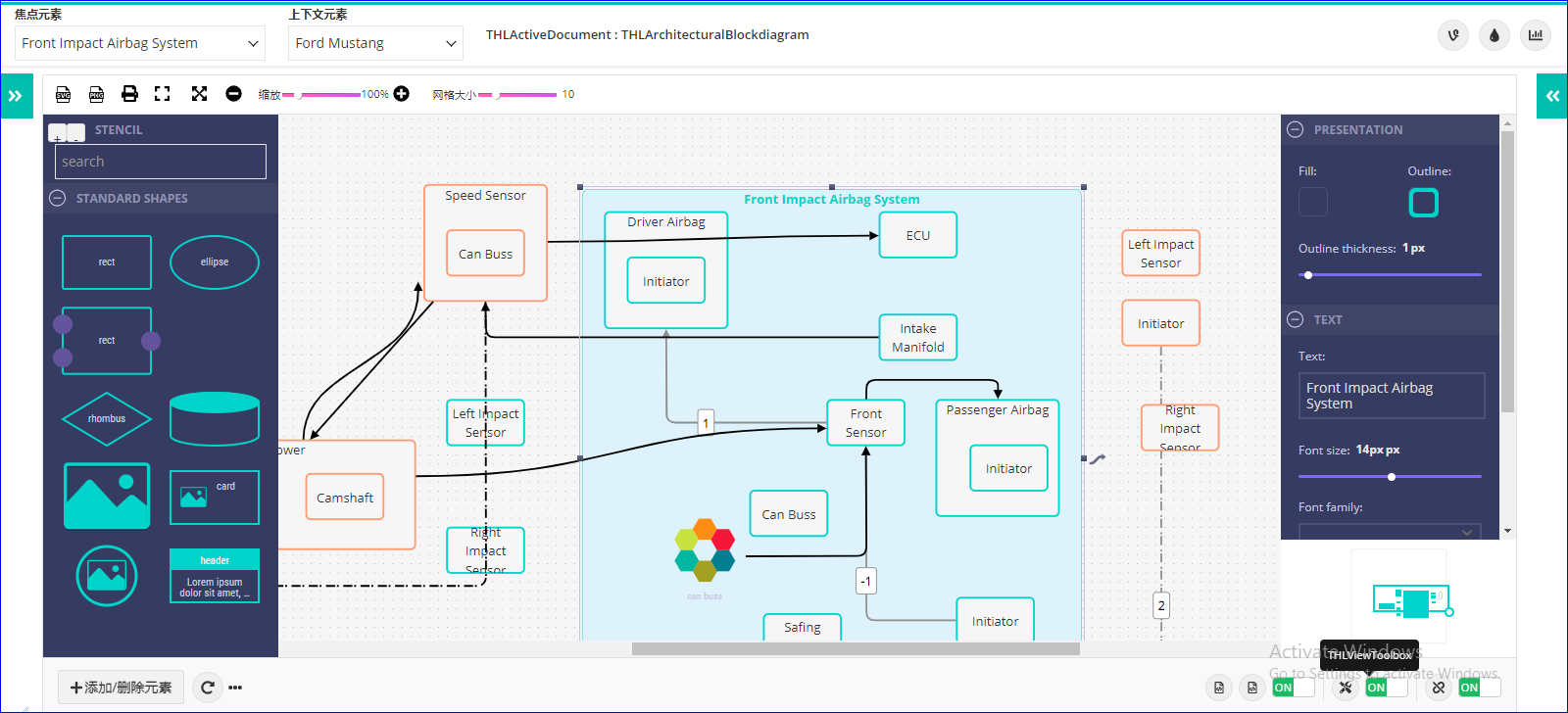
**Add image**

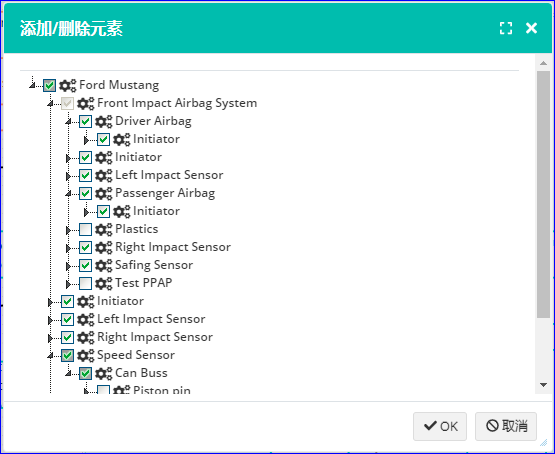


**Preliminary Architecture**

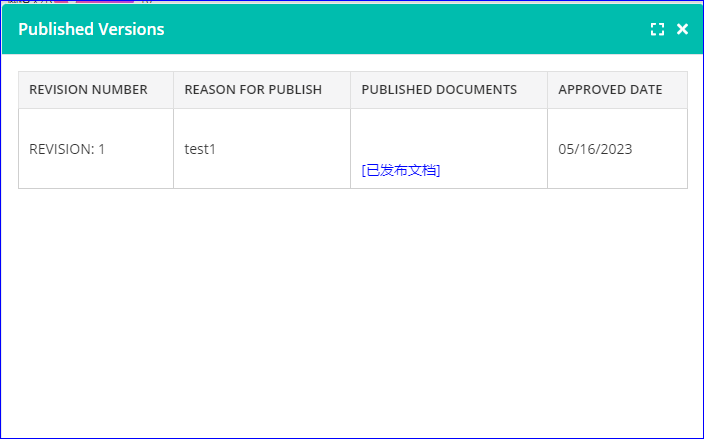


**Block diagram**

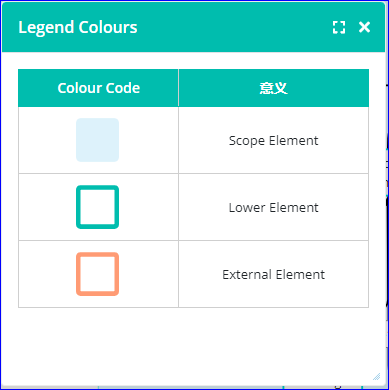




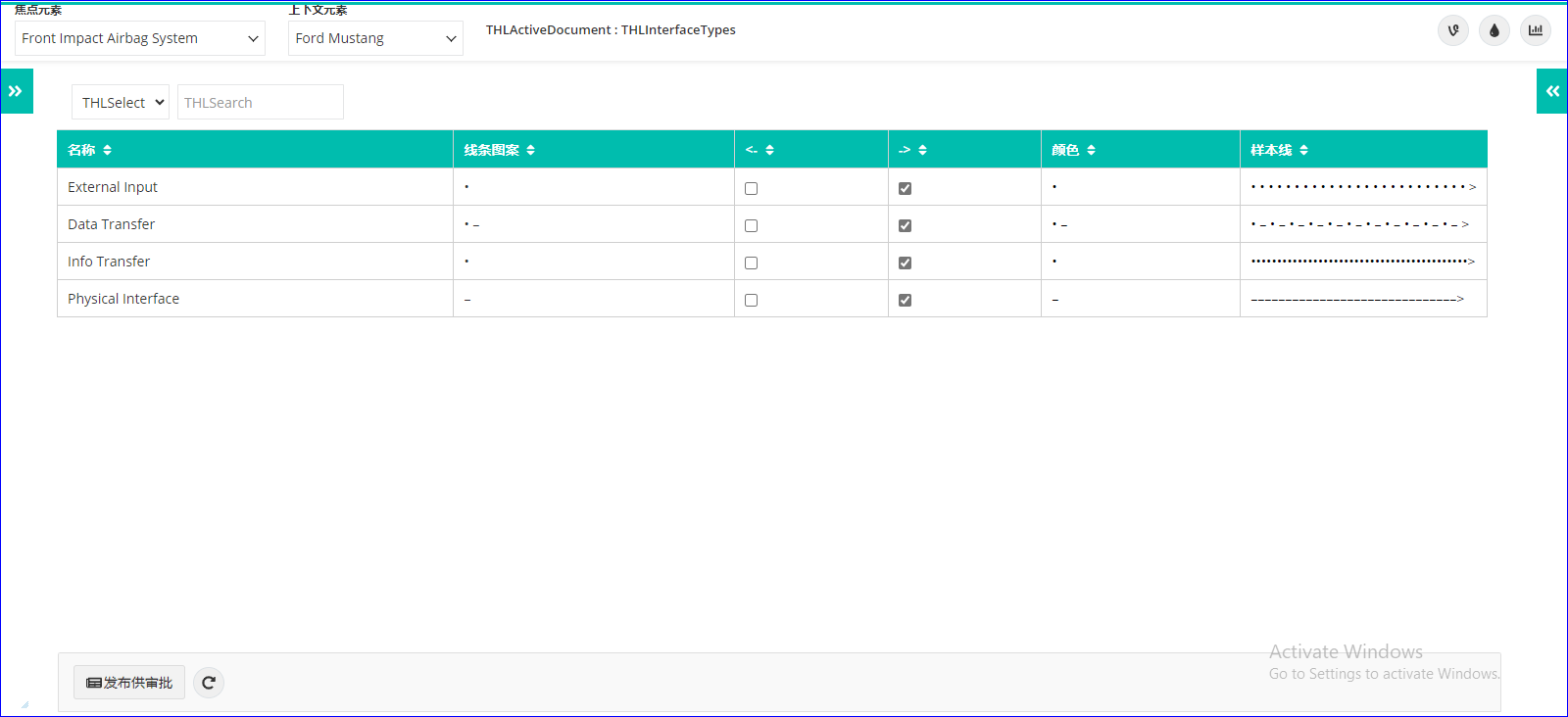
**Published version**



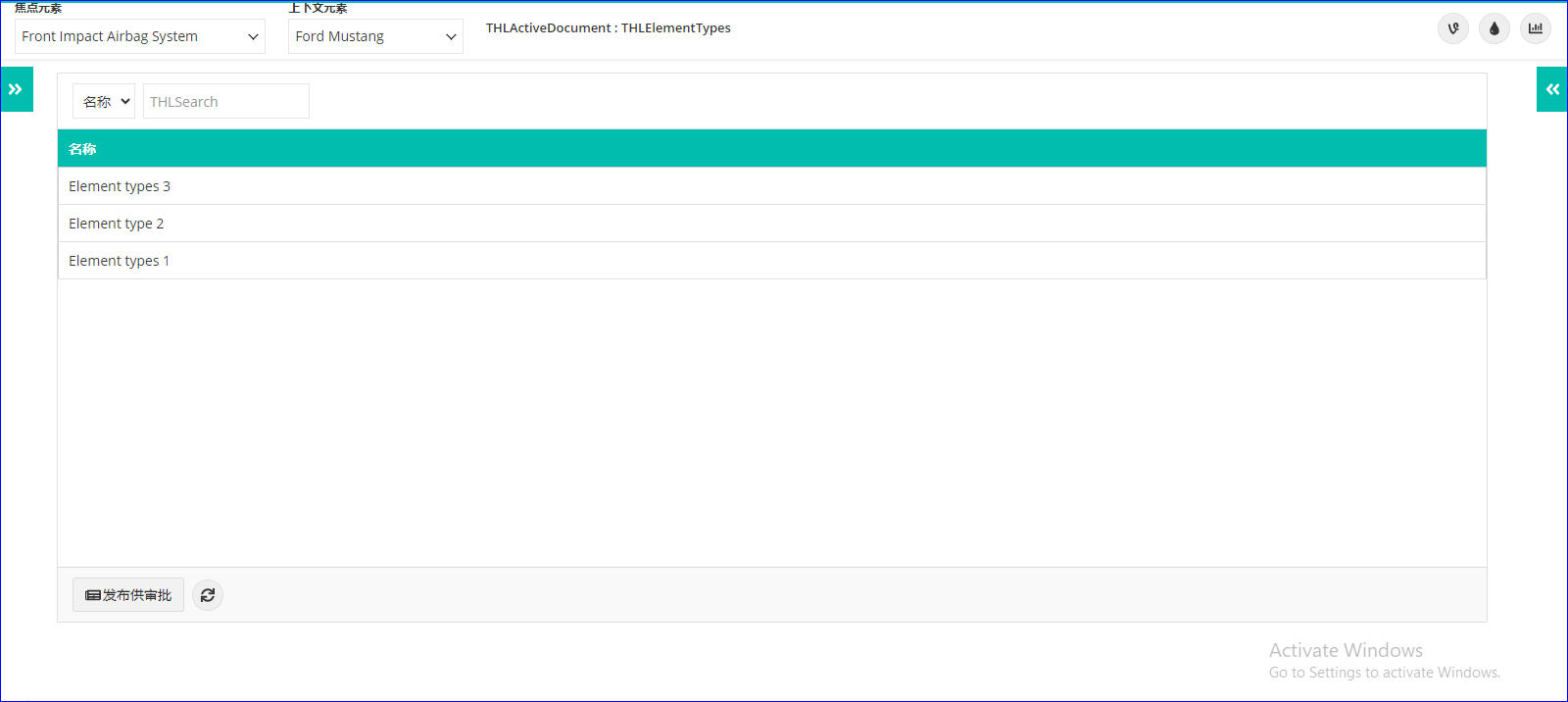
**Color code**



**Interface Type**



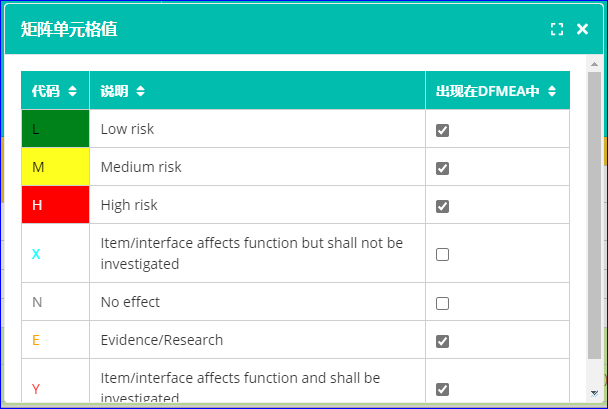
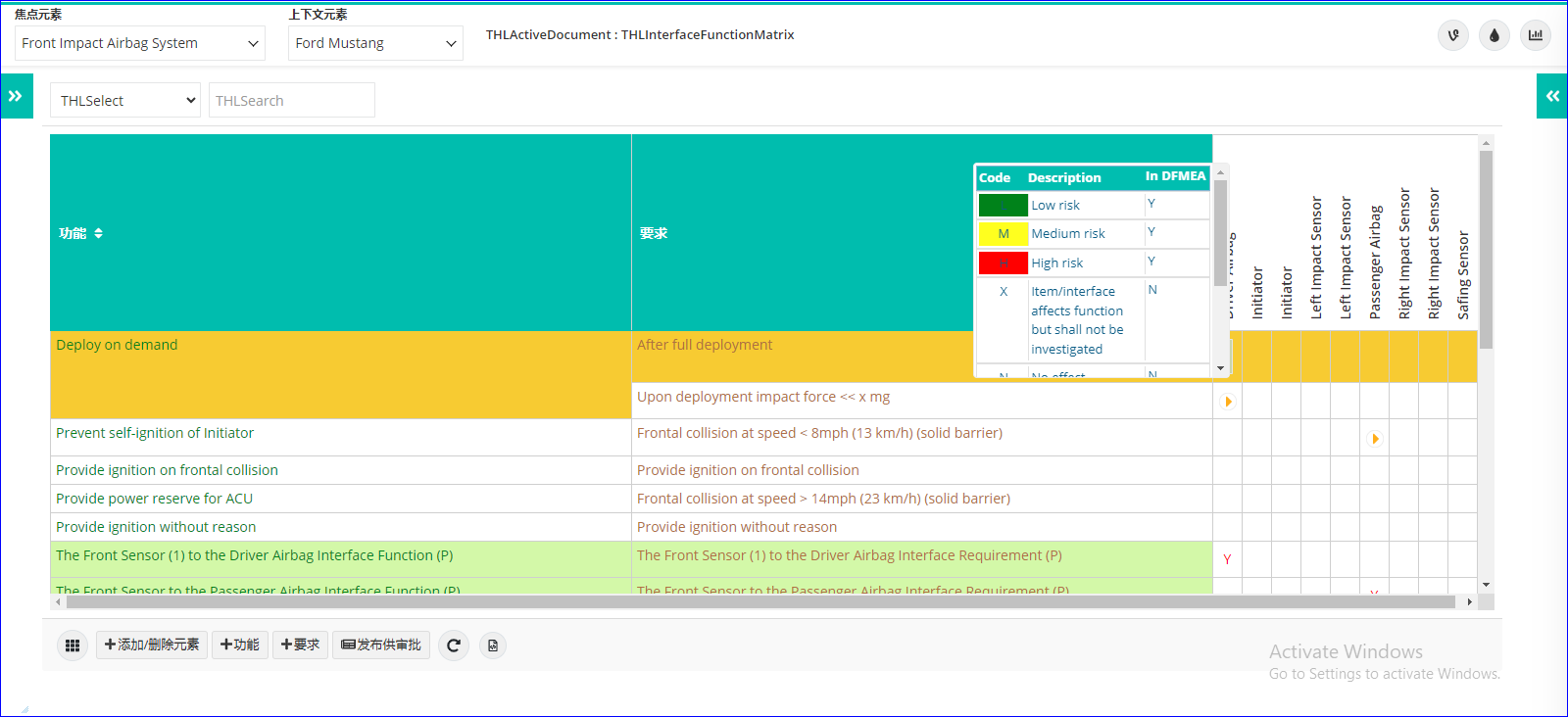
**Element type**



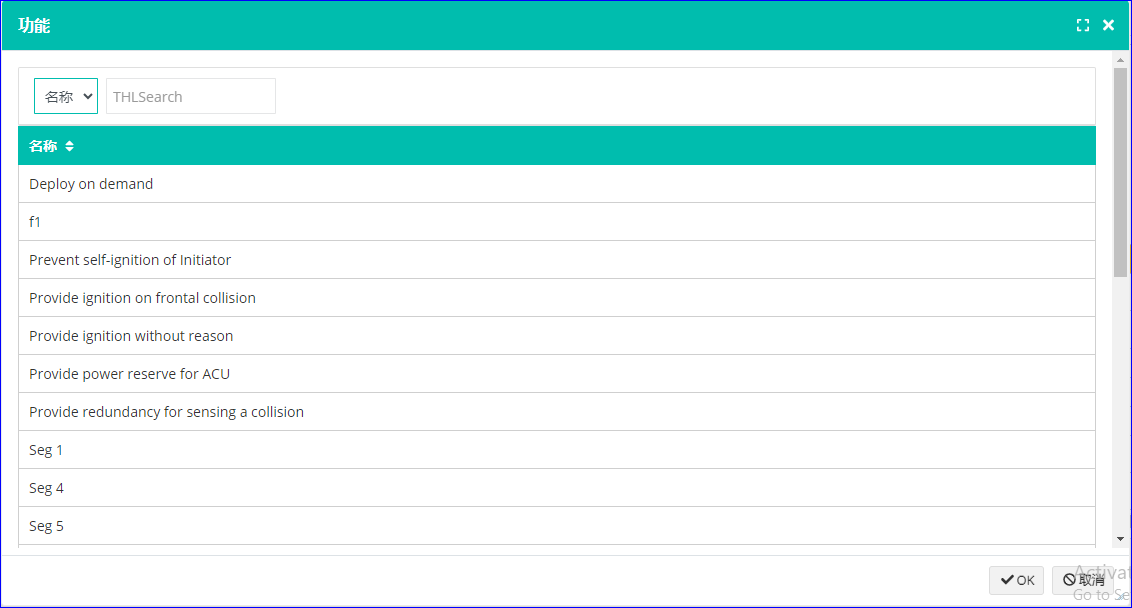
**Item Boundaries**



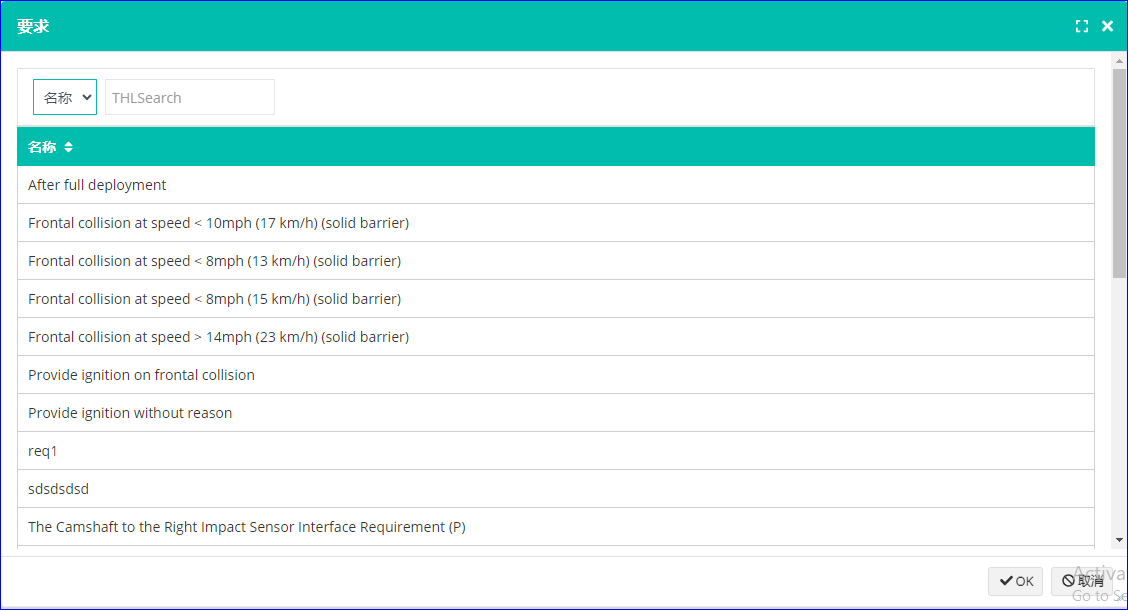
**Interface function matrix**



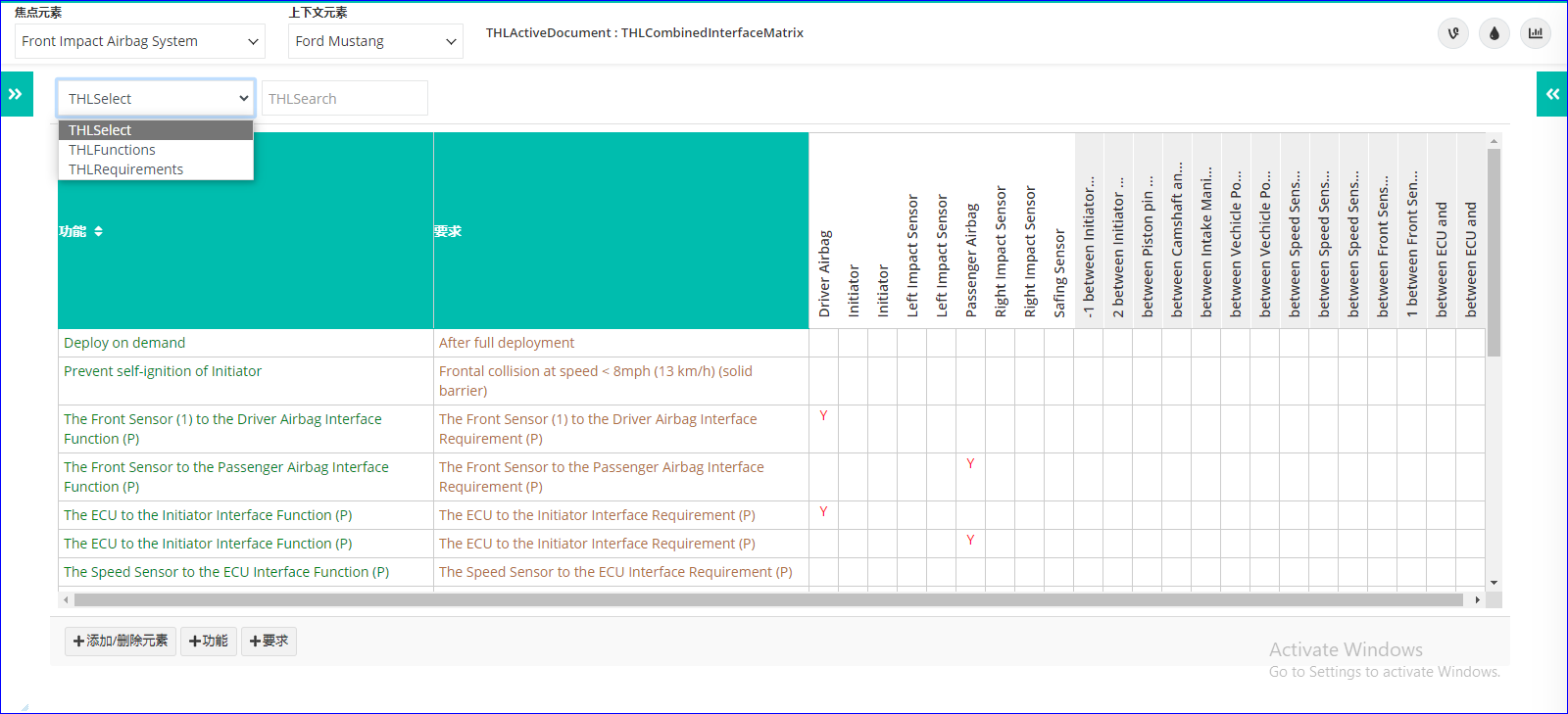
**Functions**



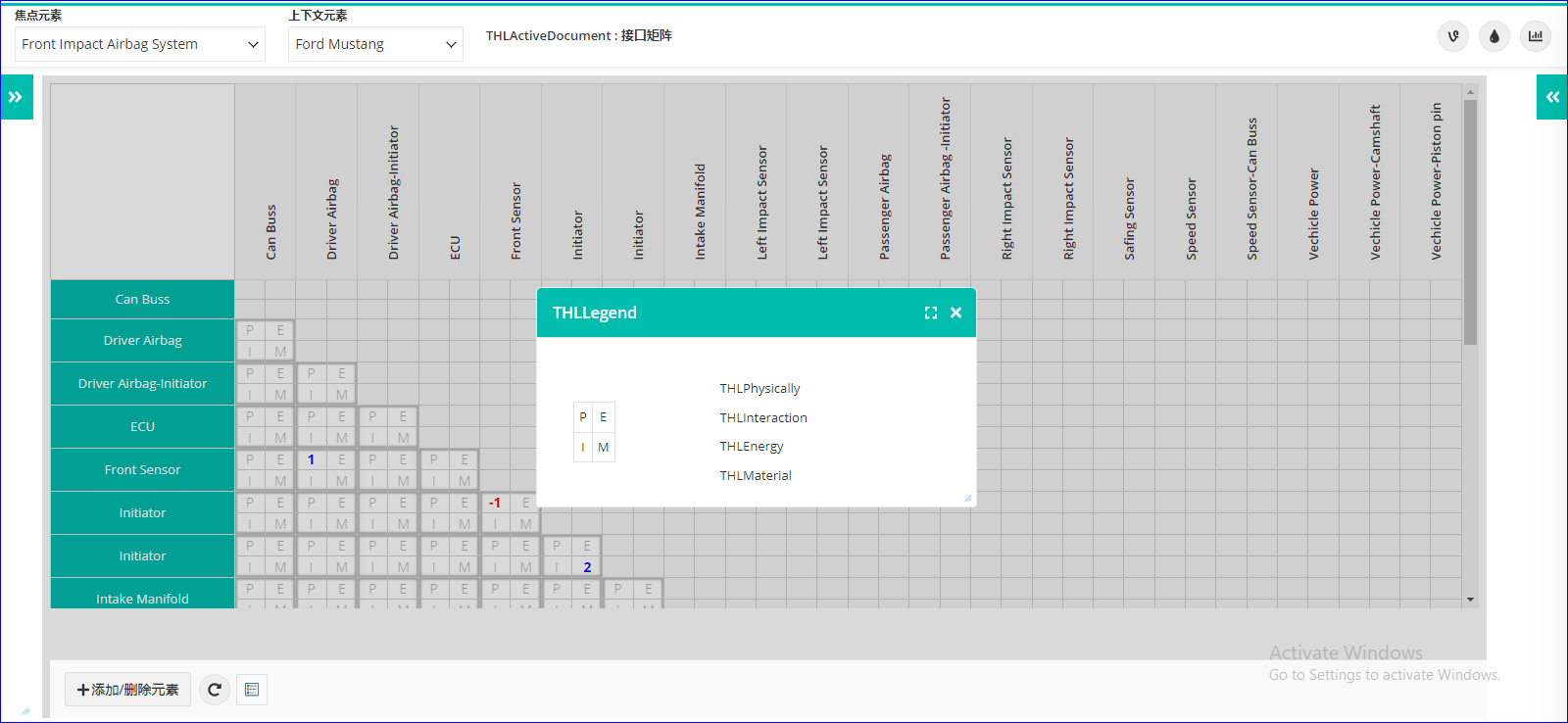
**Requirement**



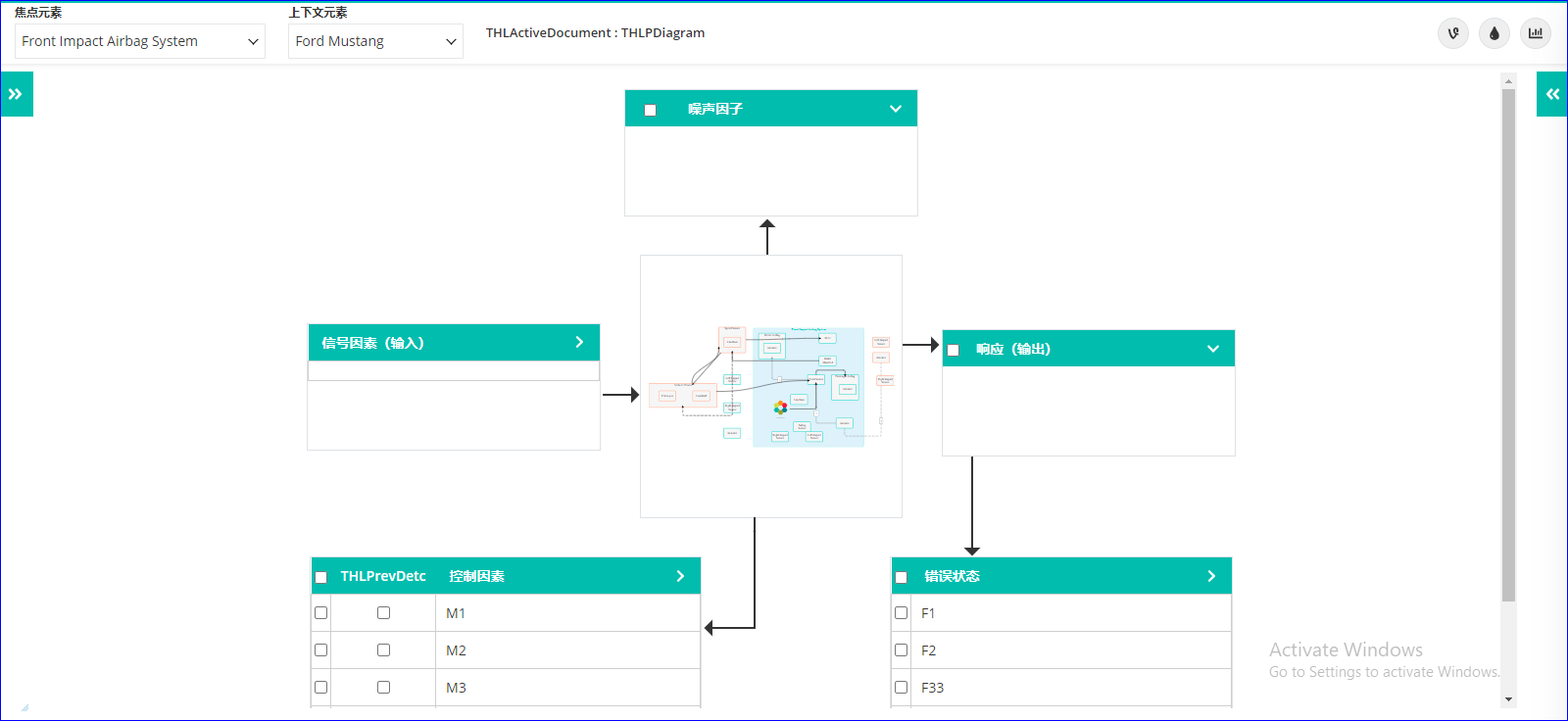
**Combined Interface matrix**



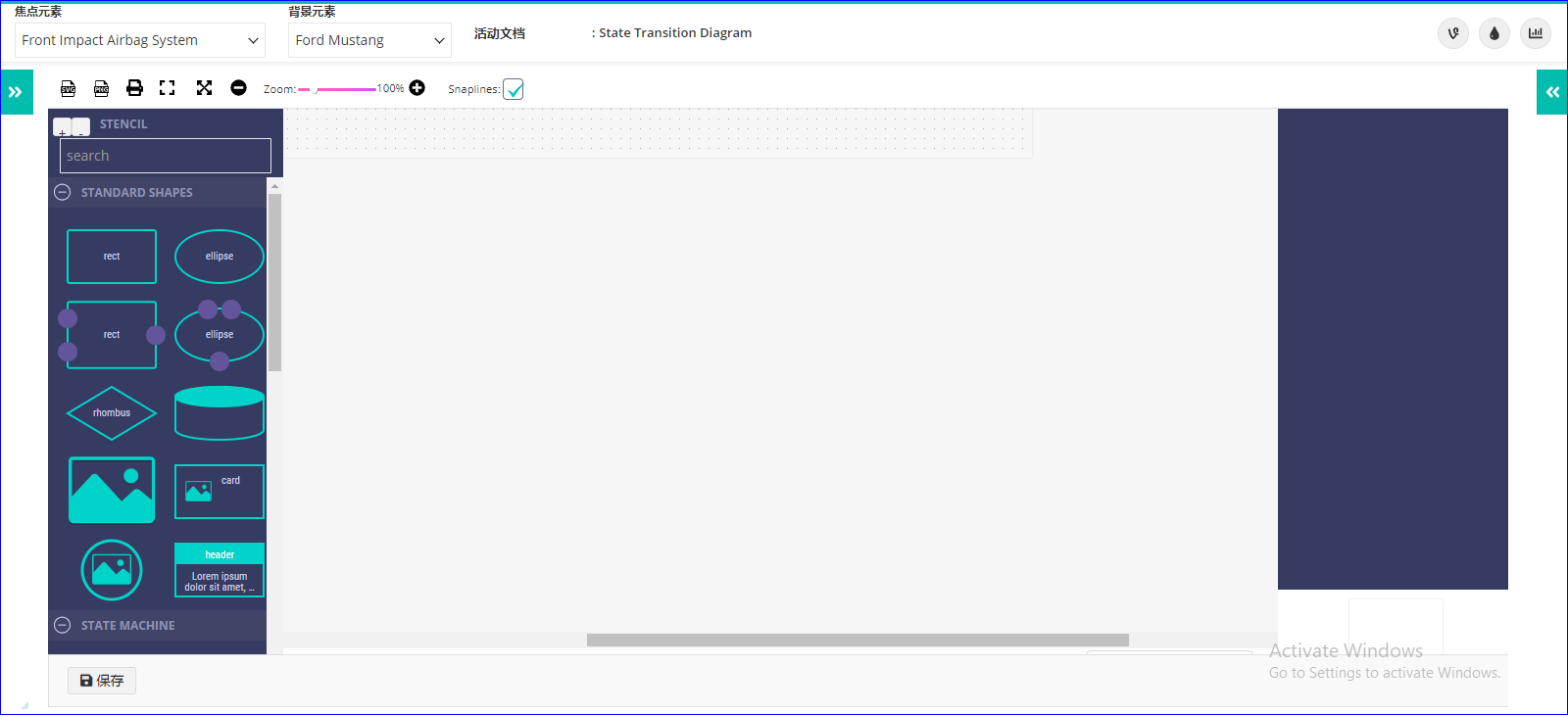
**Interface Matrix**



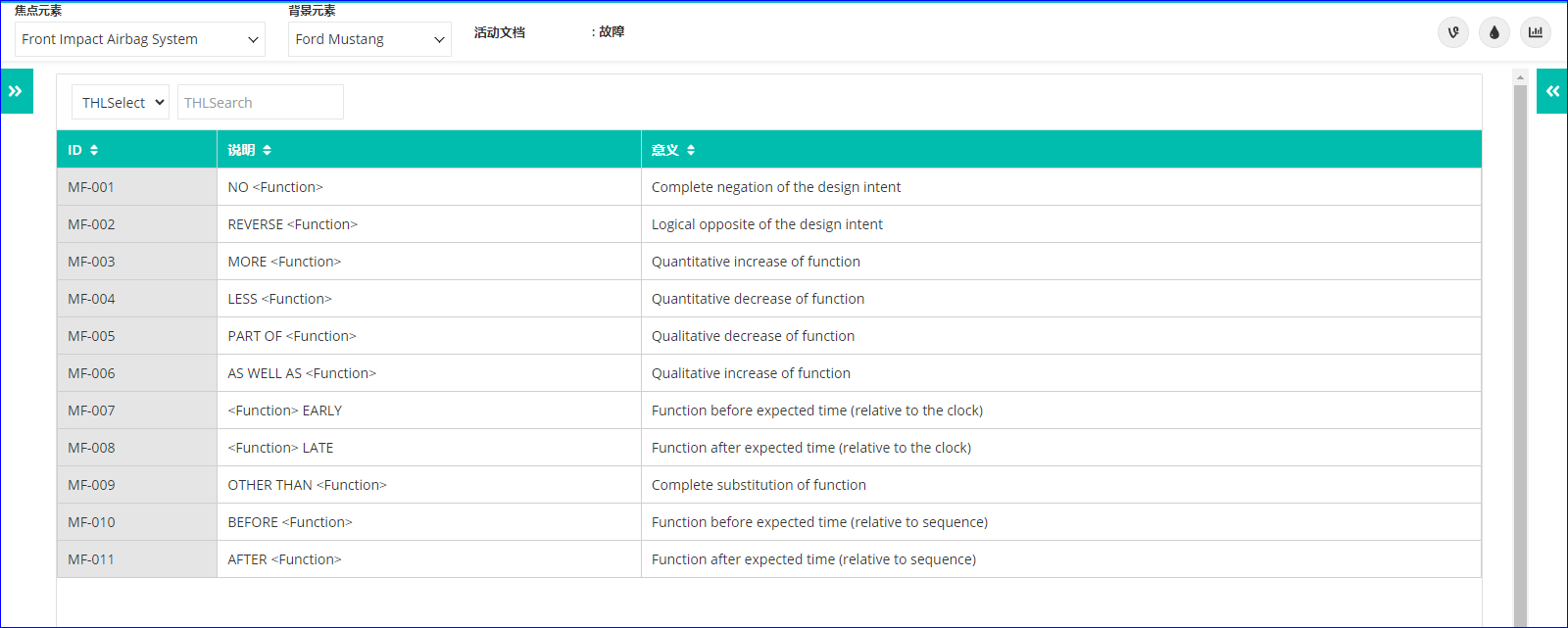
**P-Diagram**



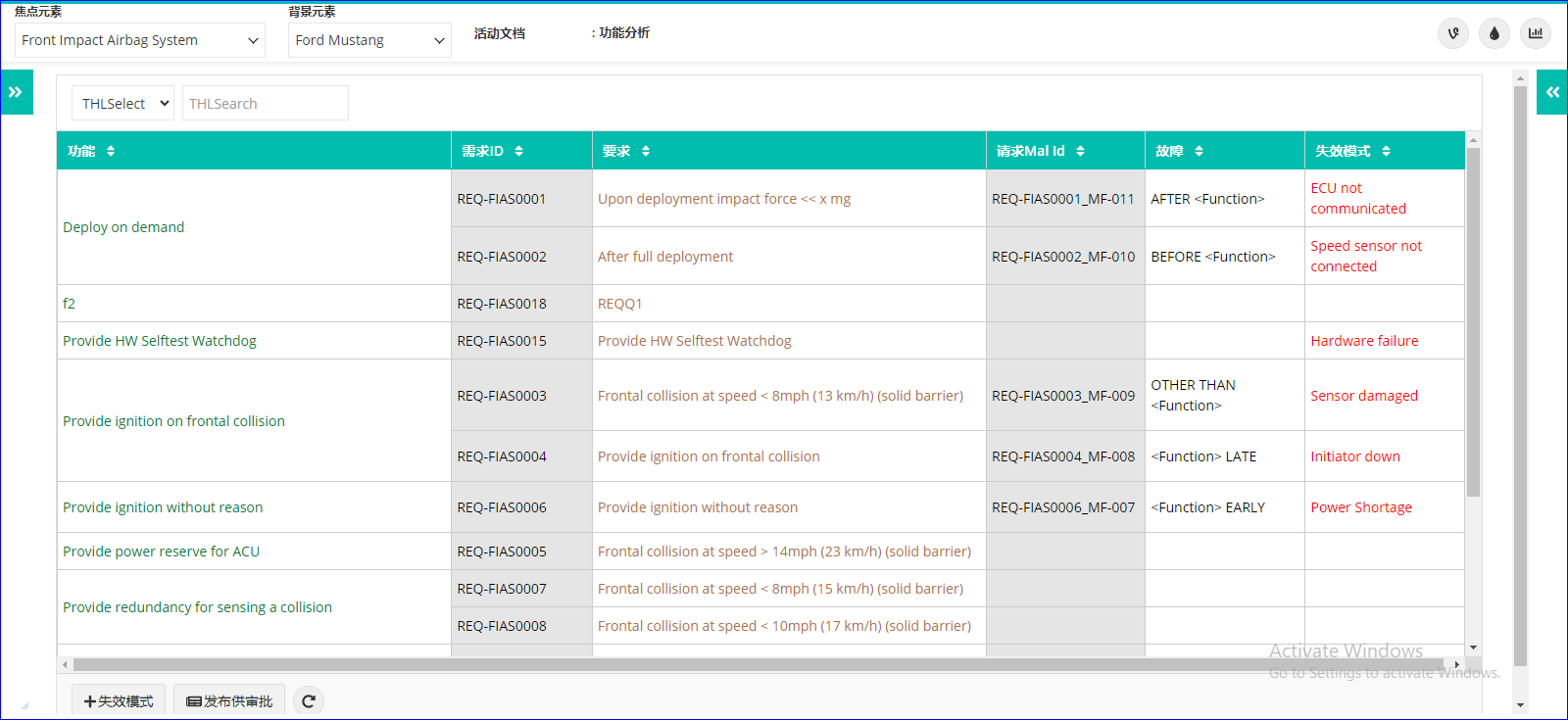
**State transition diagram**



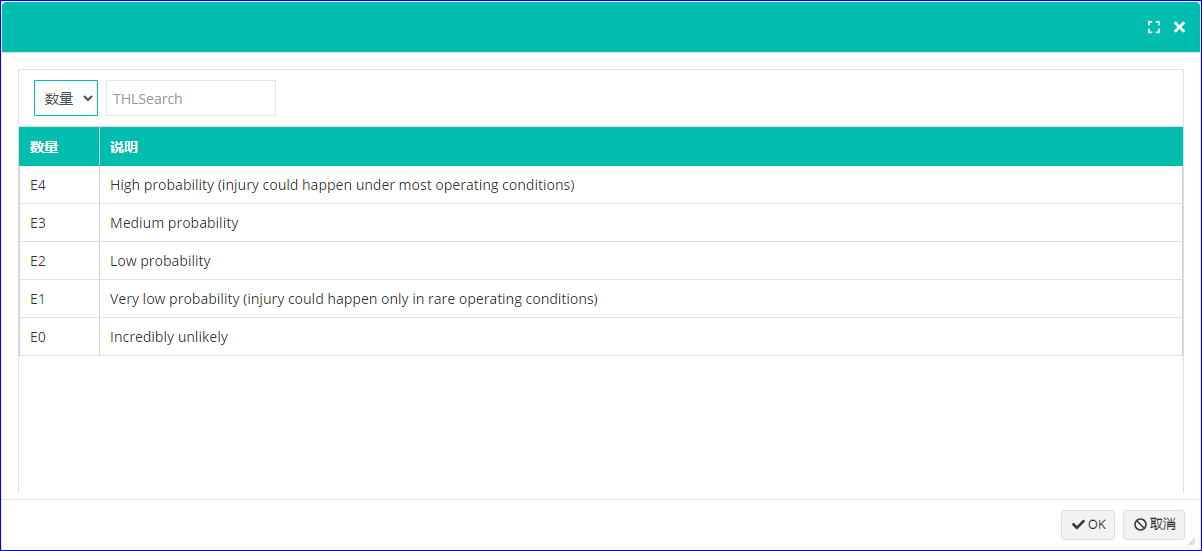
**Malfunctions**



**Functional analysis**



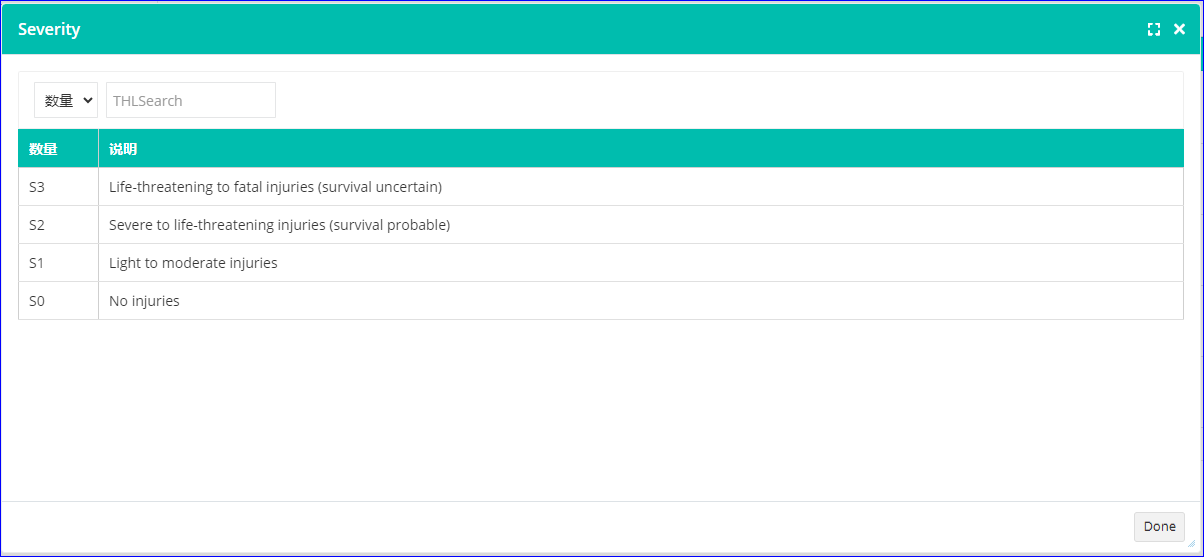
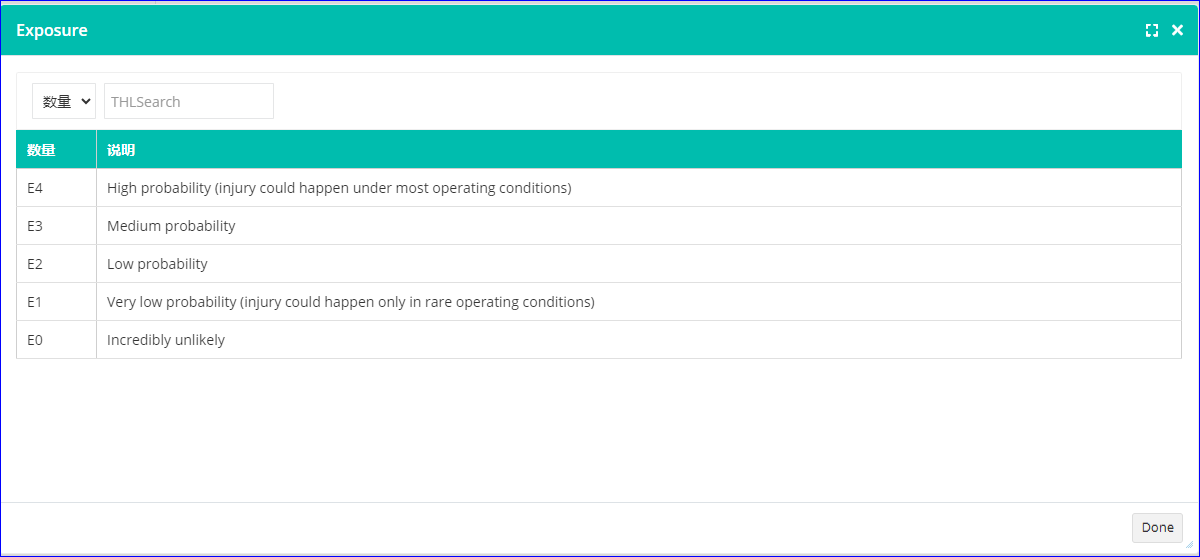
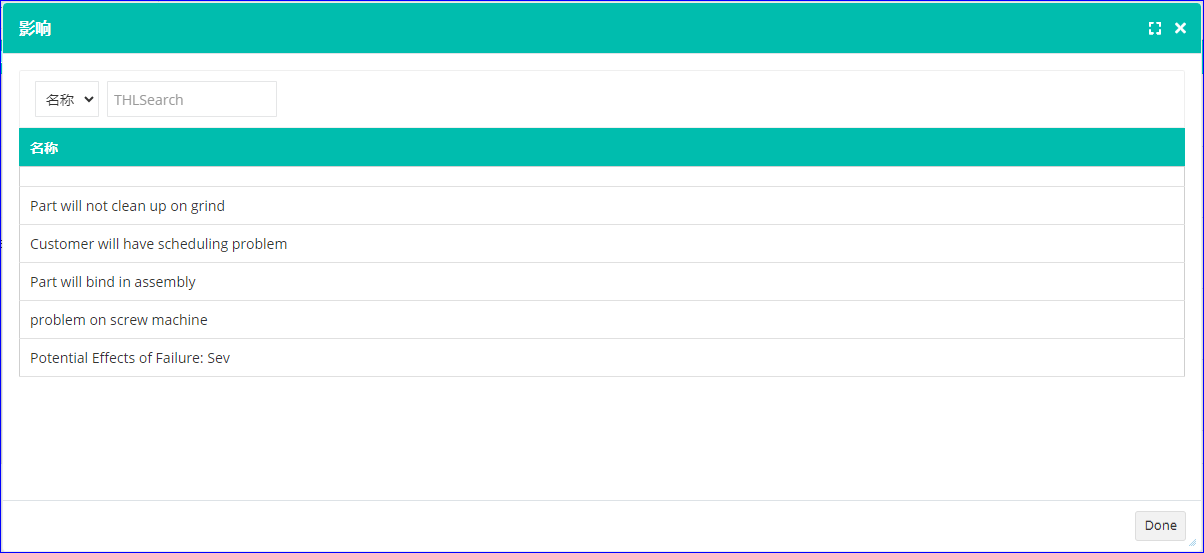
**Operational situation**

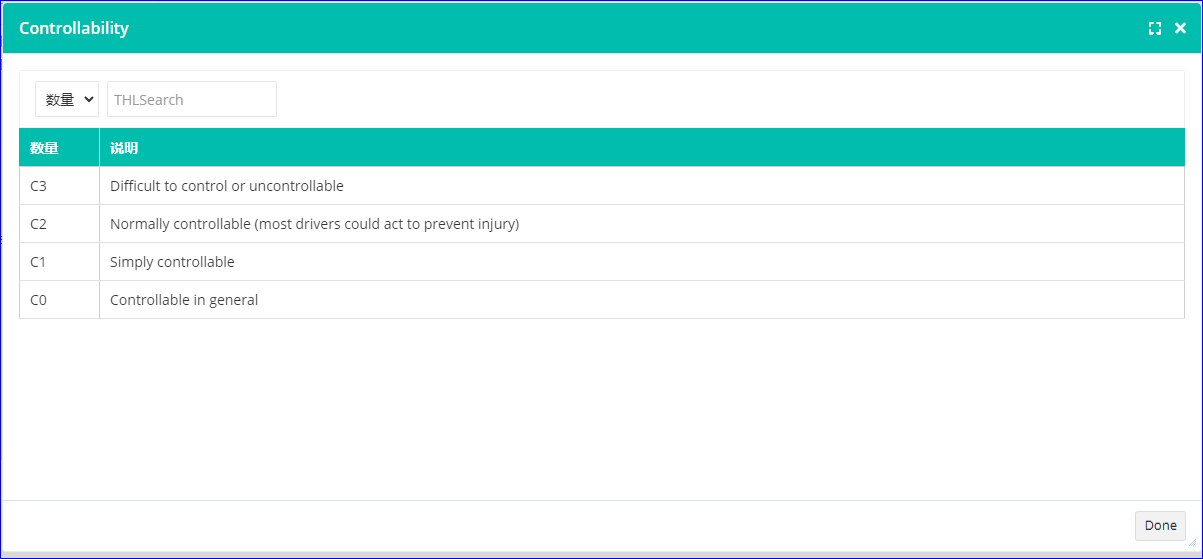


**HARA**

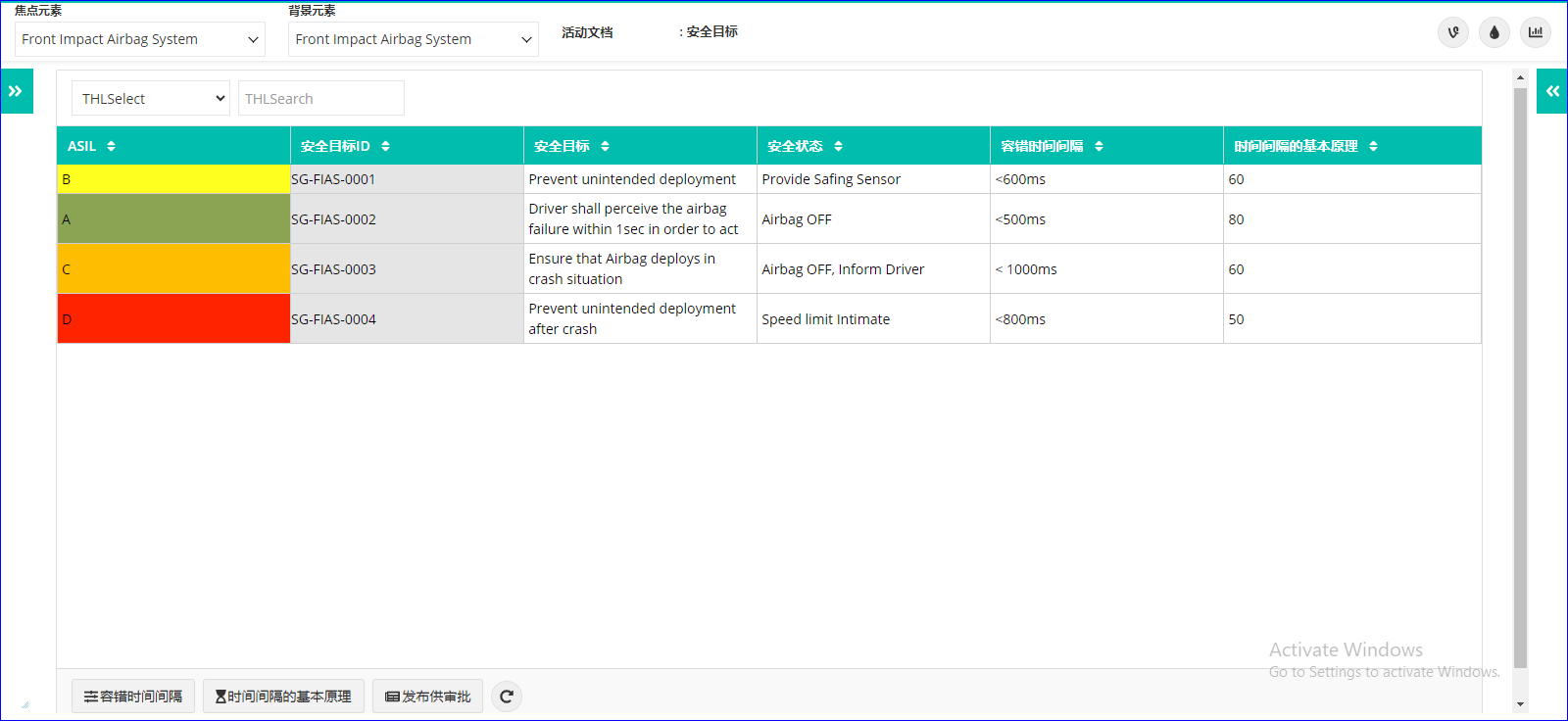


**Effect**





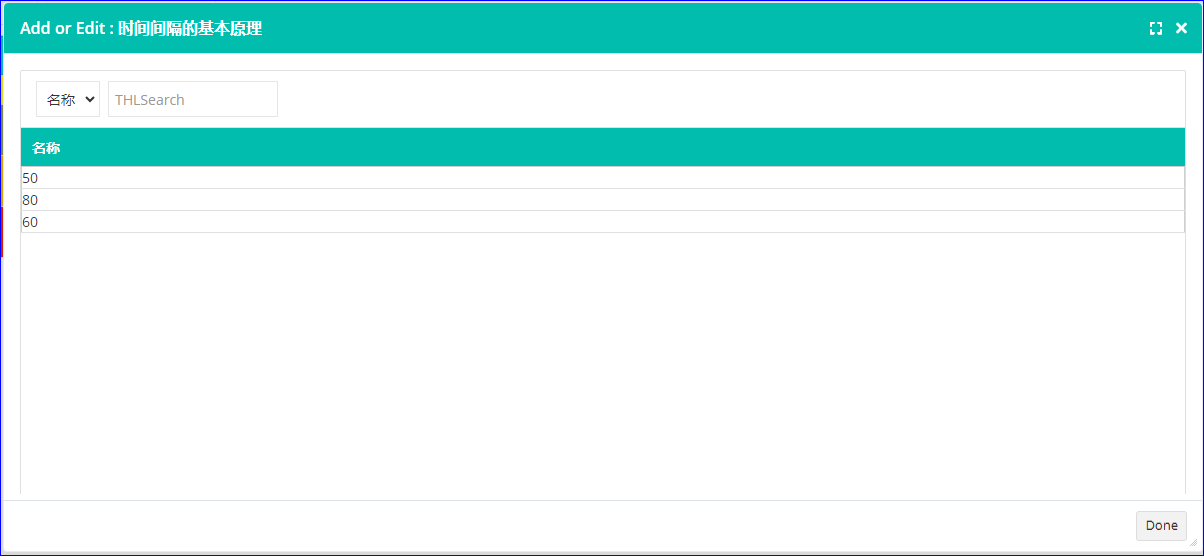
**Safety goal**



**FTI**



**RTI**



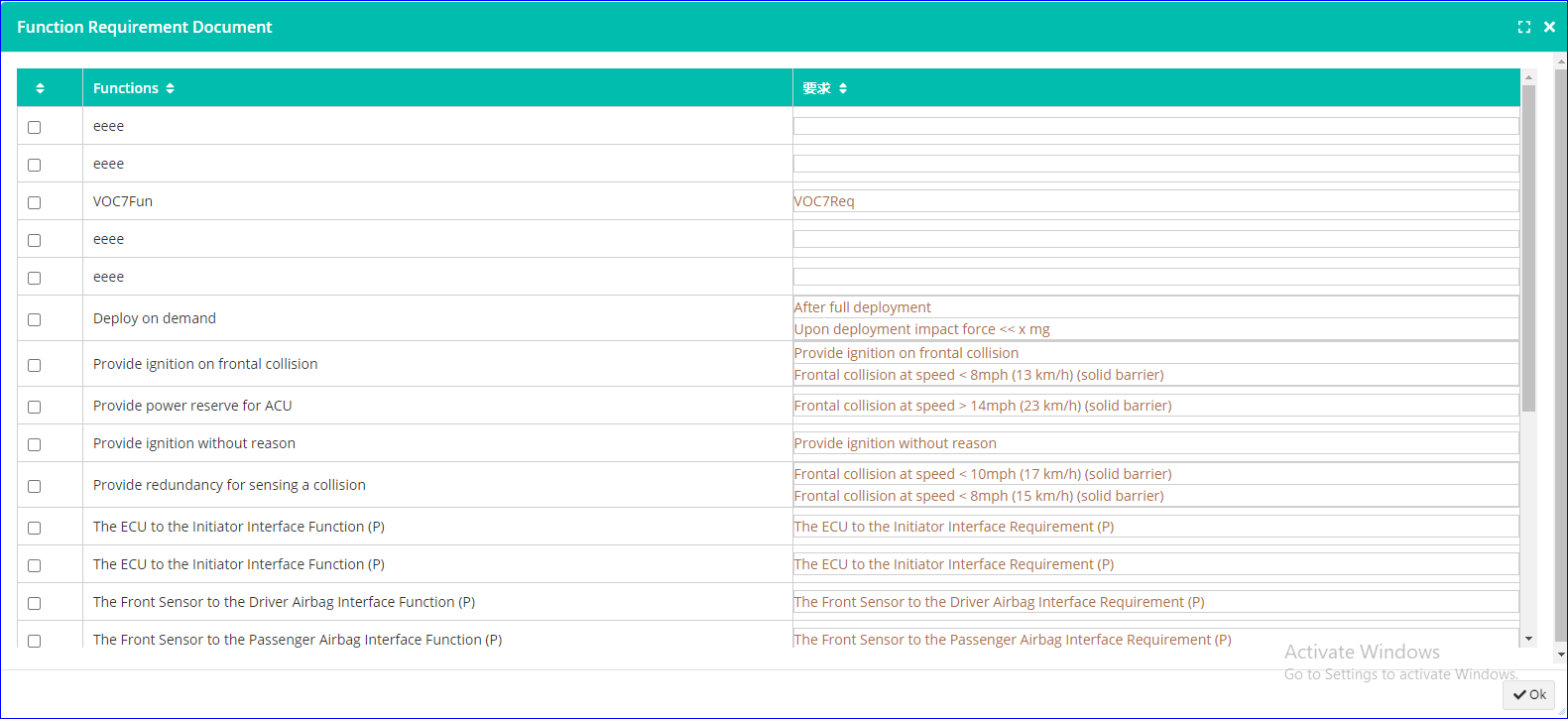
**Safe state**



**FSR**



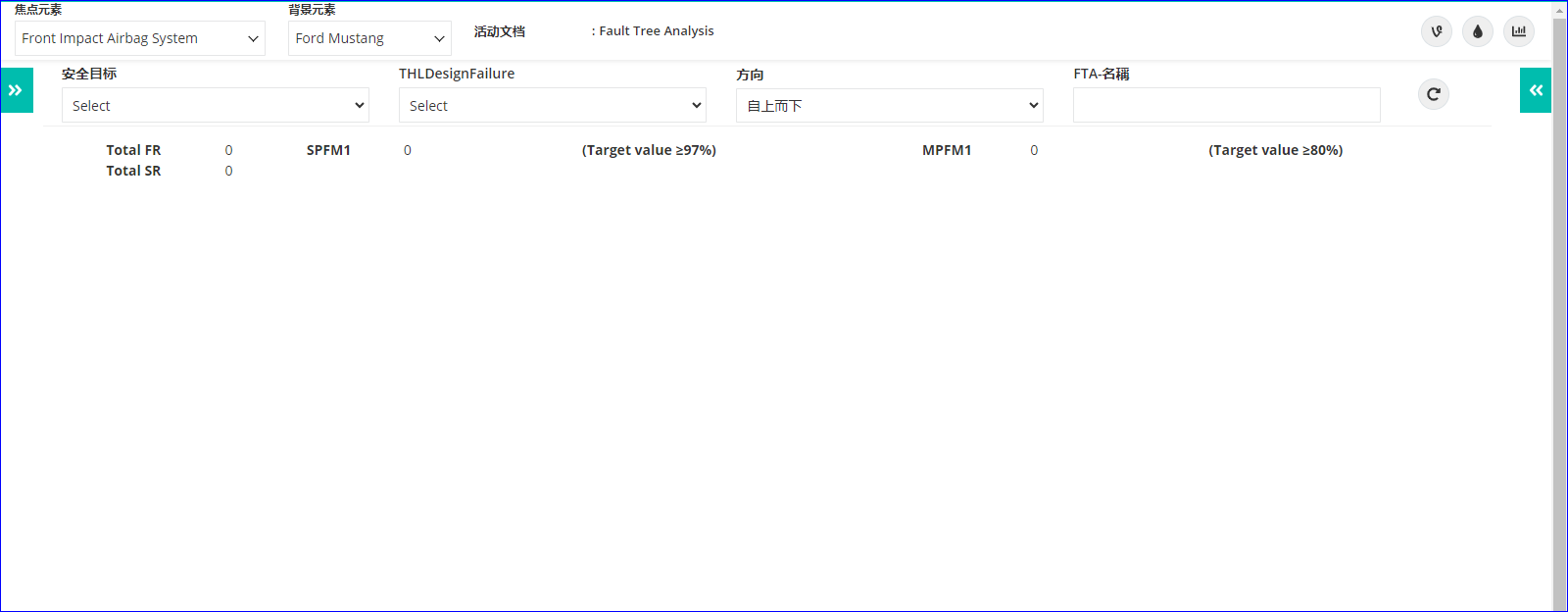
**+DFMEA**



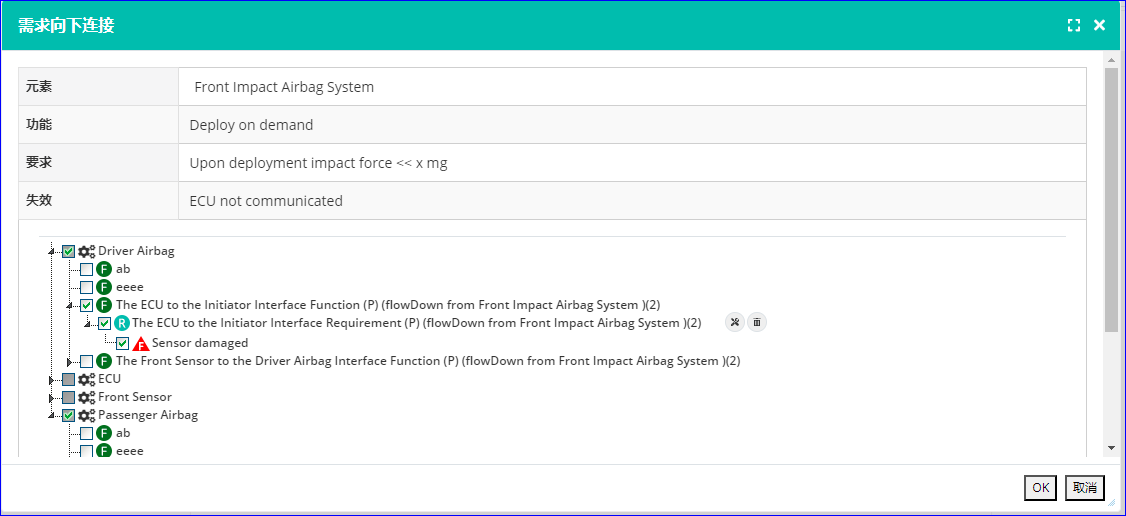
**TSR**

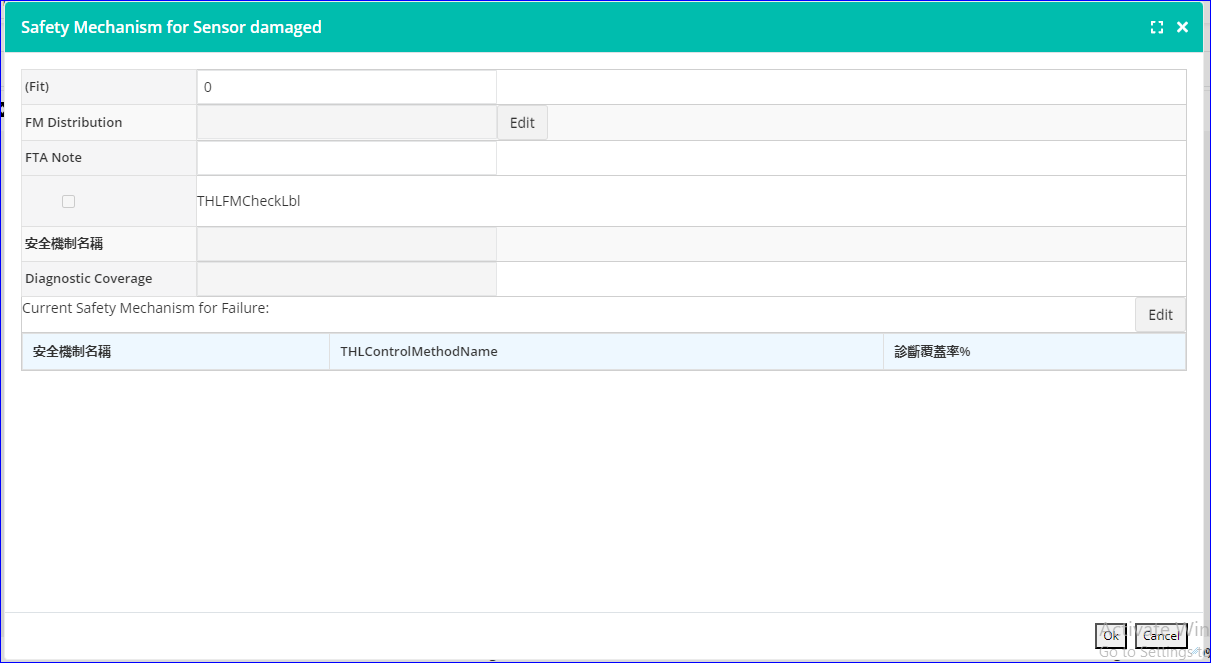


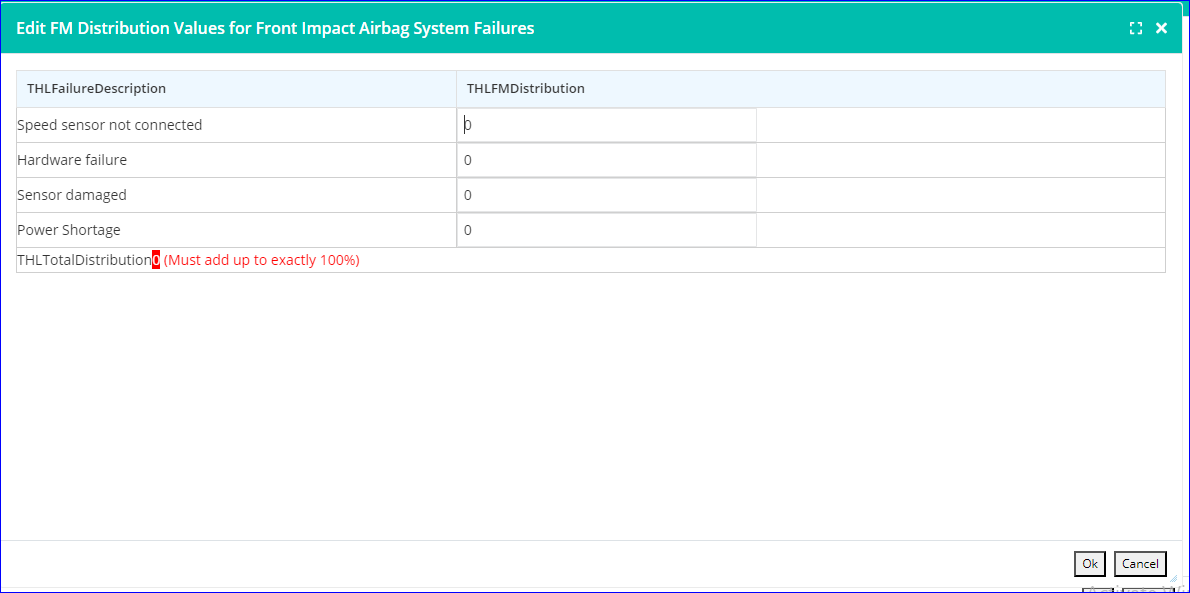
**FTA**

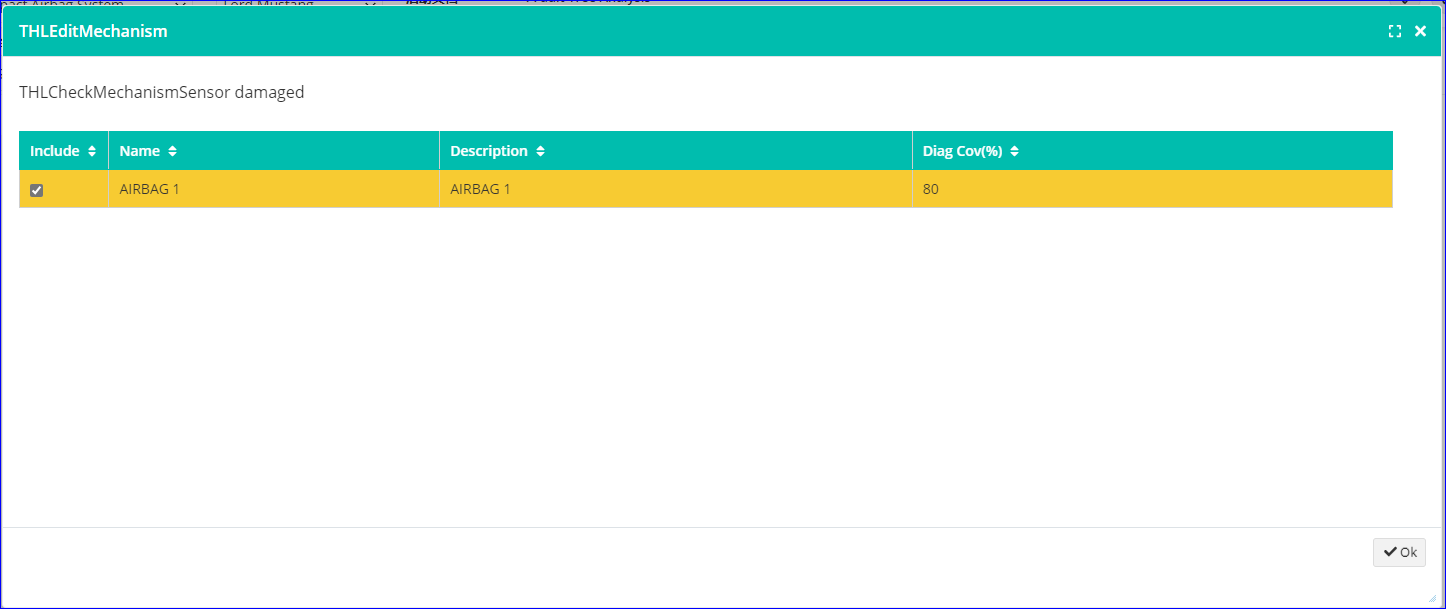




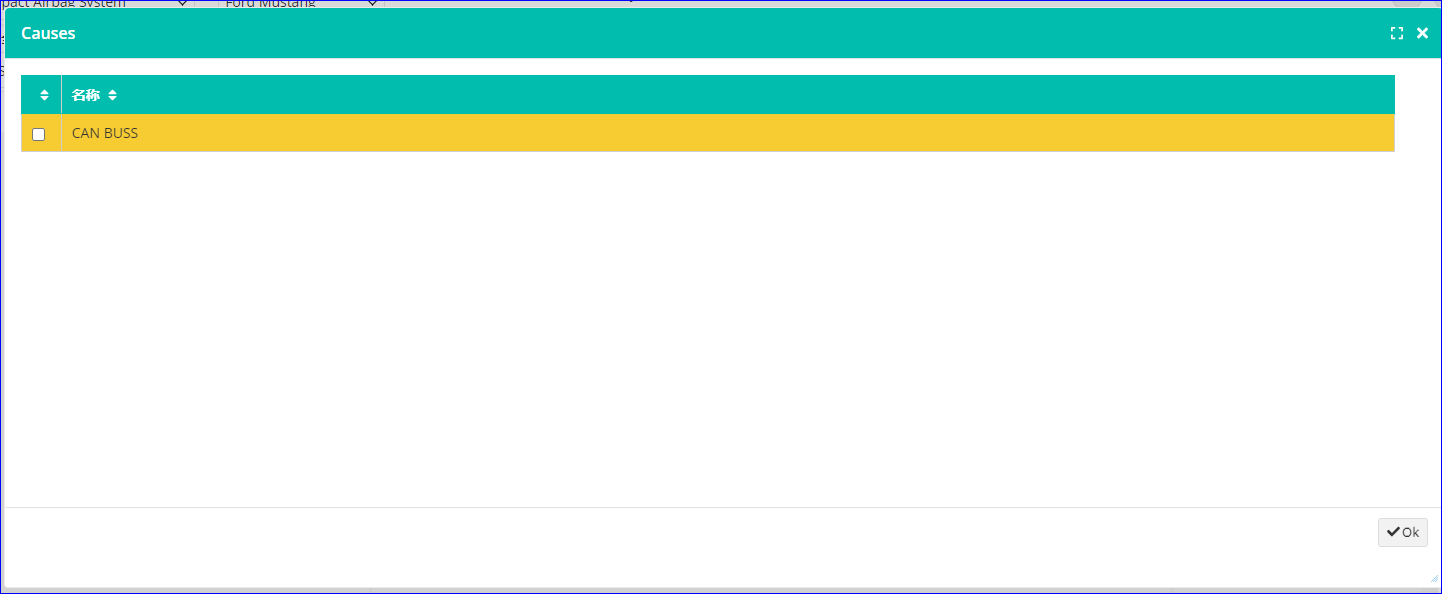








**Cause**



**Place holder**

