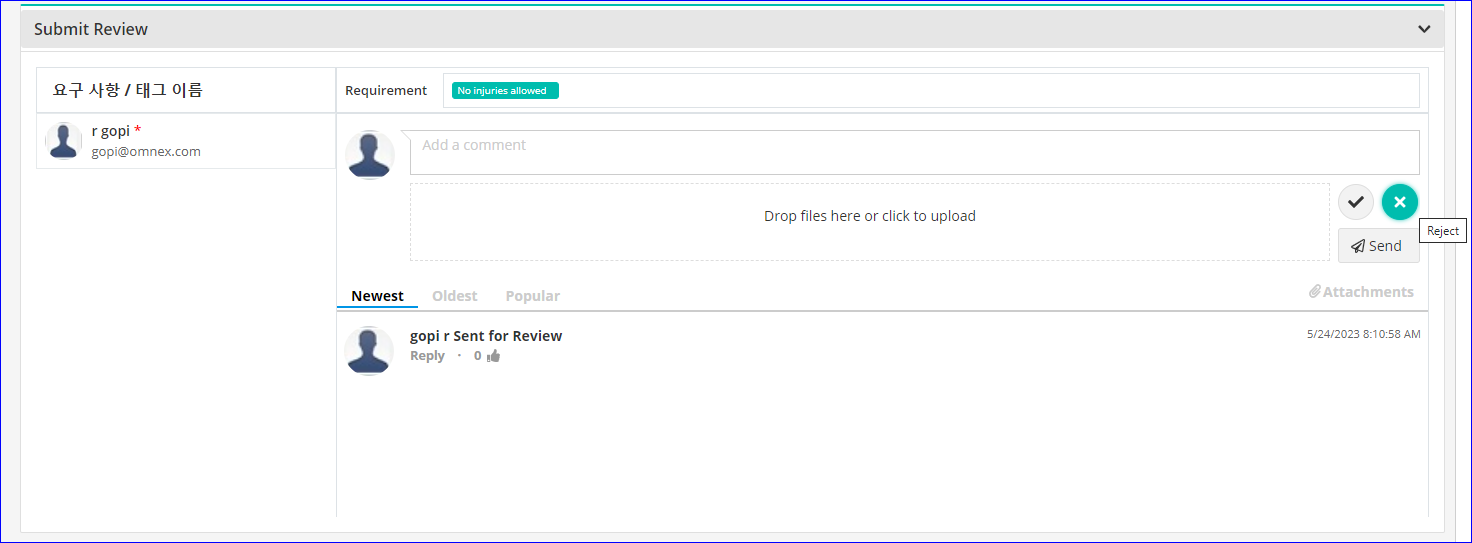
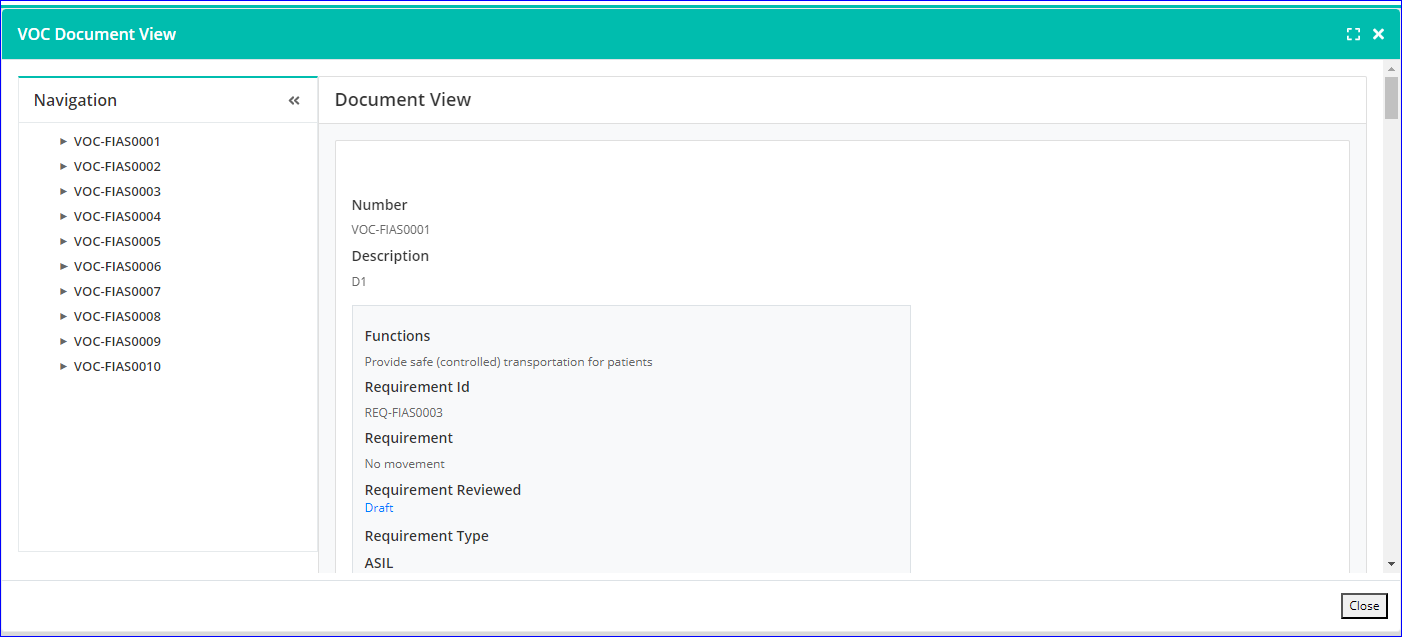
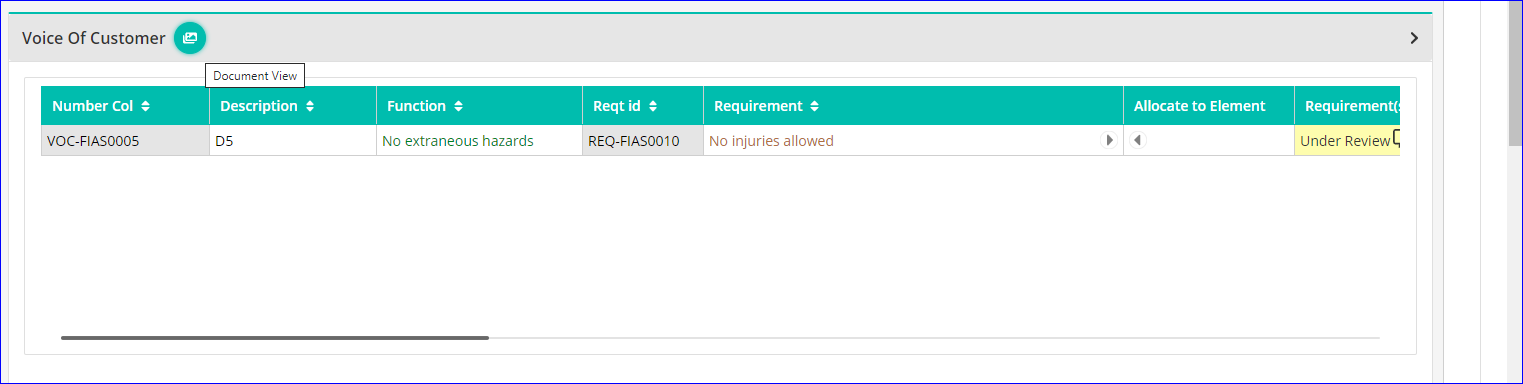
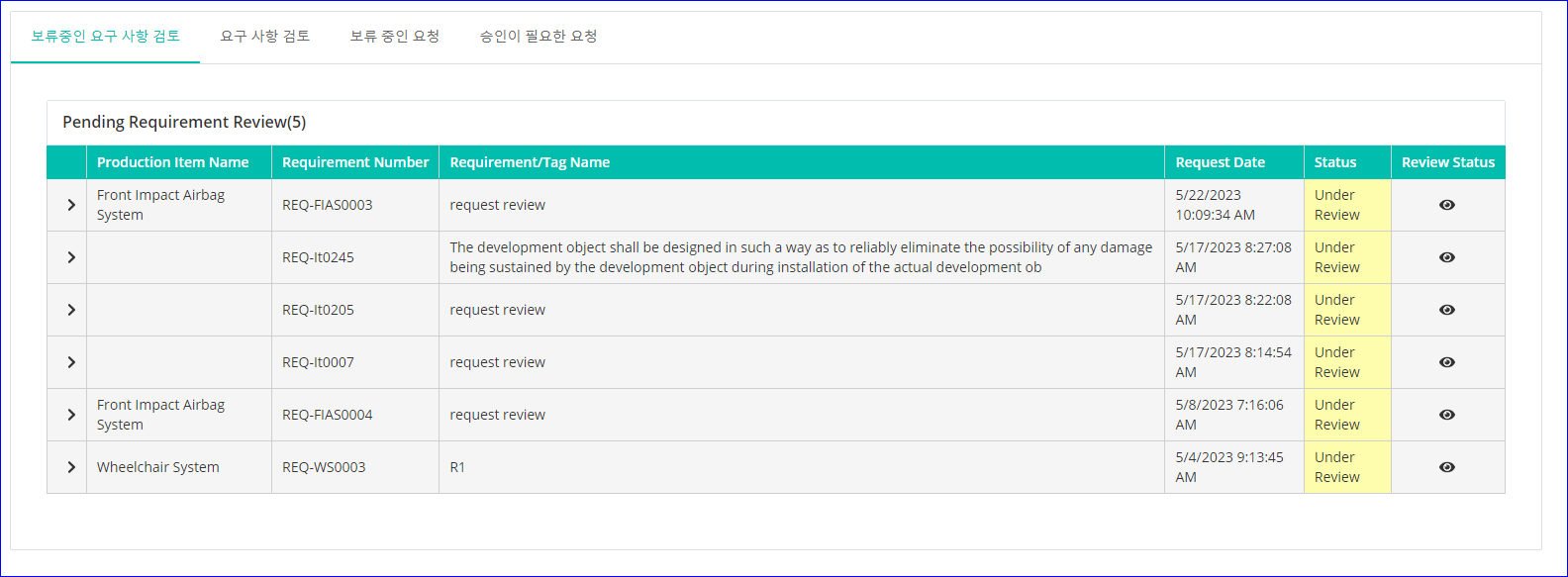
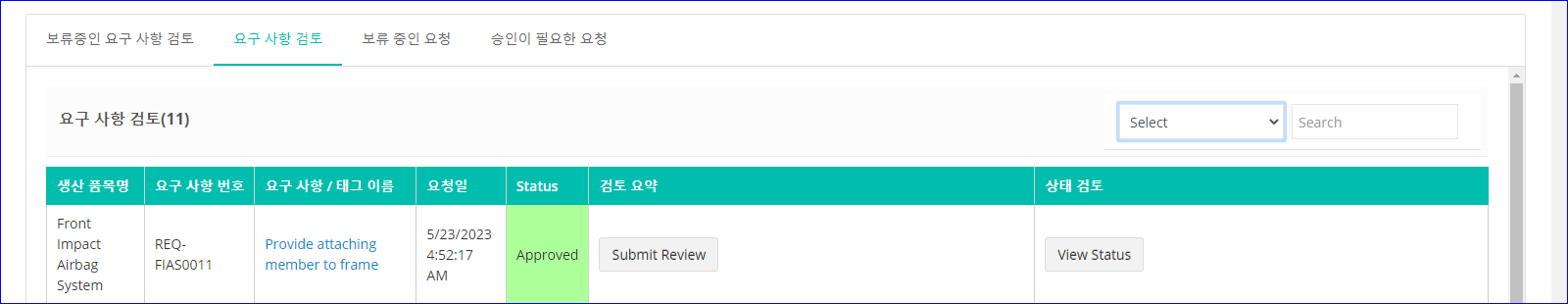
**Preference**

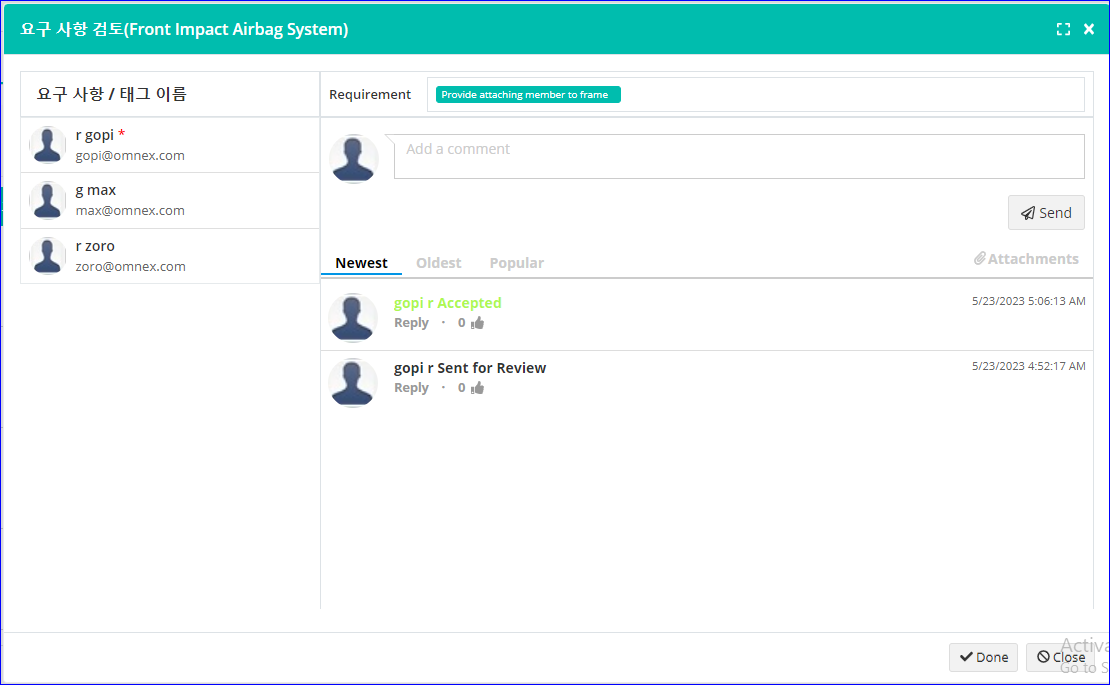


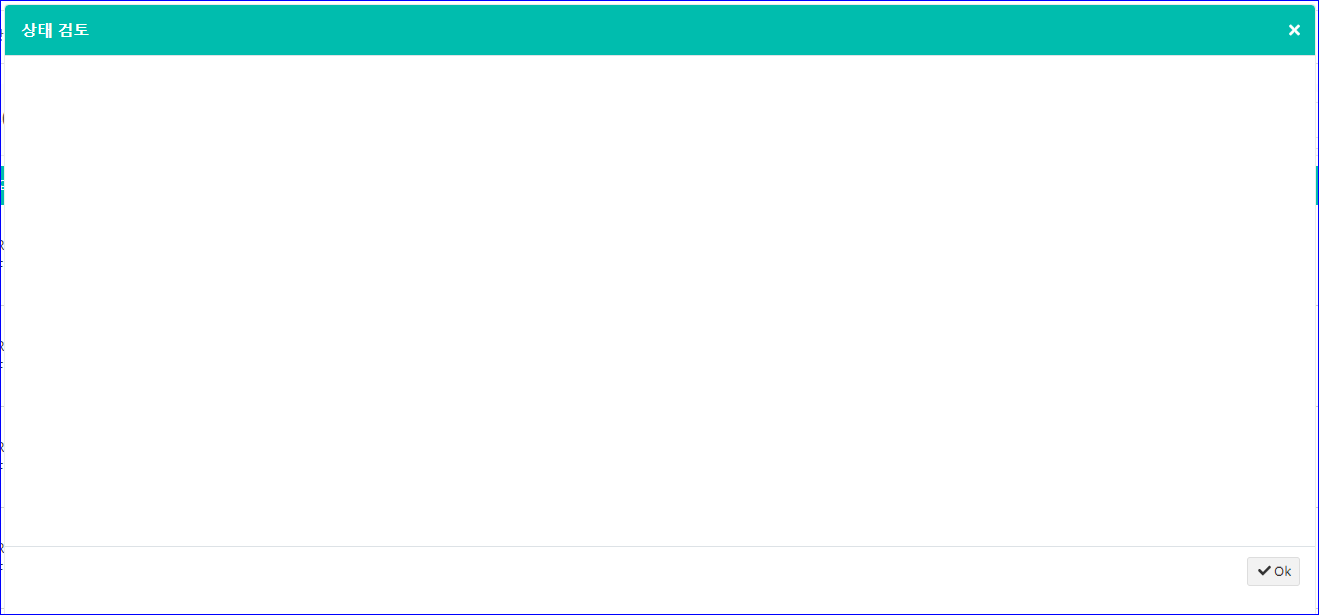
**Action   
Pending Requirement Review**



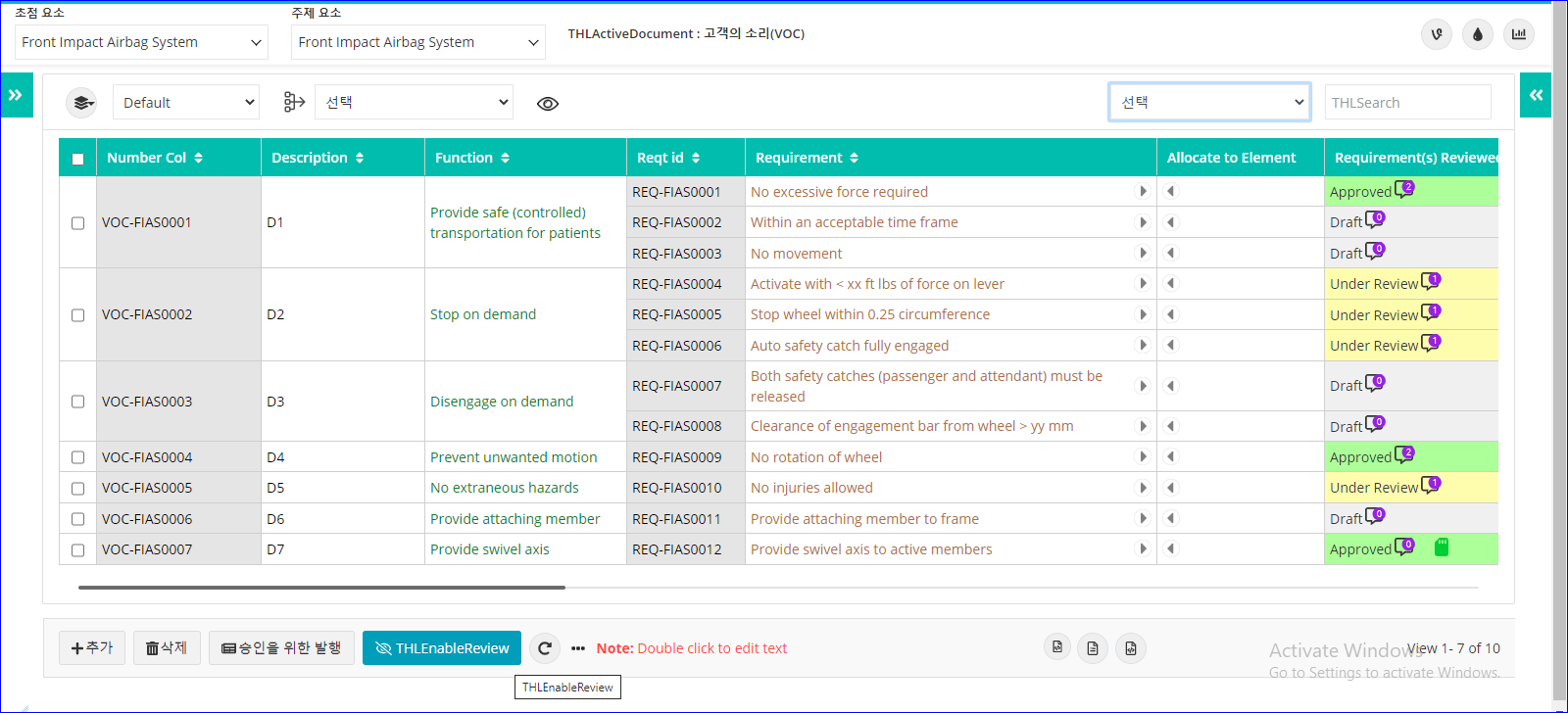
**Requirements Reviewed**



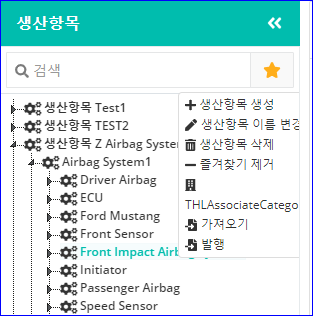




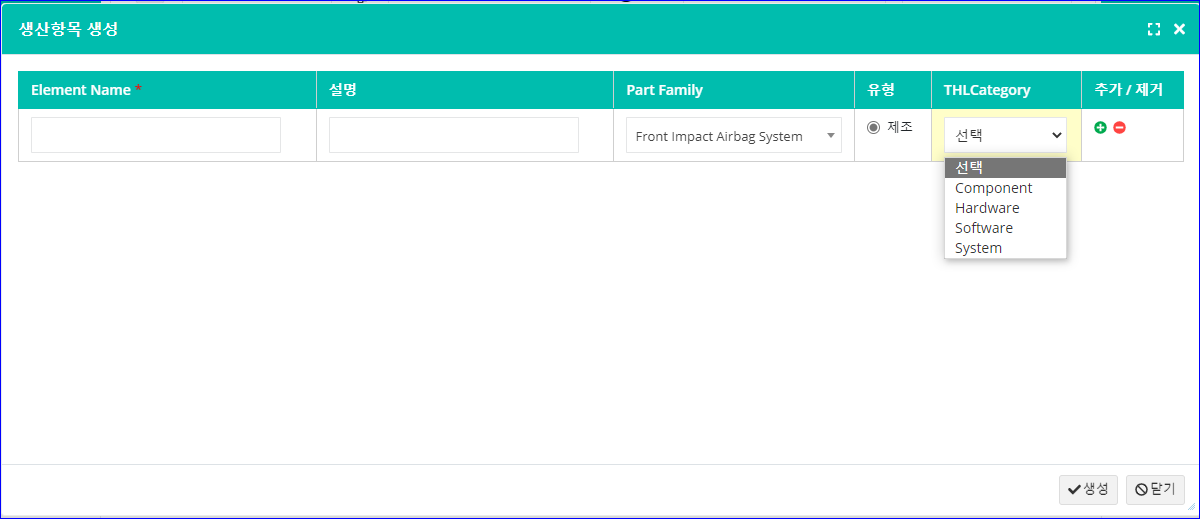
**VOC**



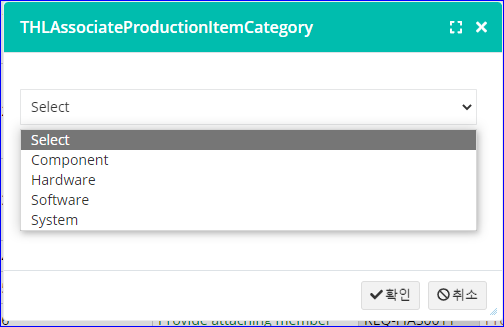
**PI tree**



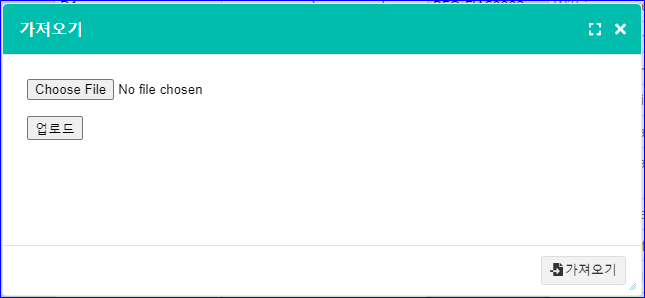
**Create PI**



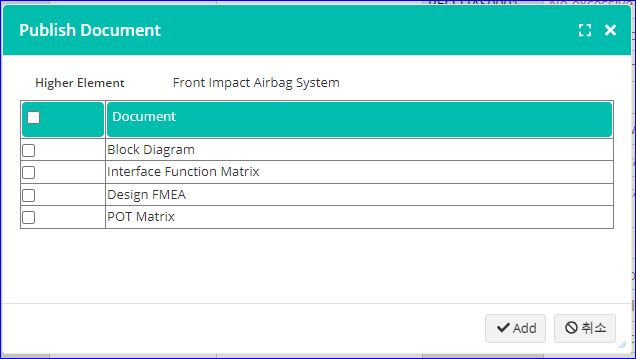
**Associate category**



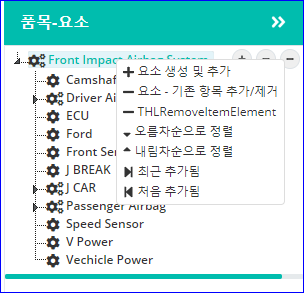
**Import**



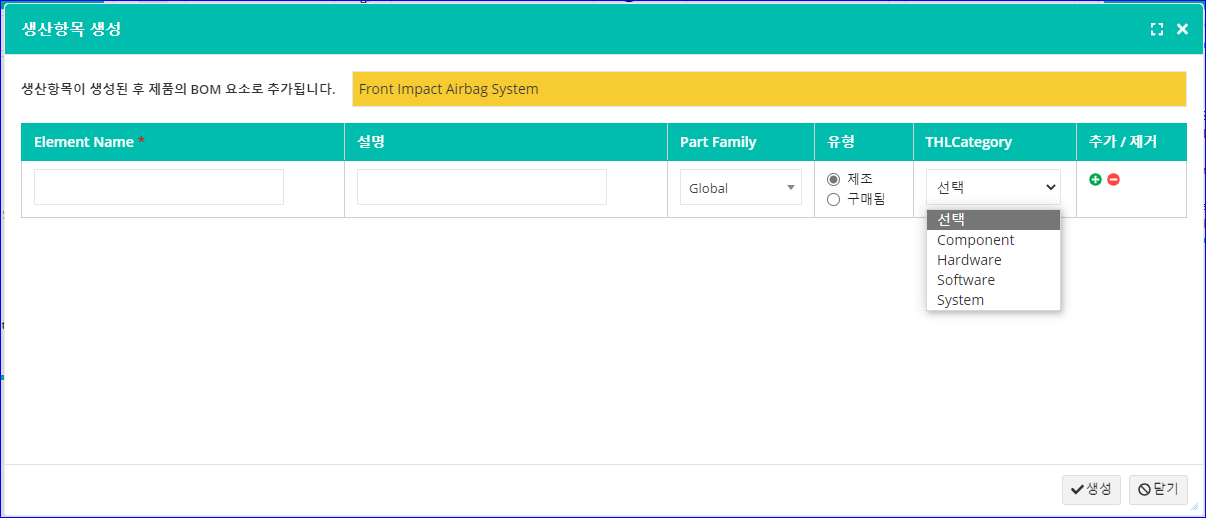
**Publish Documents**



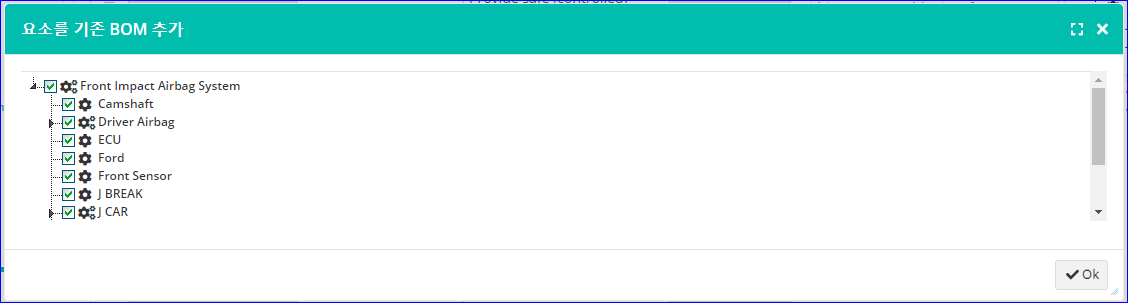
**BOM tree**



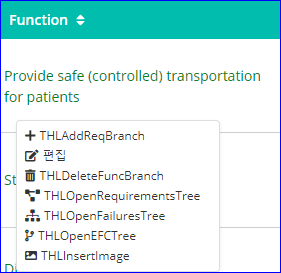
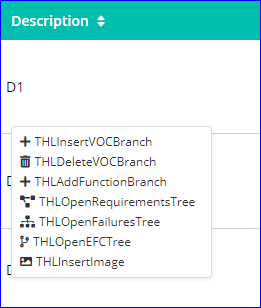
**Create and Add element**



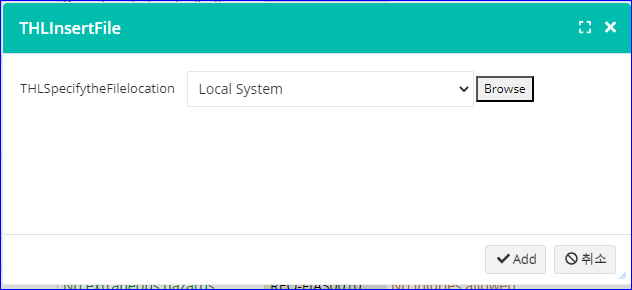
**Remove Existing element**



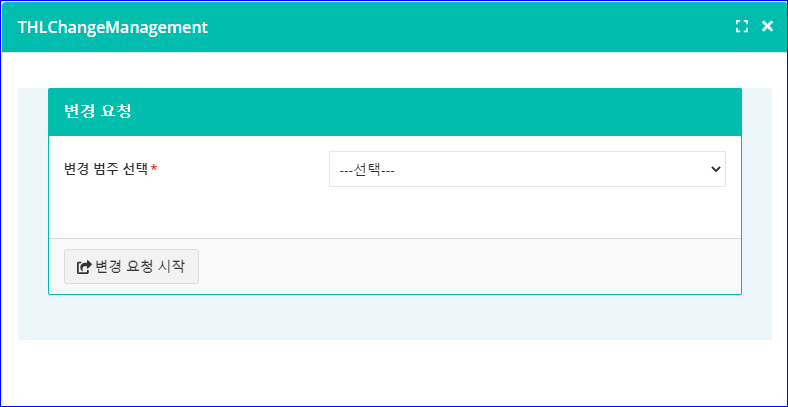
**All the context menus have THL issue**



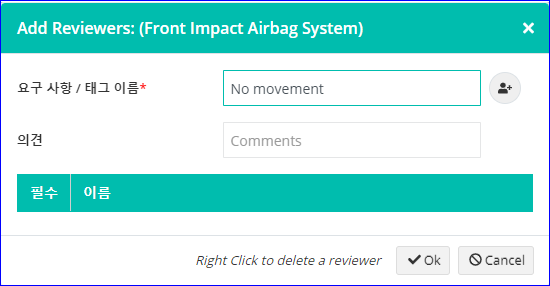
**Insert Image**



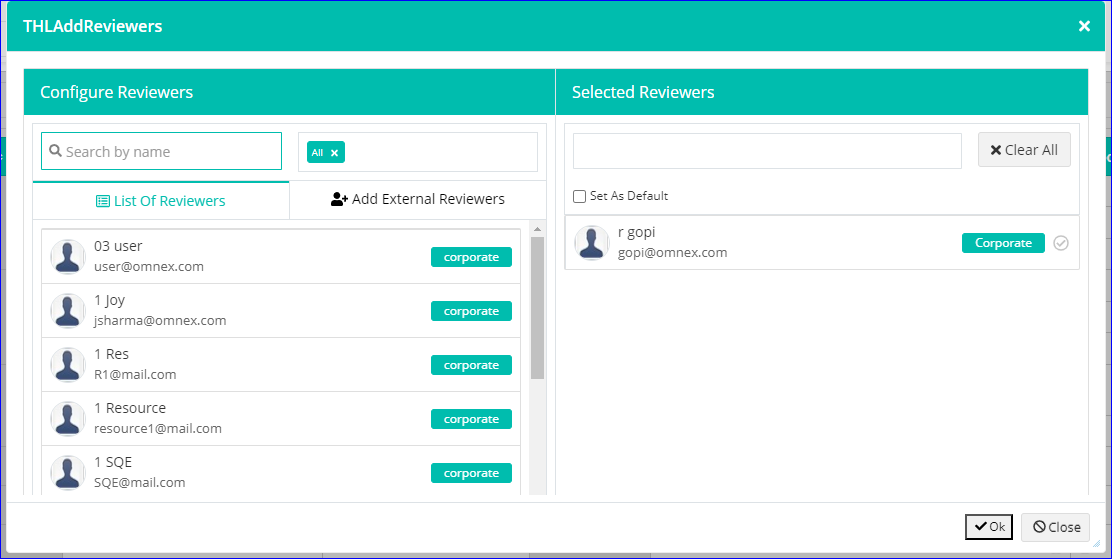
**Initiate Change Request**



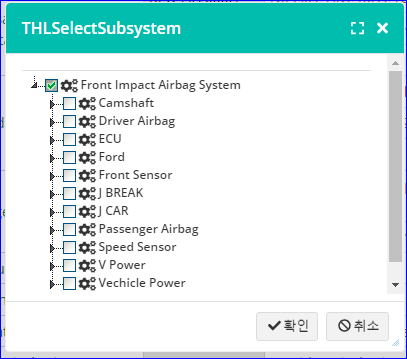
**Send For Review**



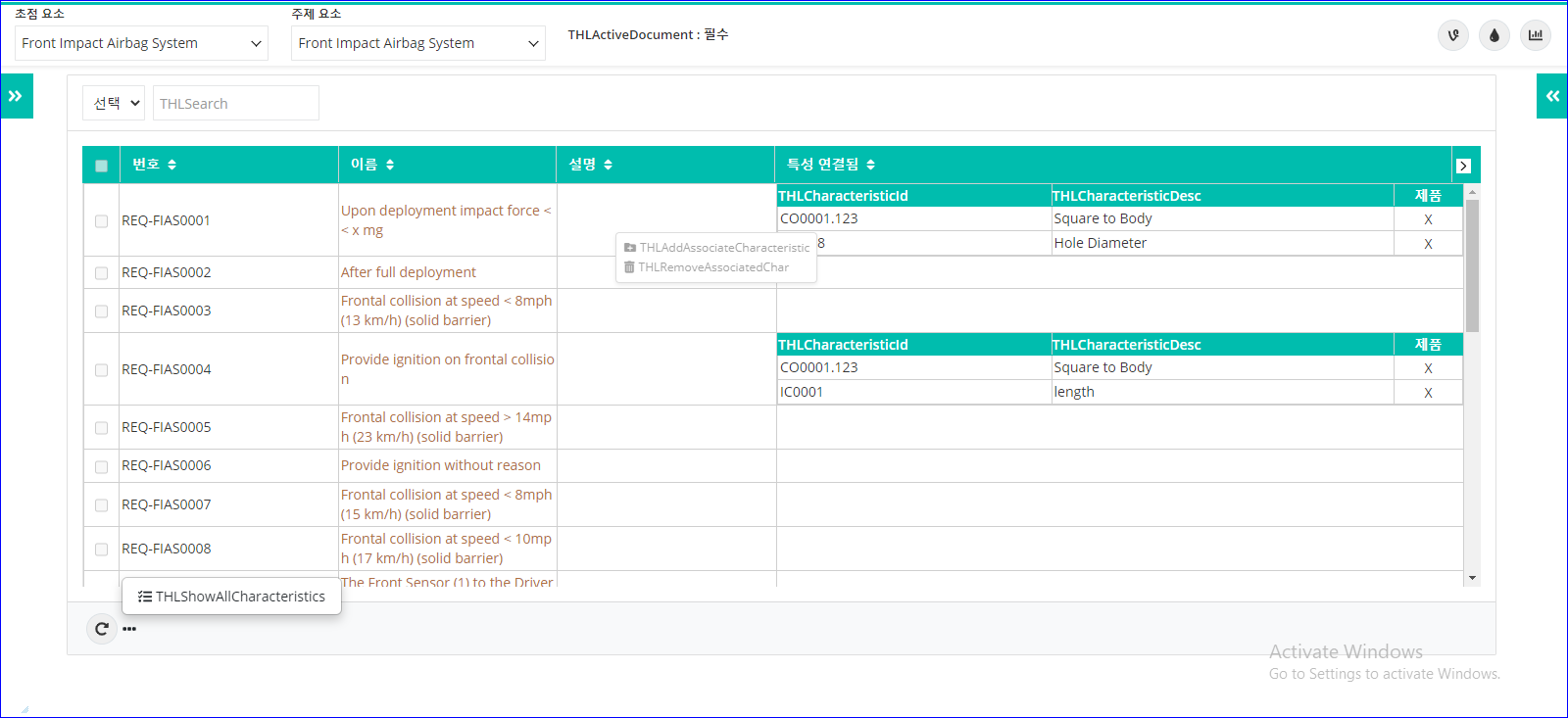
**Add Reviewers**



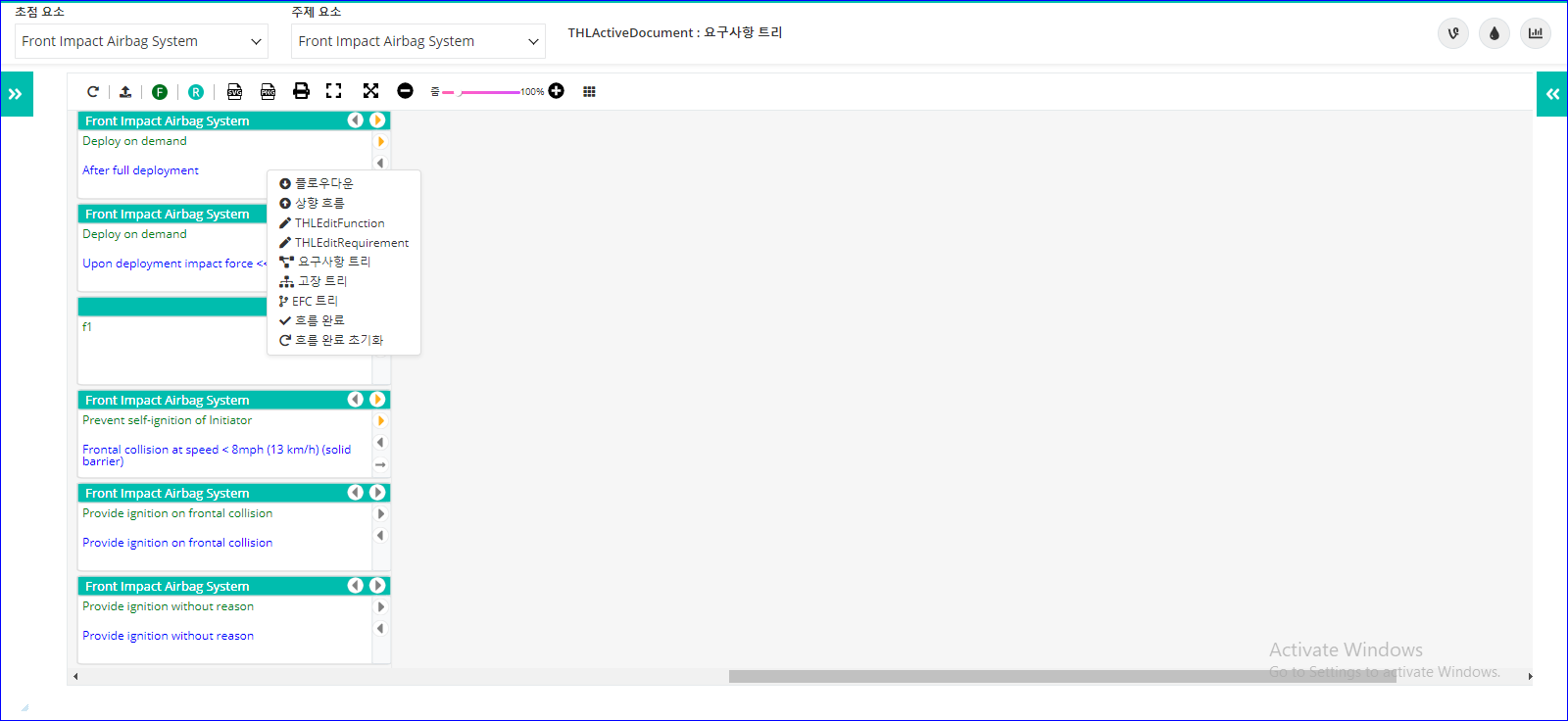
**Move to subsystem**



**Requirements**



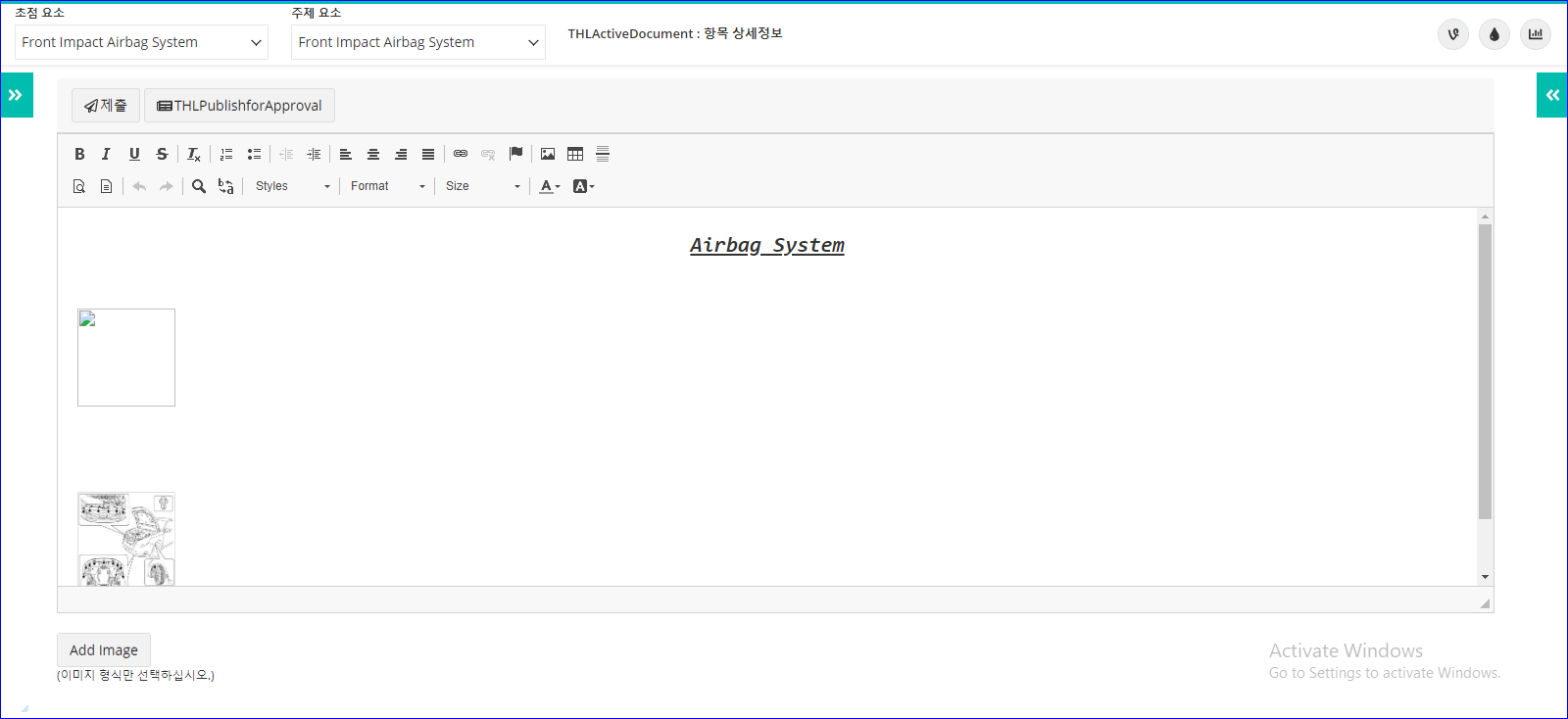
**Requirements tree**

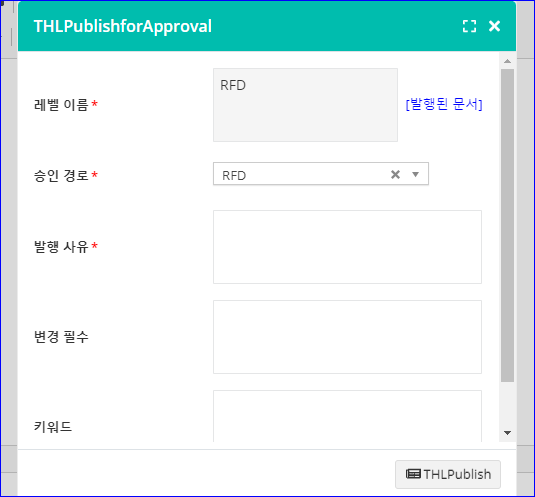


**Risk analysis**

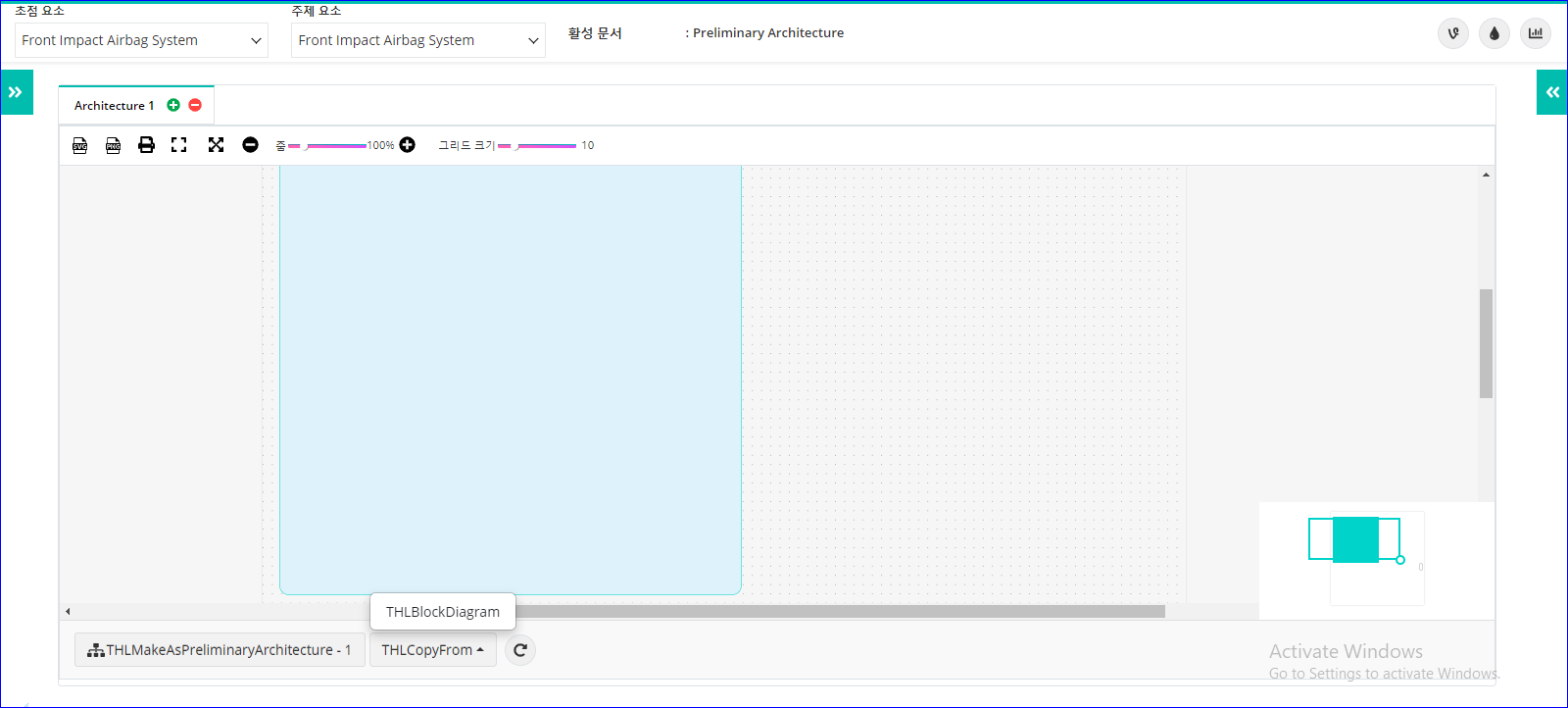


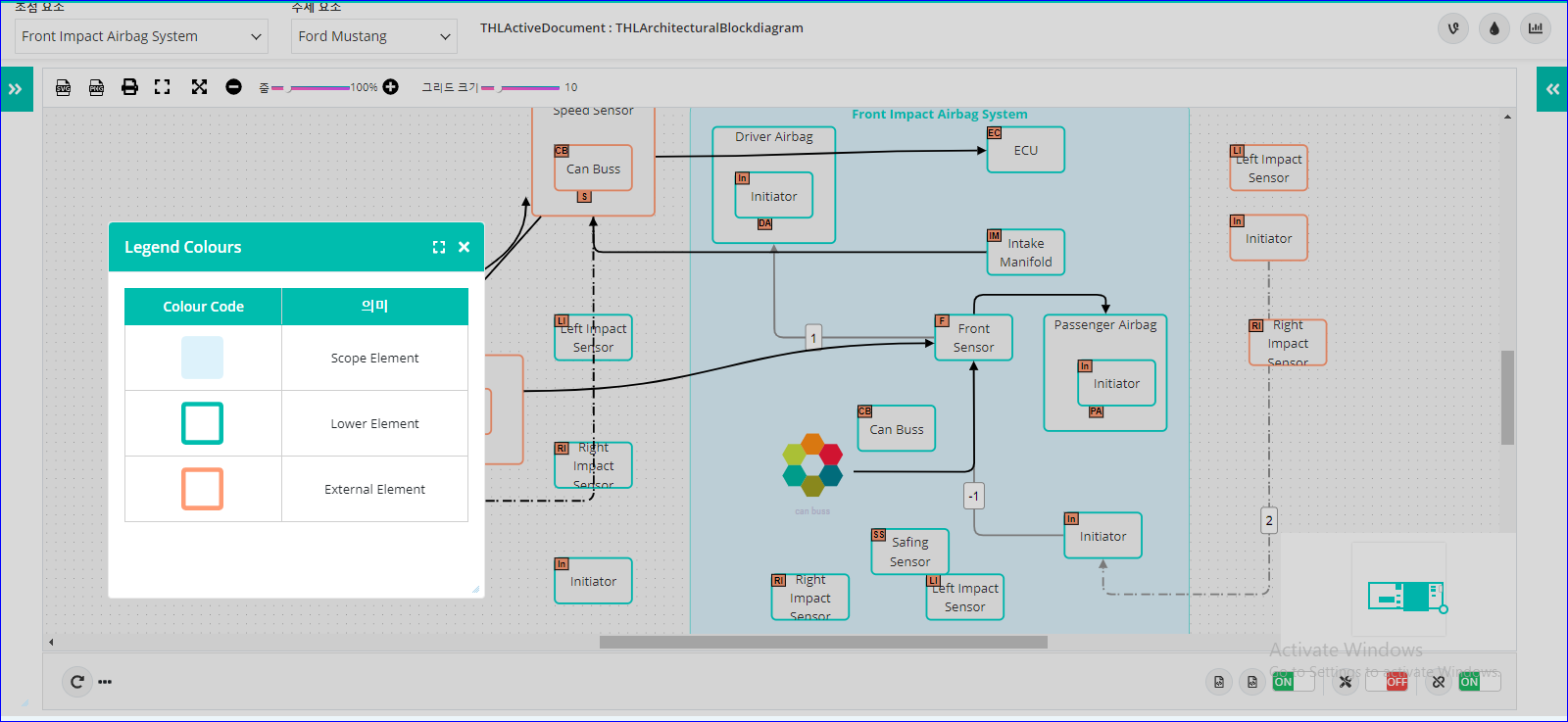
**Impact Analysis & Item Details**

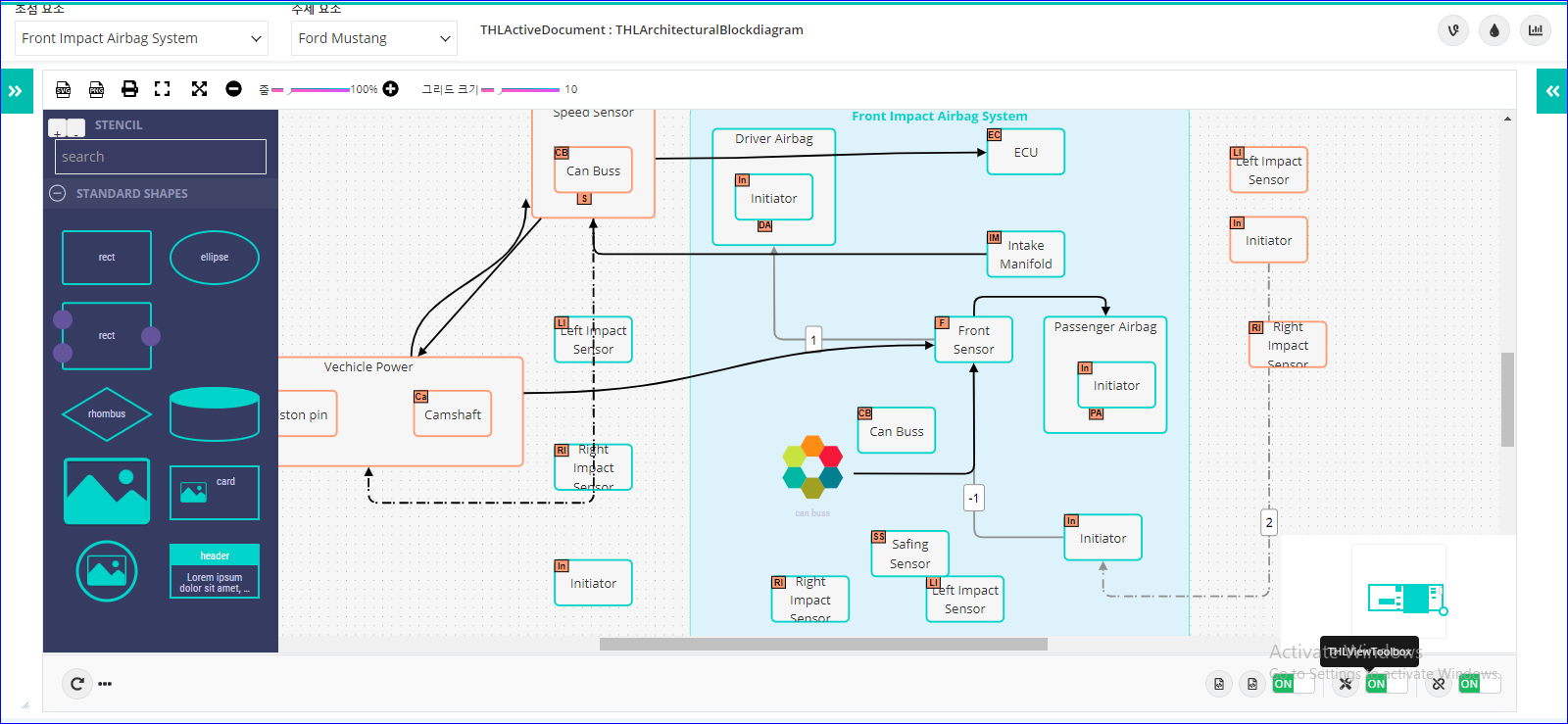


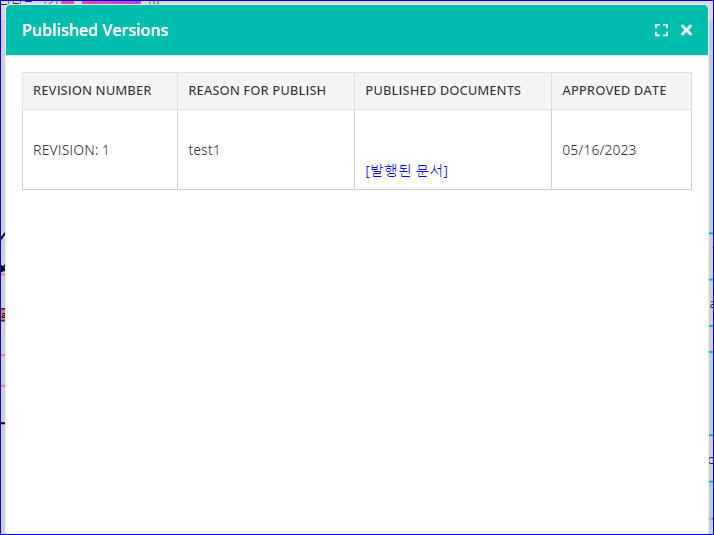


**Preliminary Architecturee**

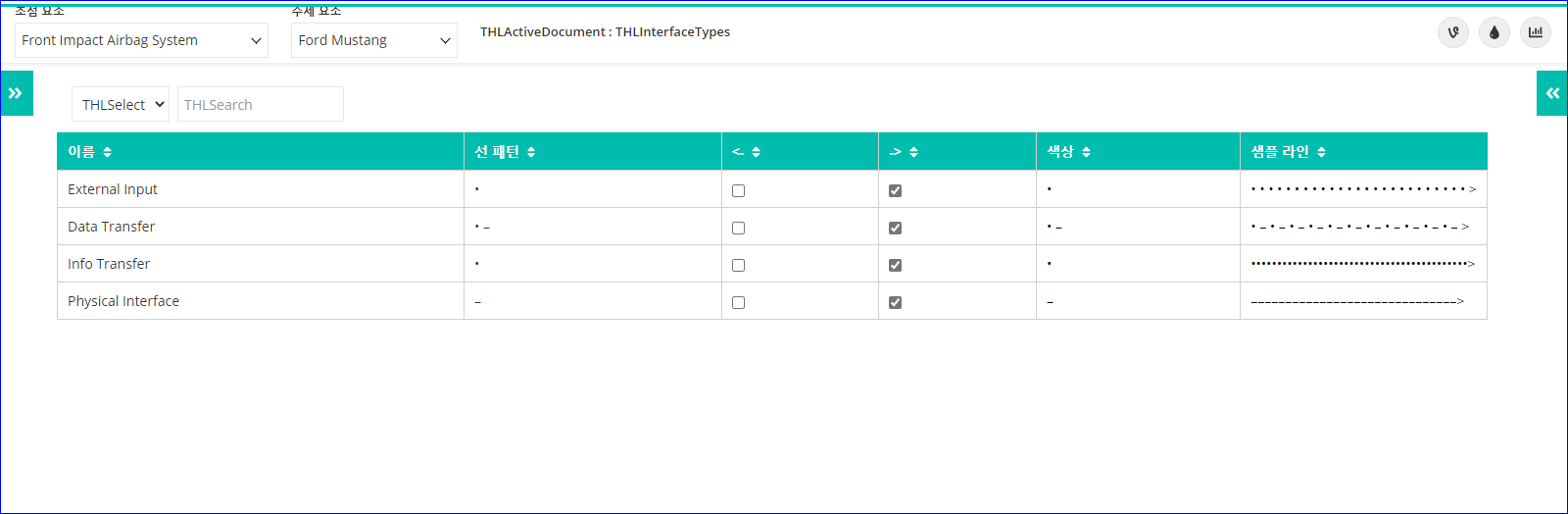


**Block Diagram** 

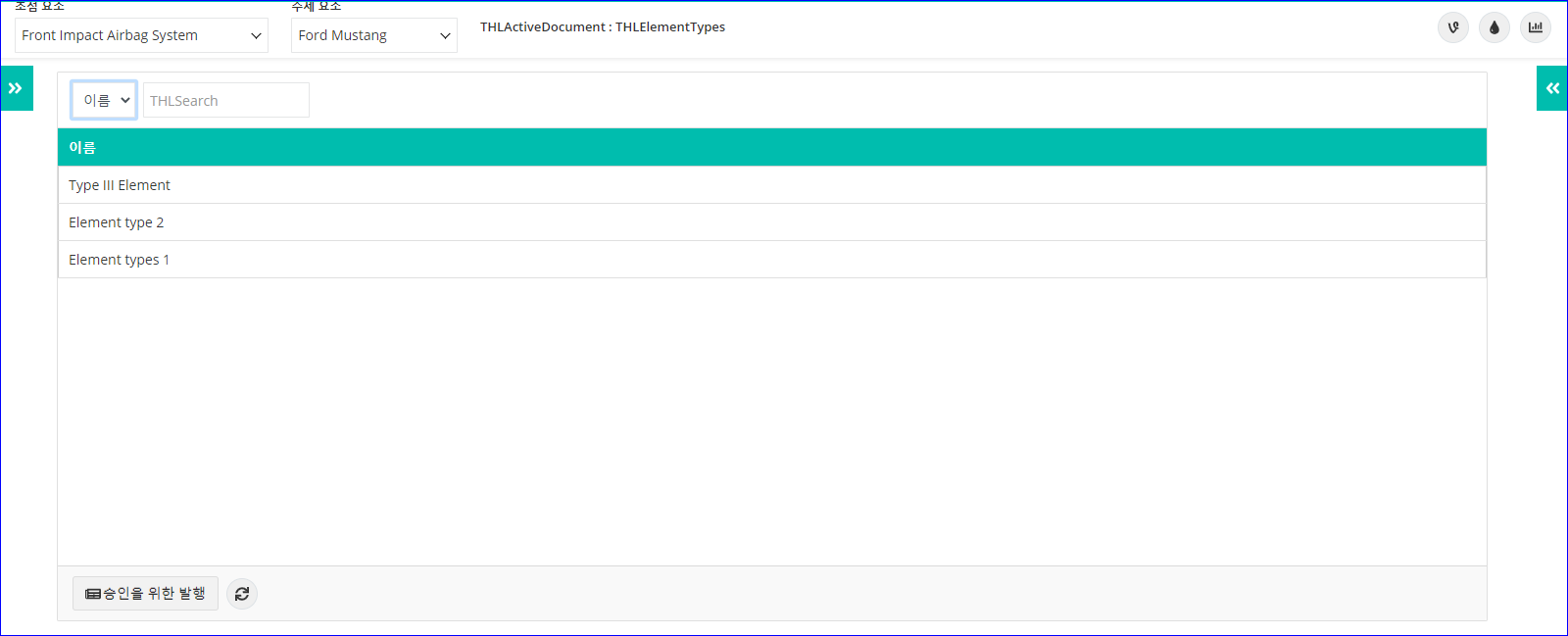




**Interface Type**



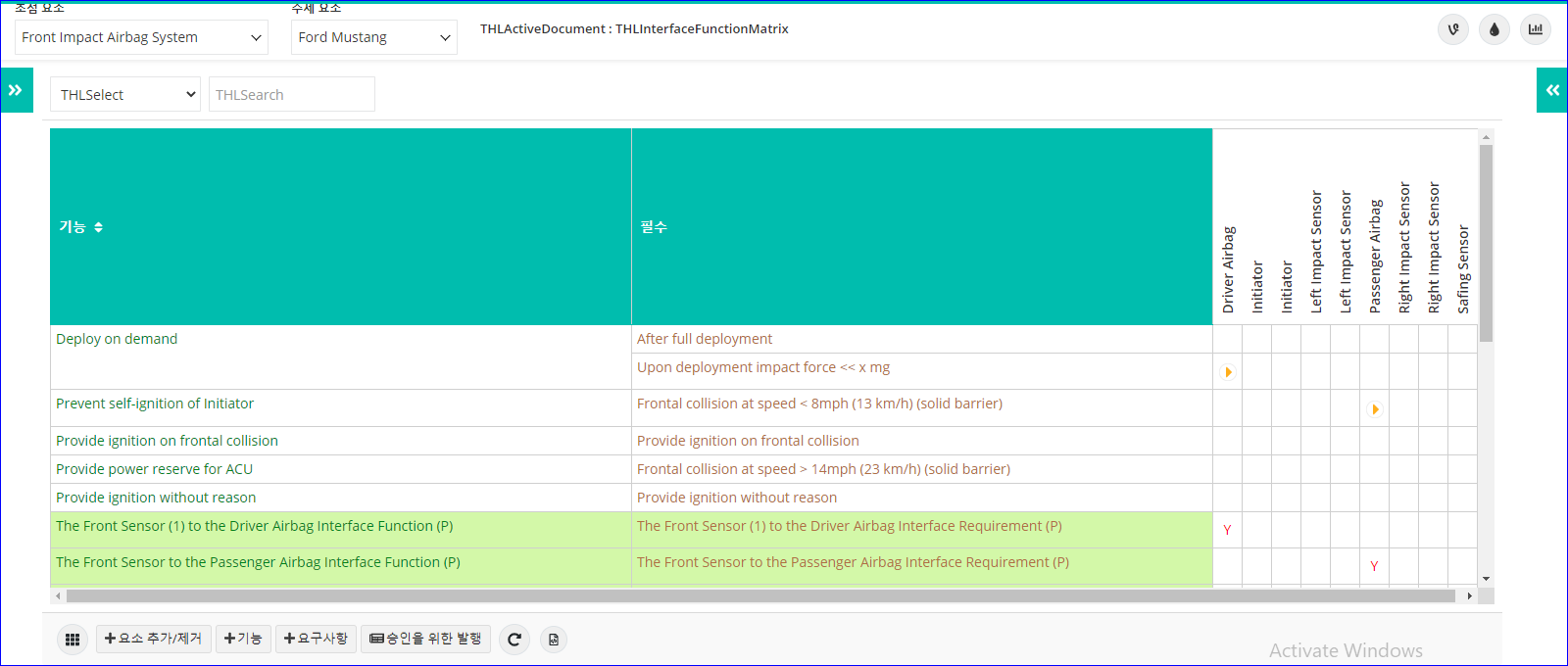
**Element Type**



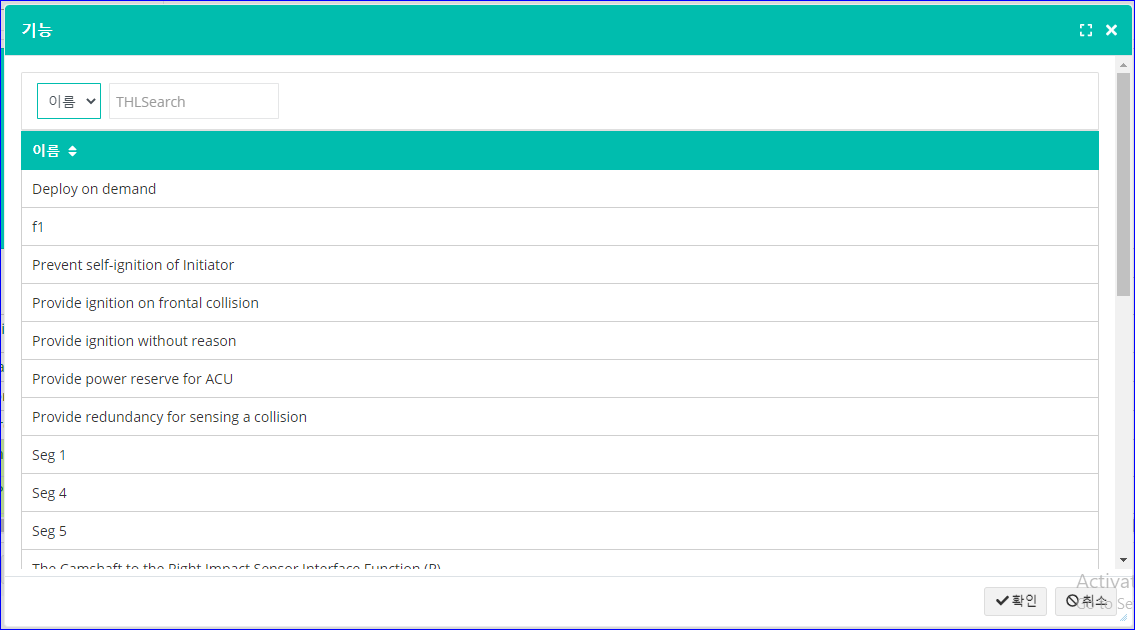
**Item boundaries**

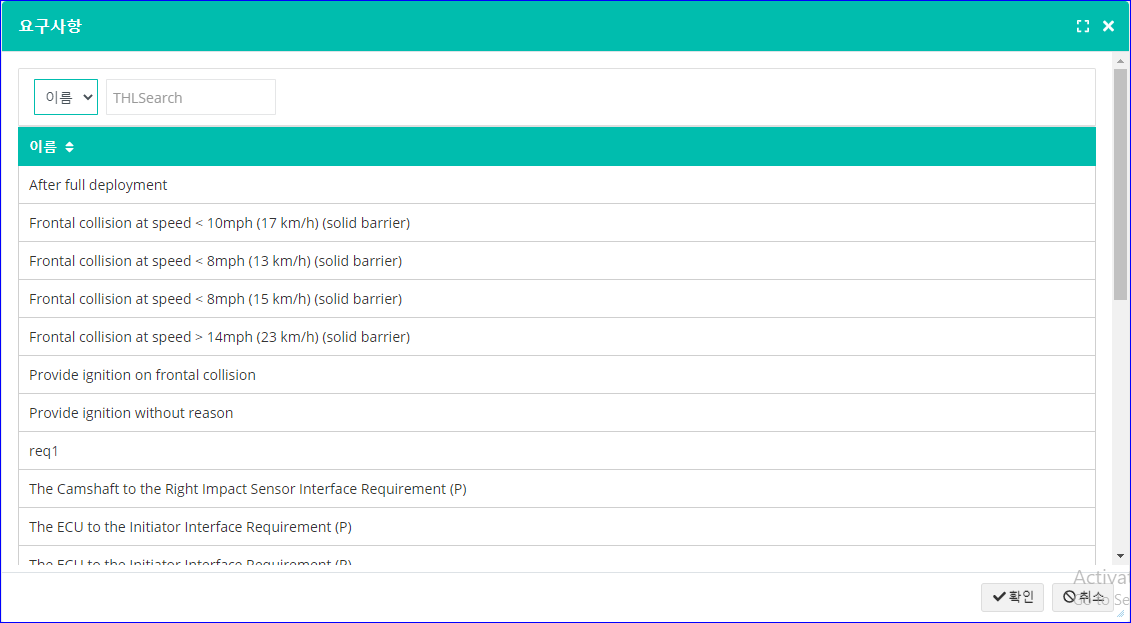


**IFM**

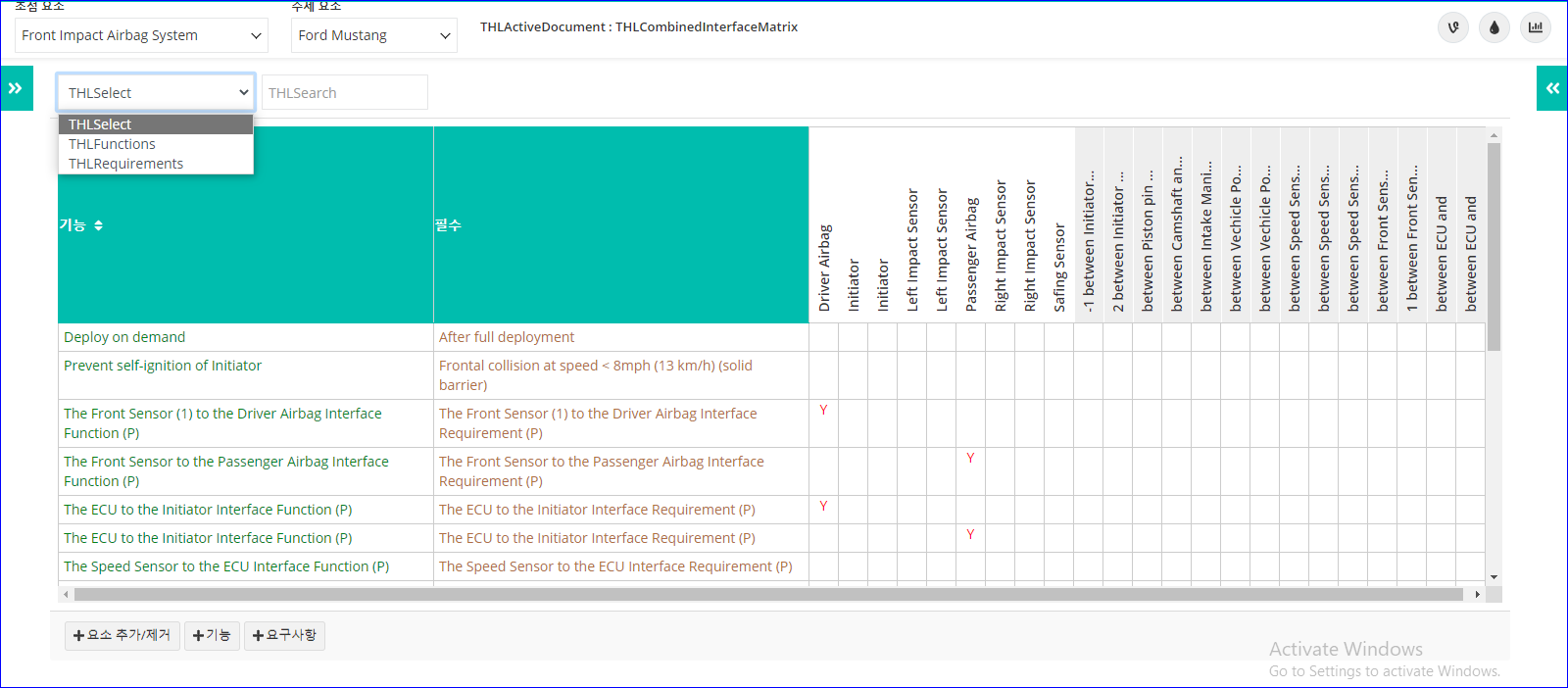




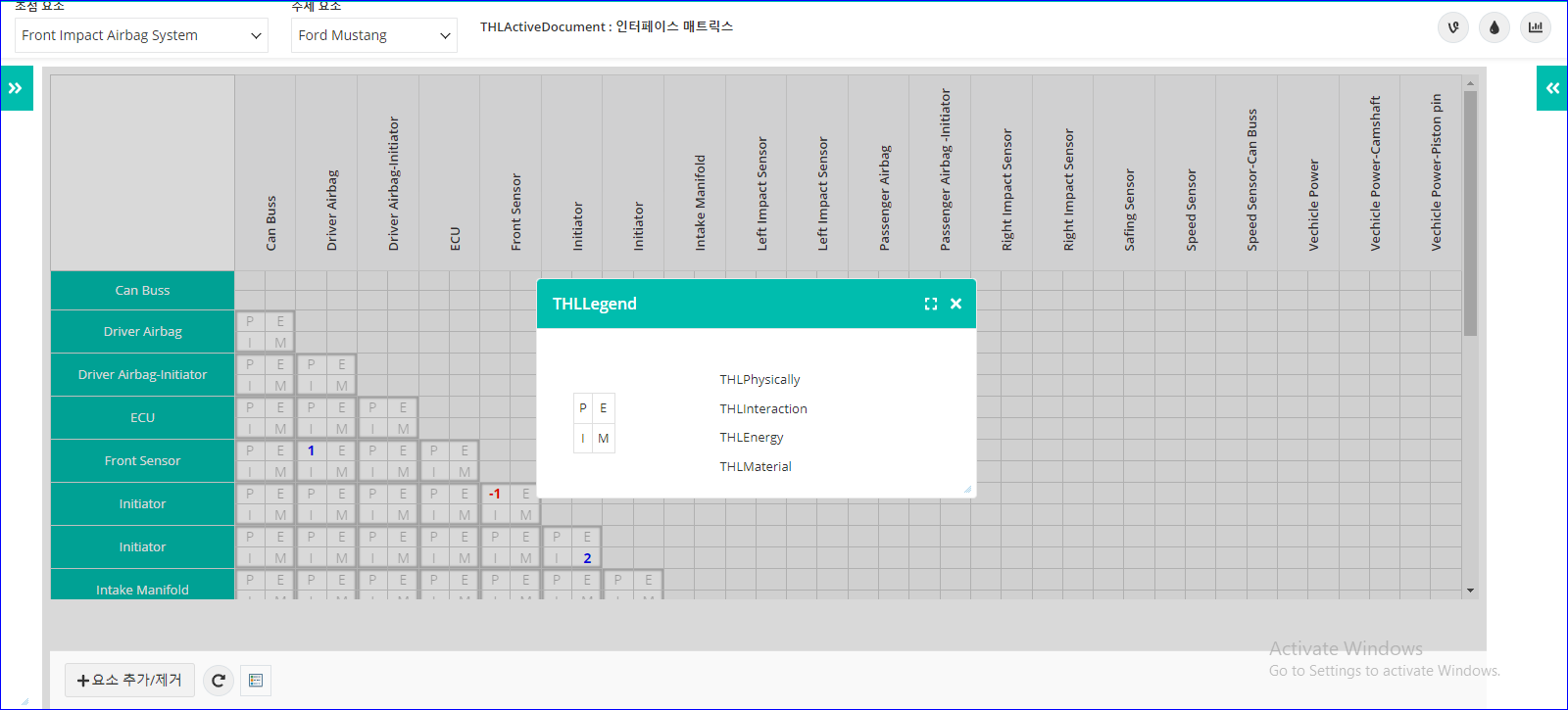




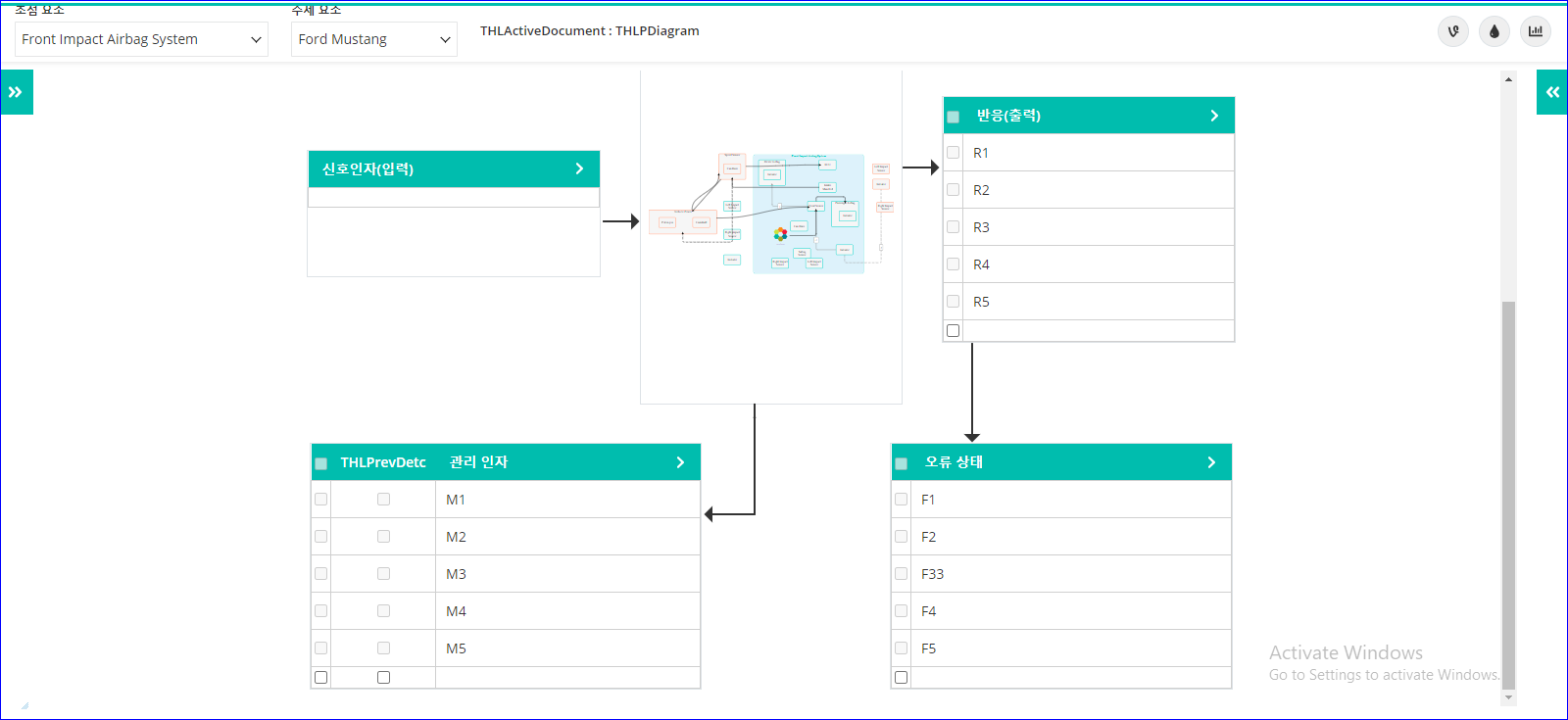
**CIM**



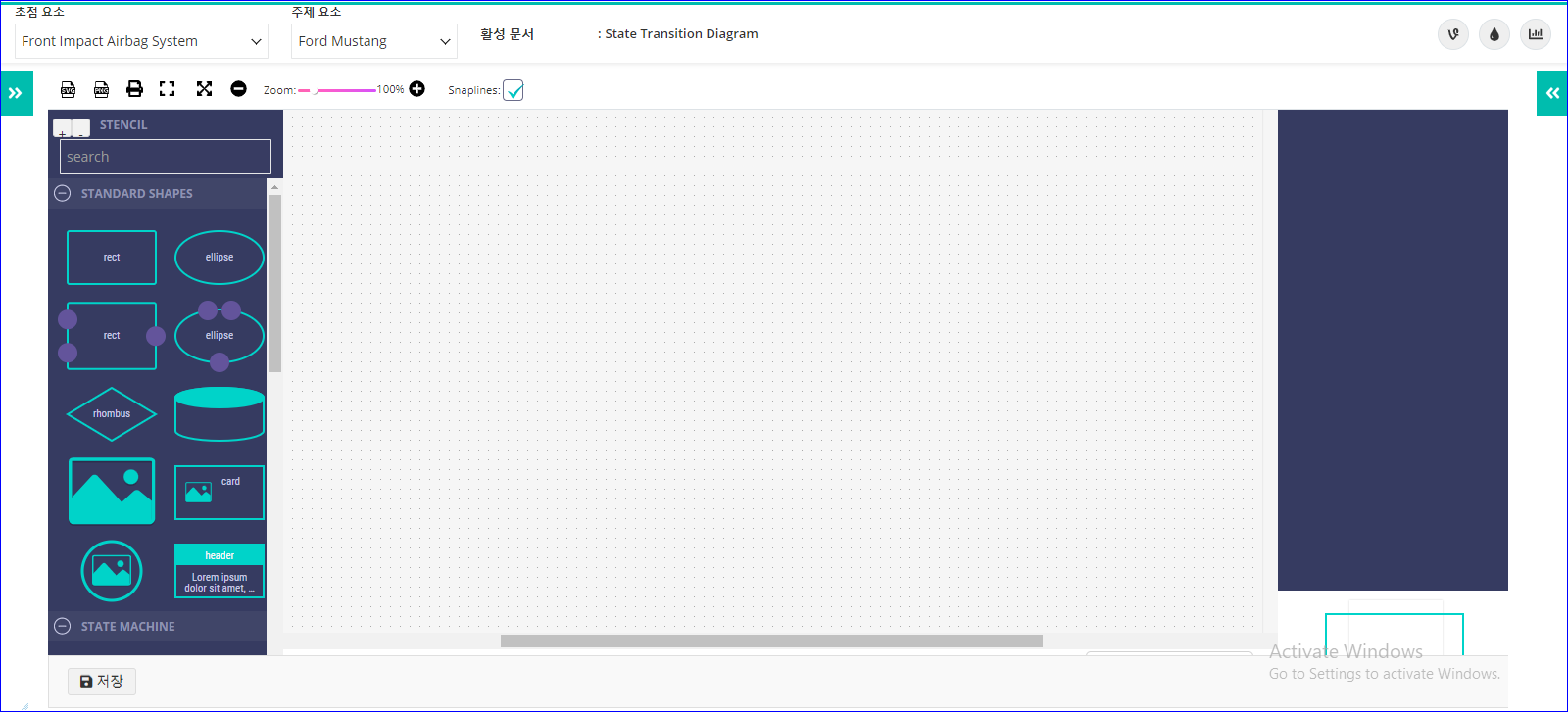
**Interface Matrix**



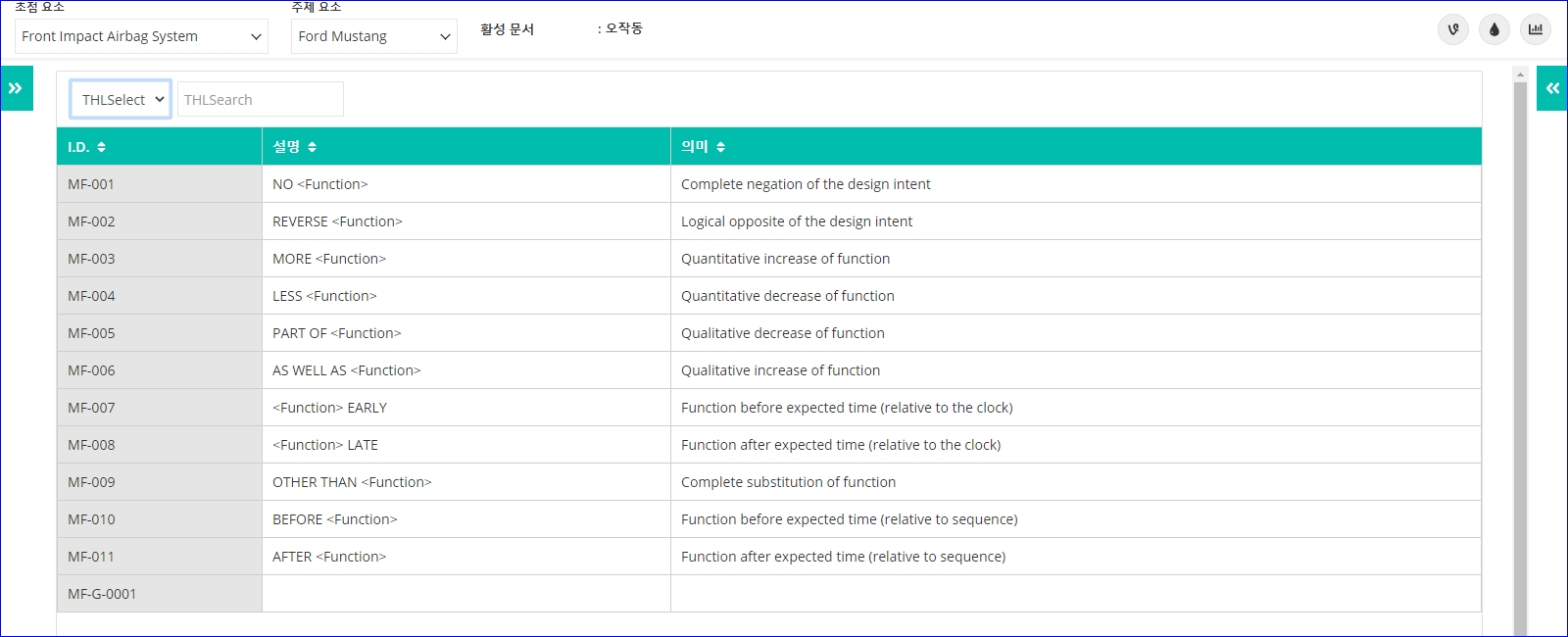
**P-Diagram**



**State Transition Diagram**



**Malfunction**

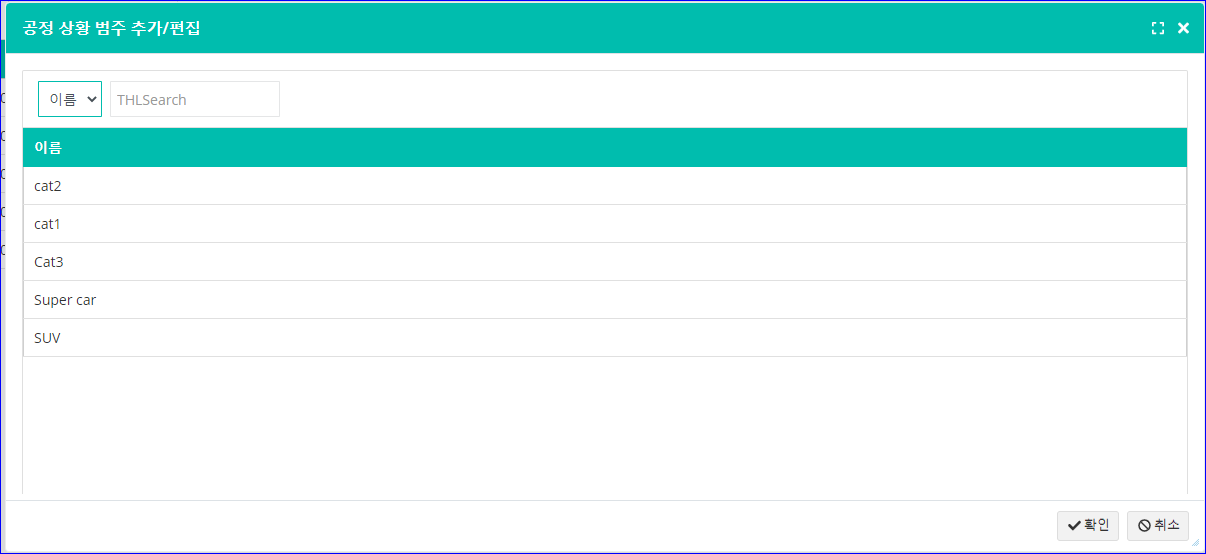


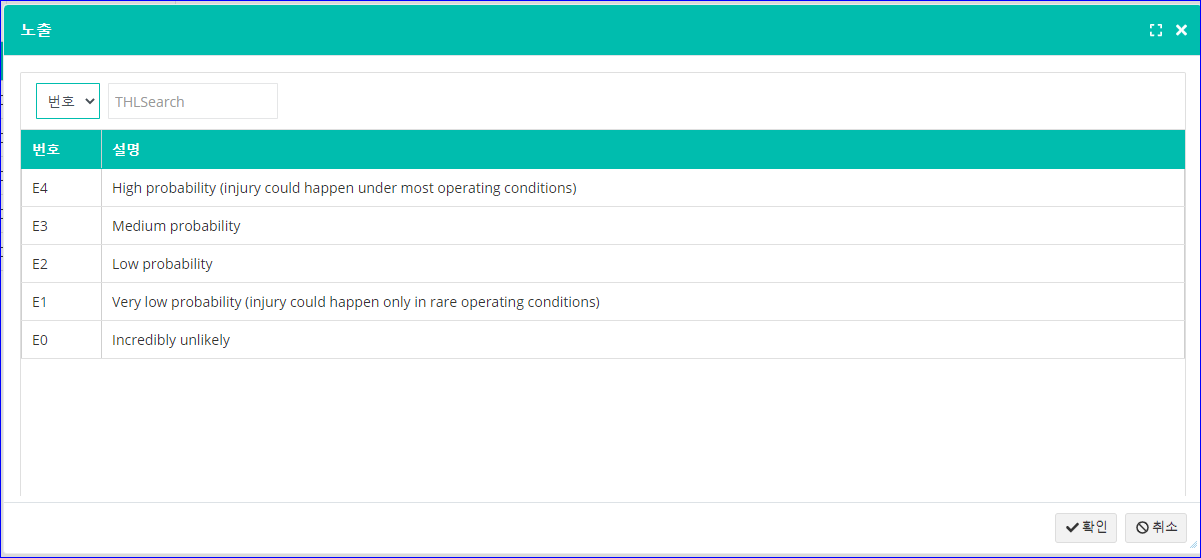
**Functional Analysis**



**Operational situation**

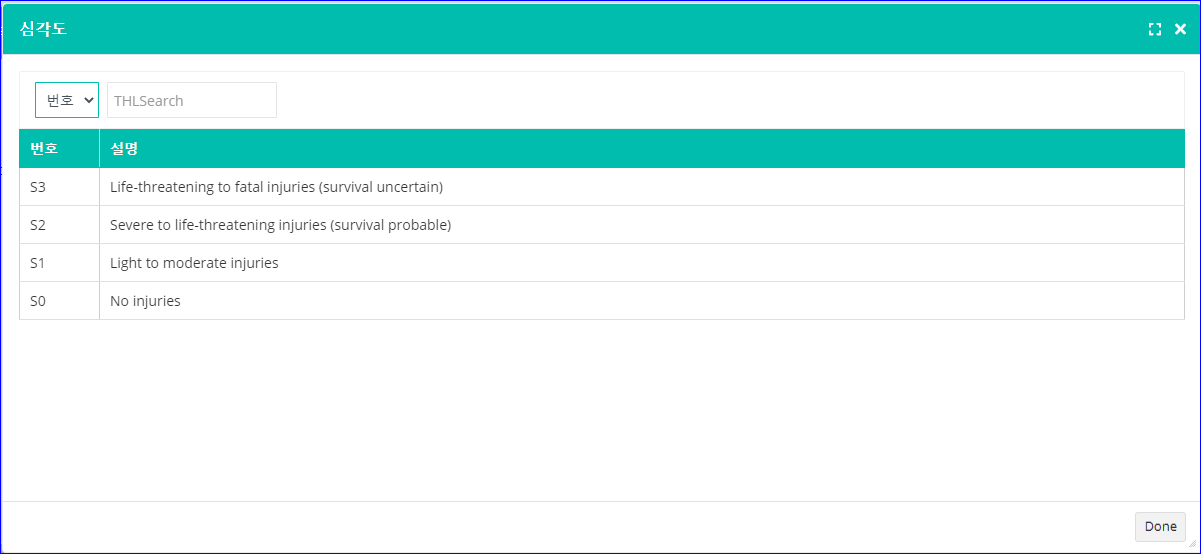


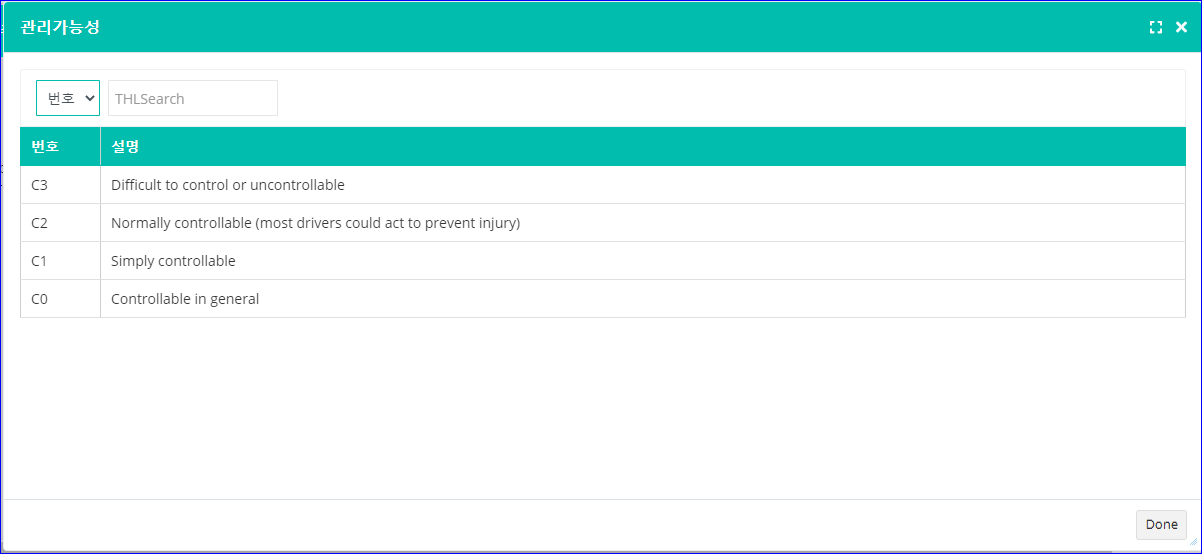




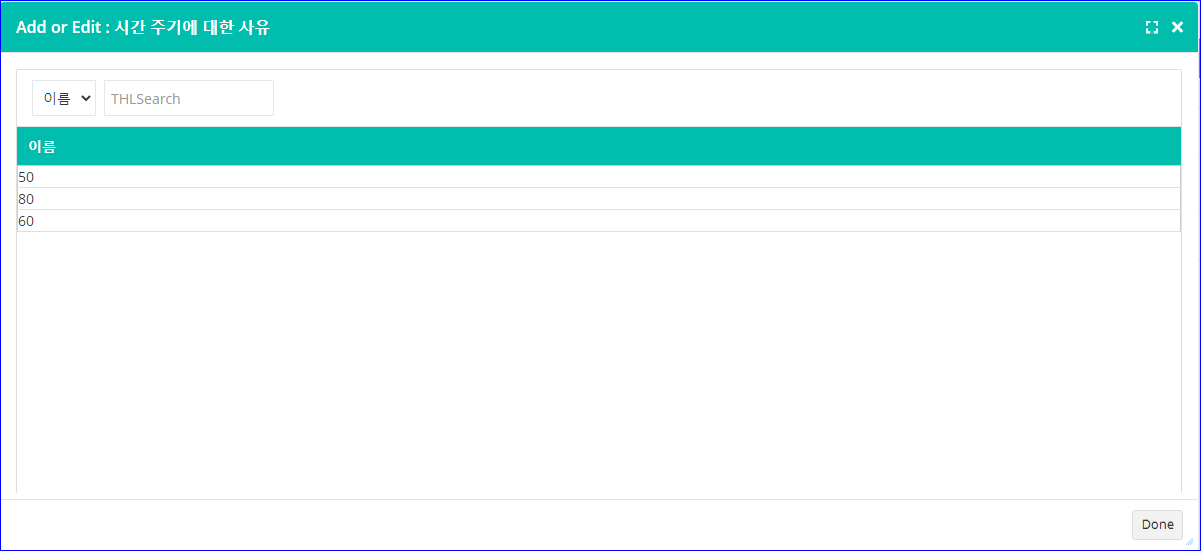
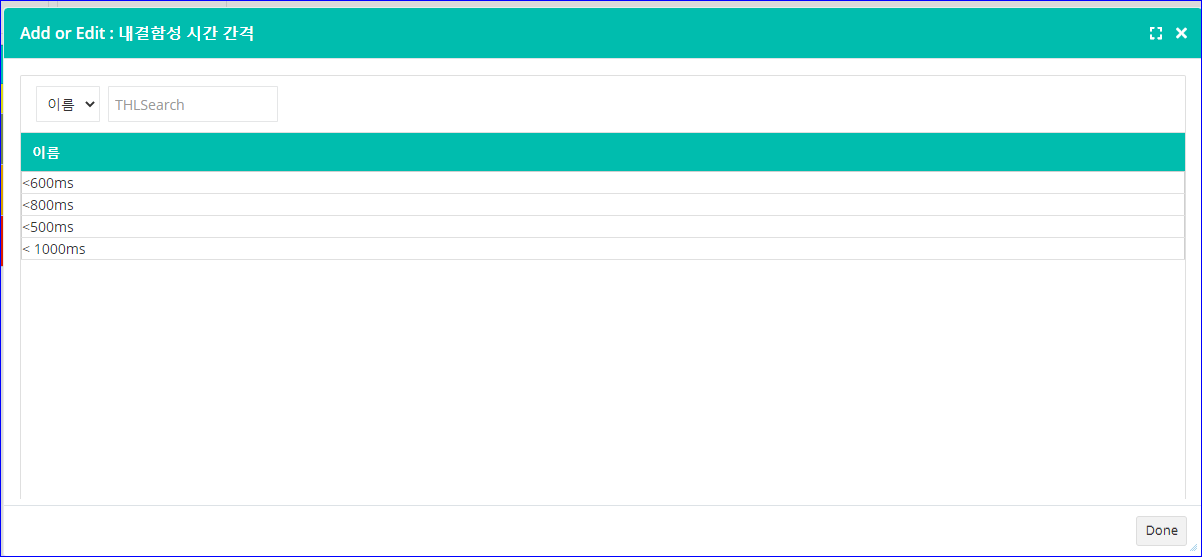
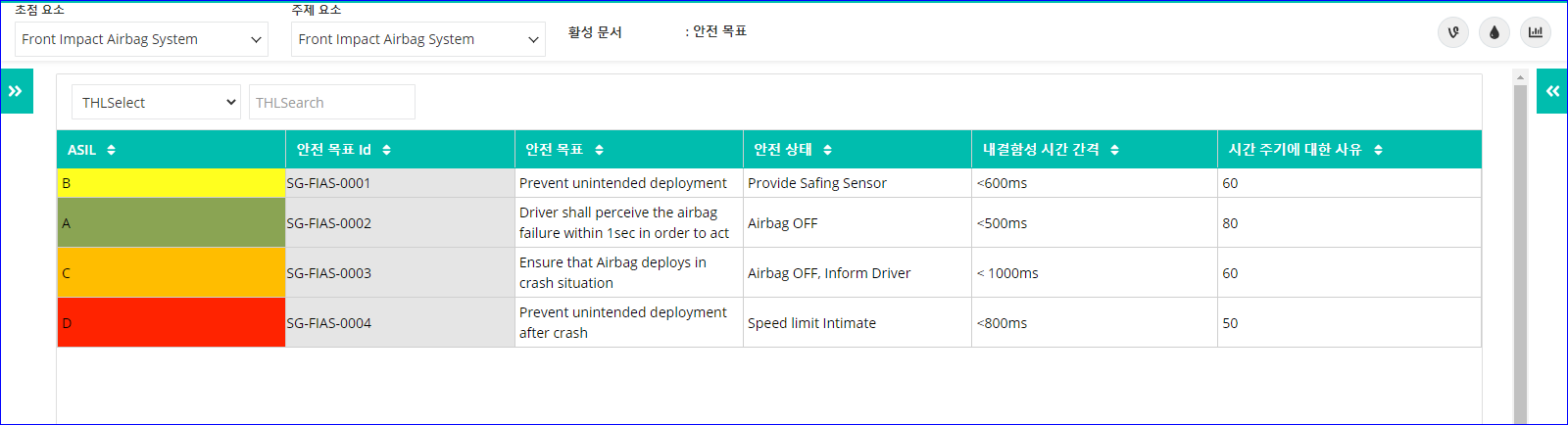
**HARA**







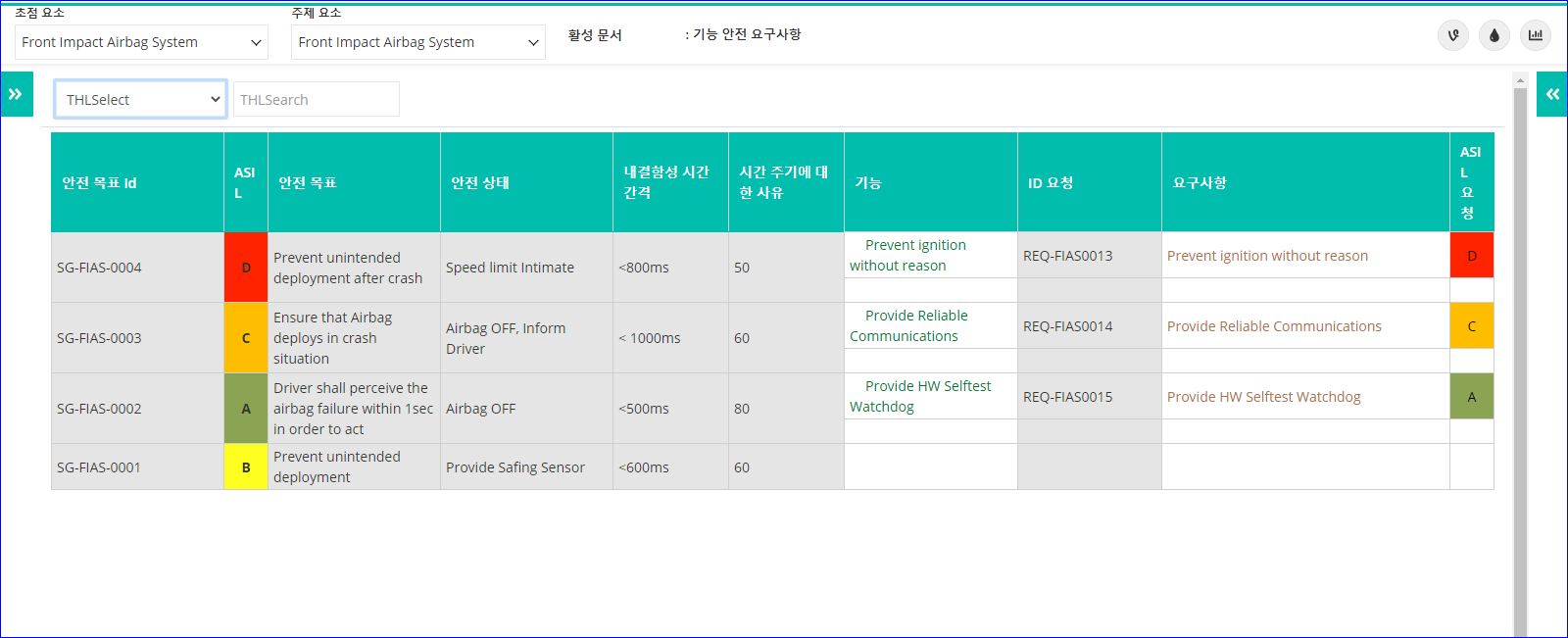
**Safety goal**

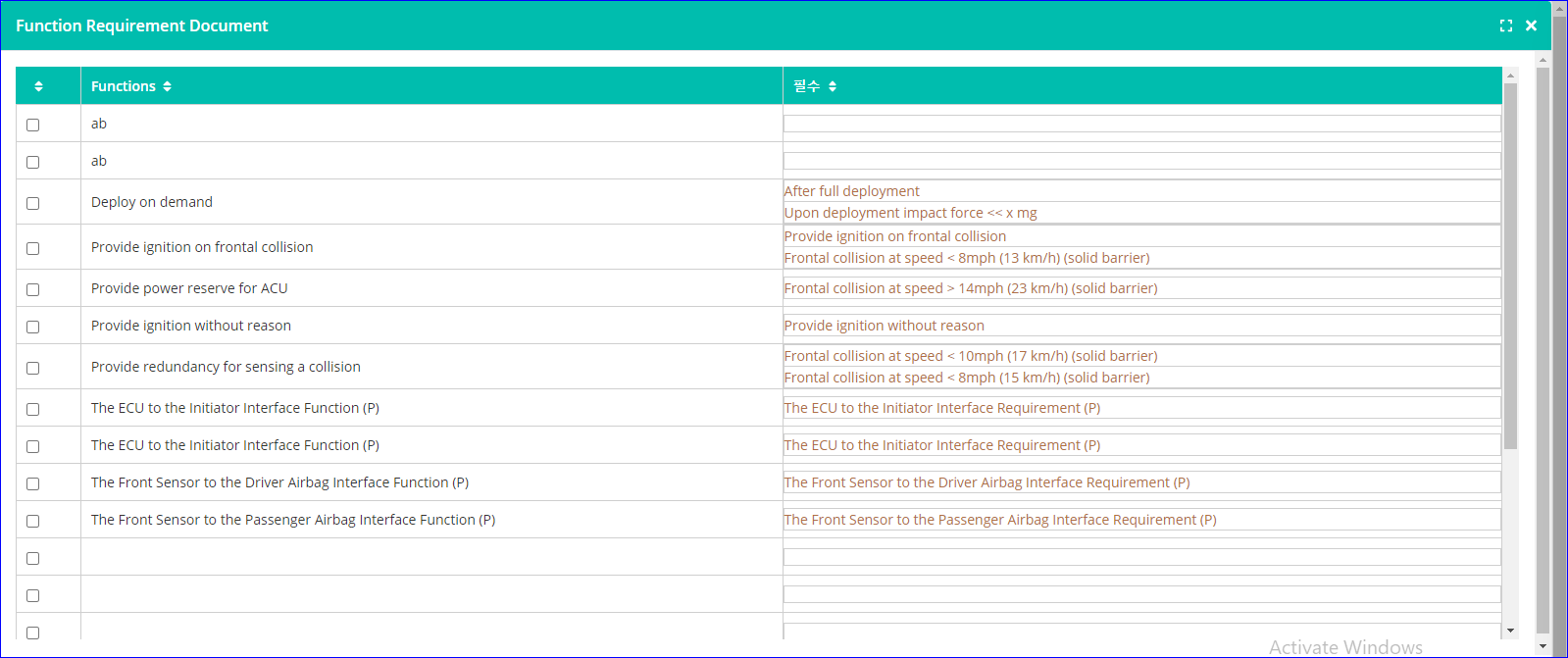


**Safe State**

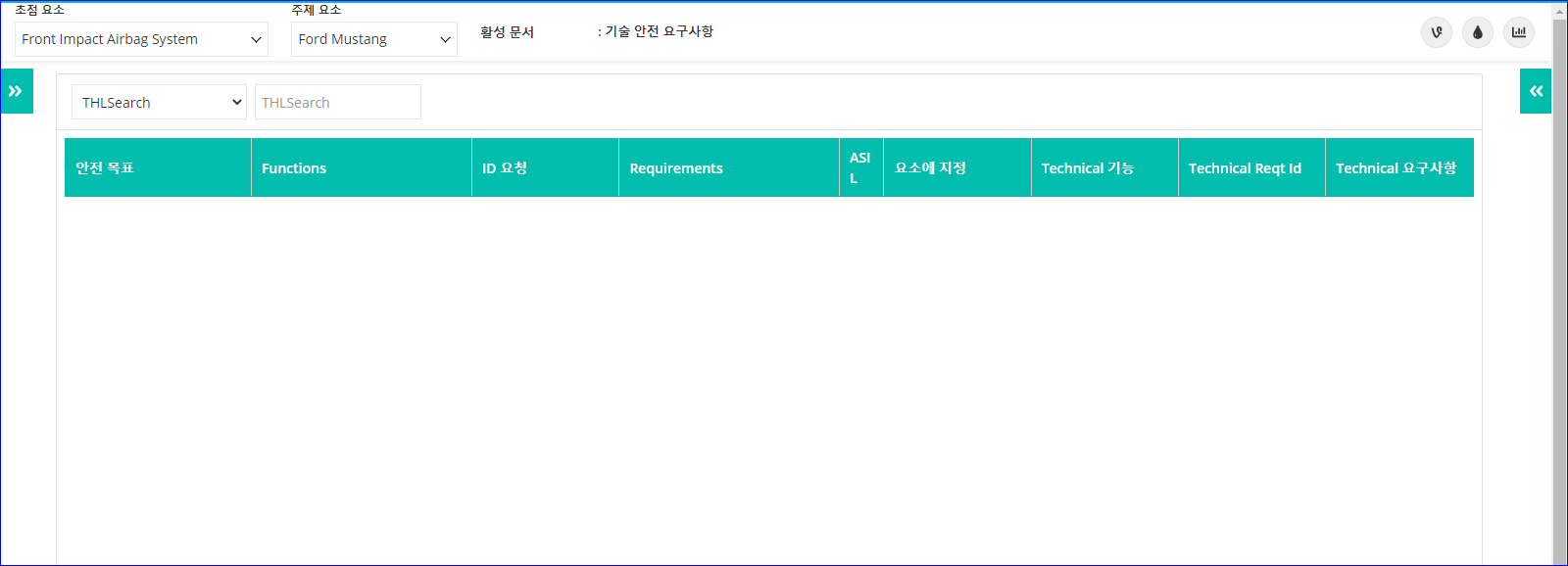


**FSR**

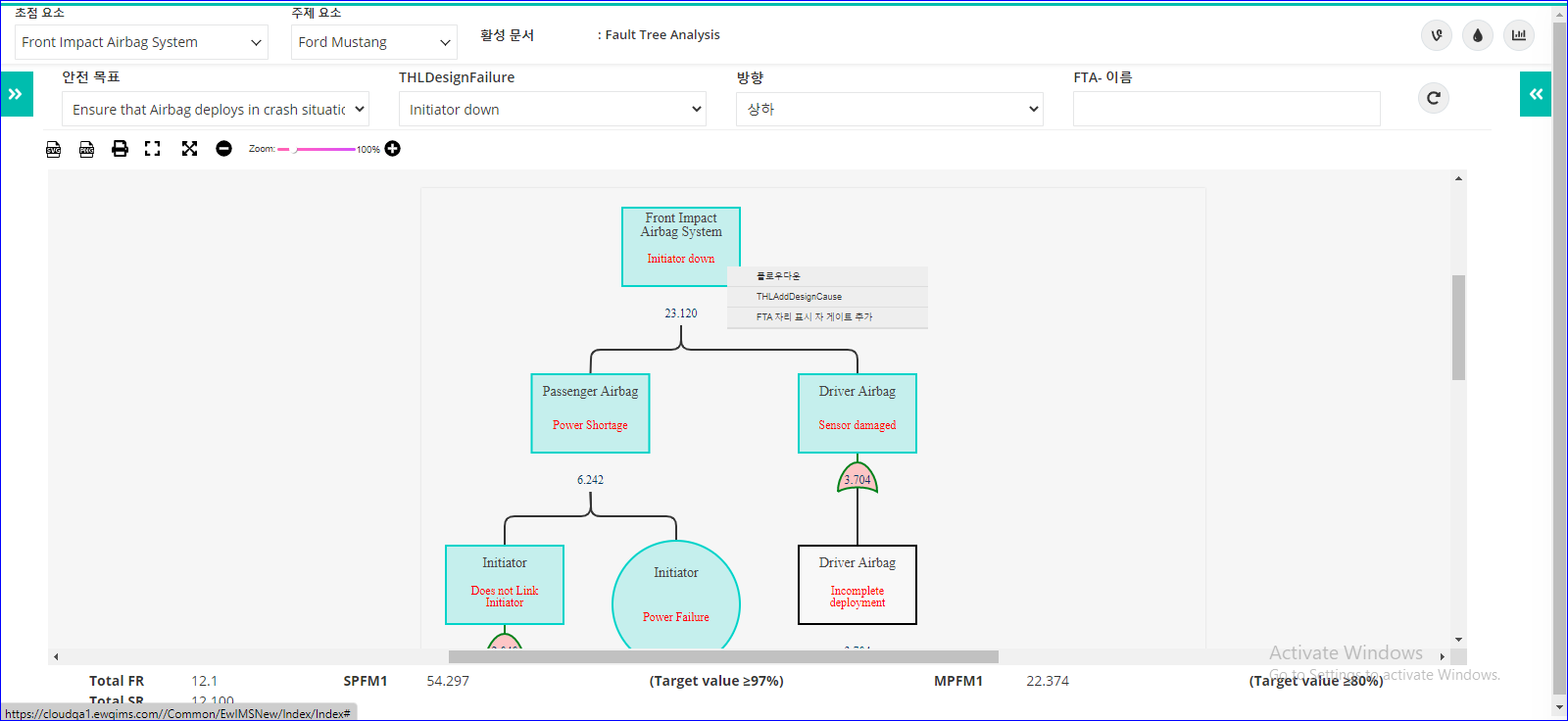


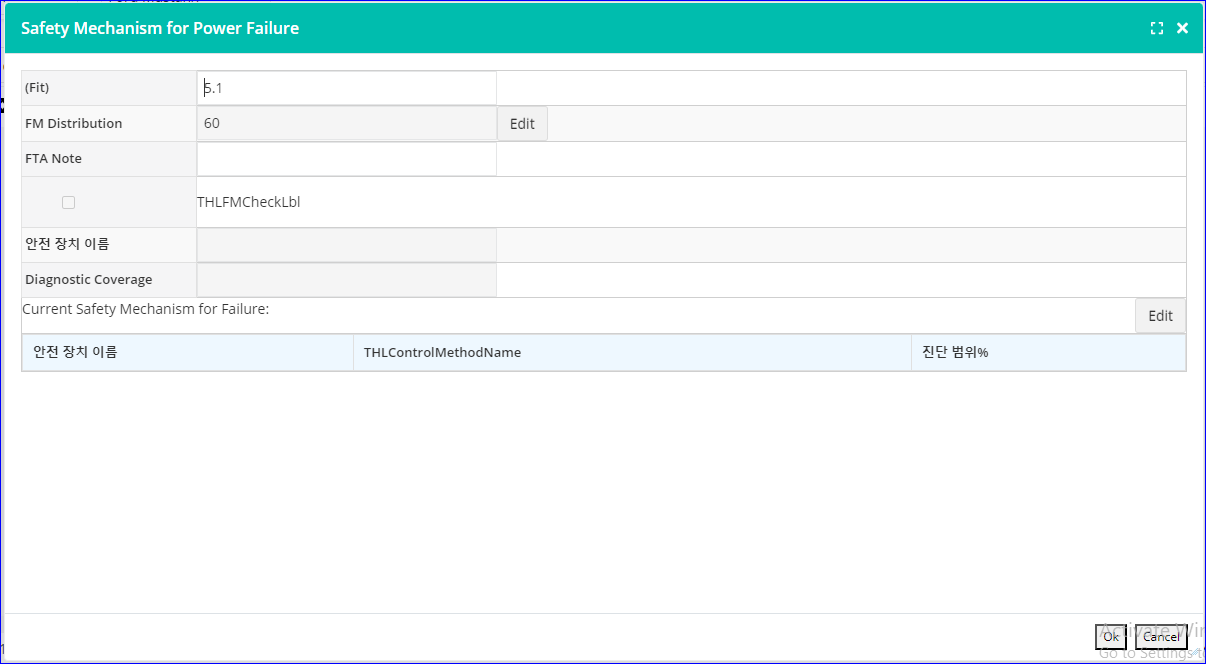
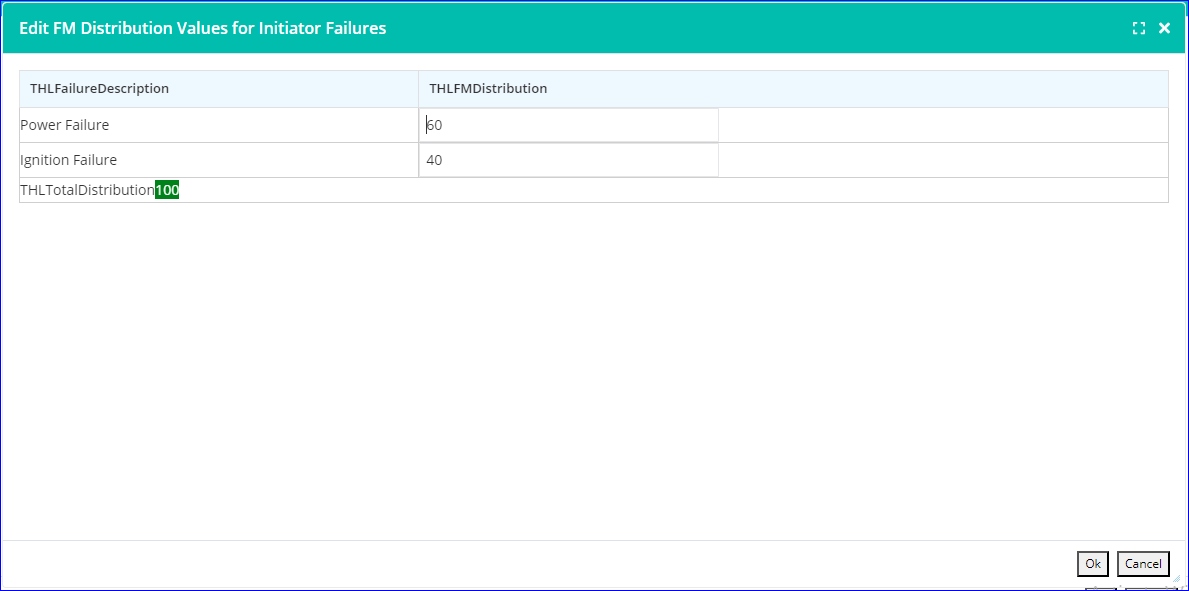
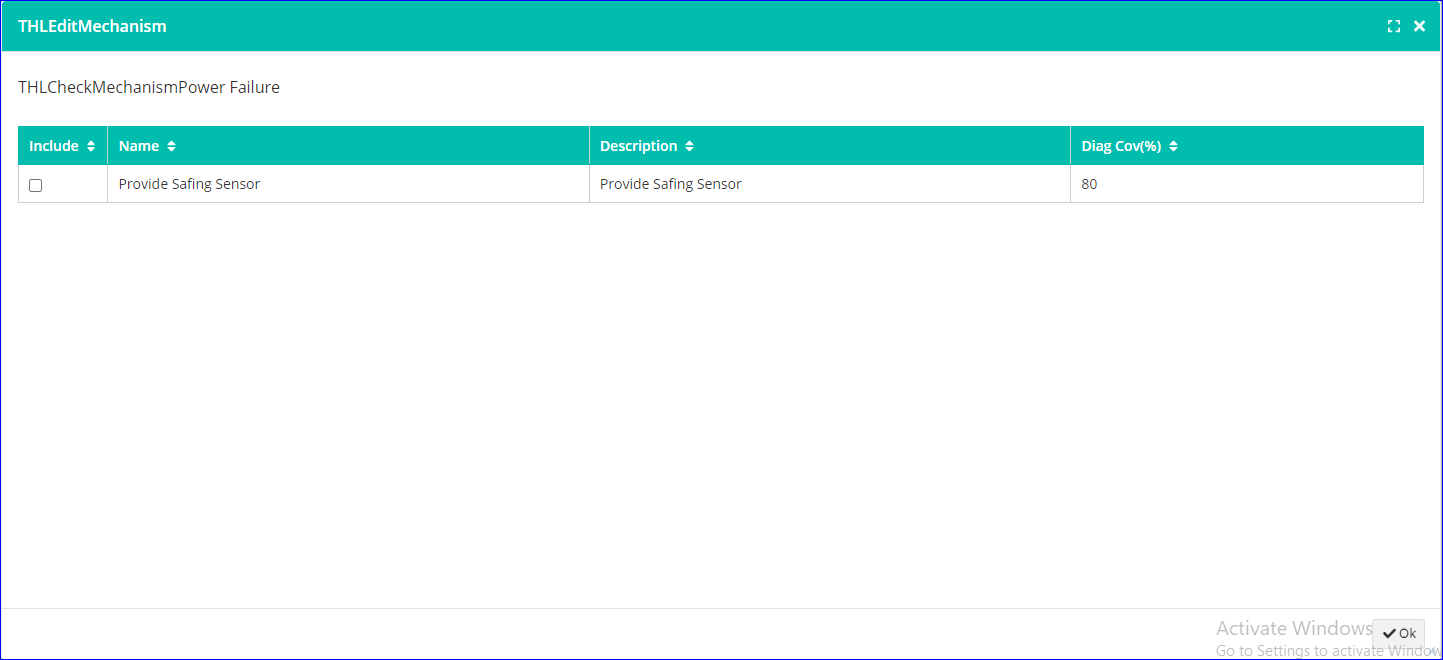


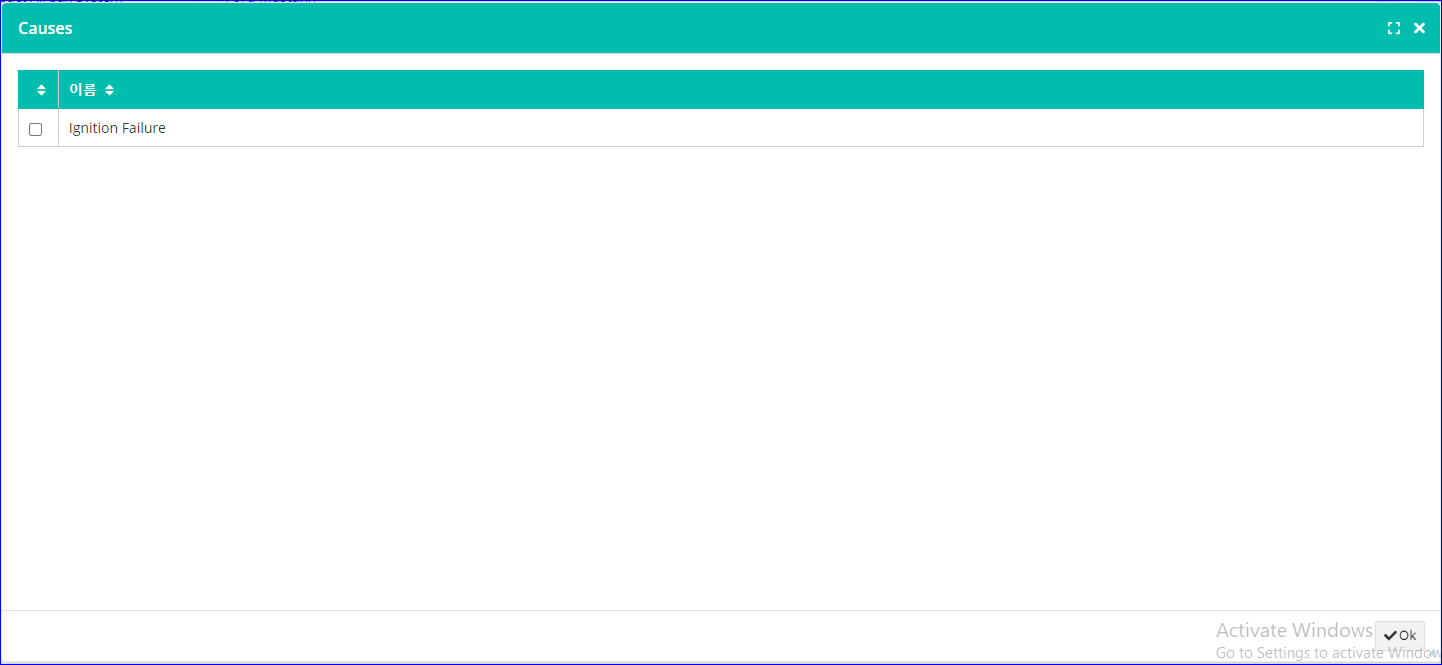
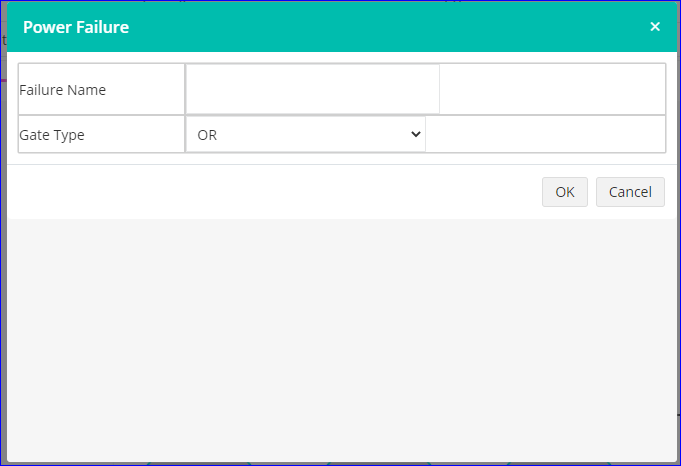
**TSR**



**FTA**



**FMDA**

