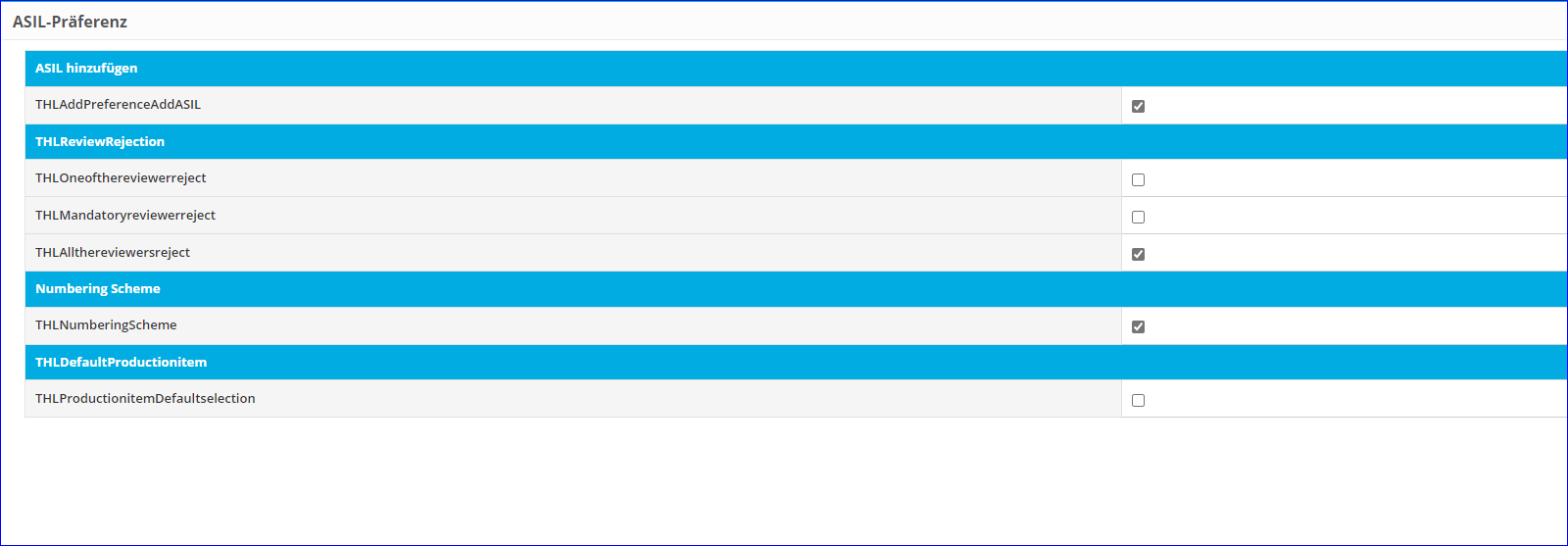
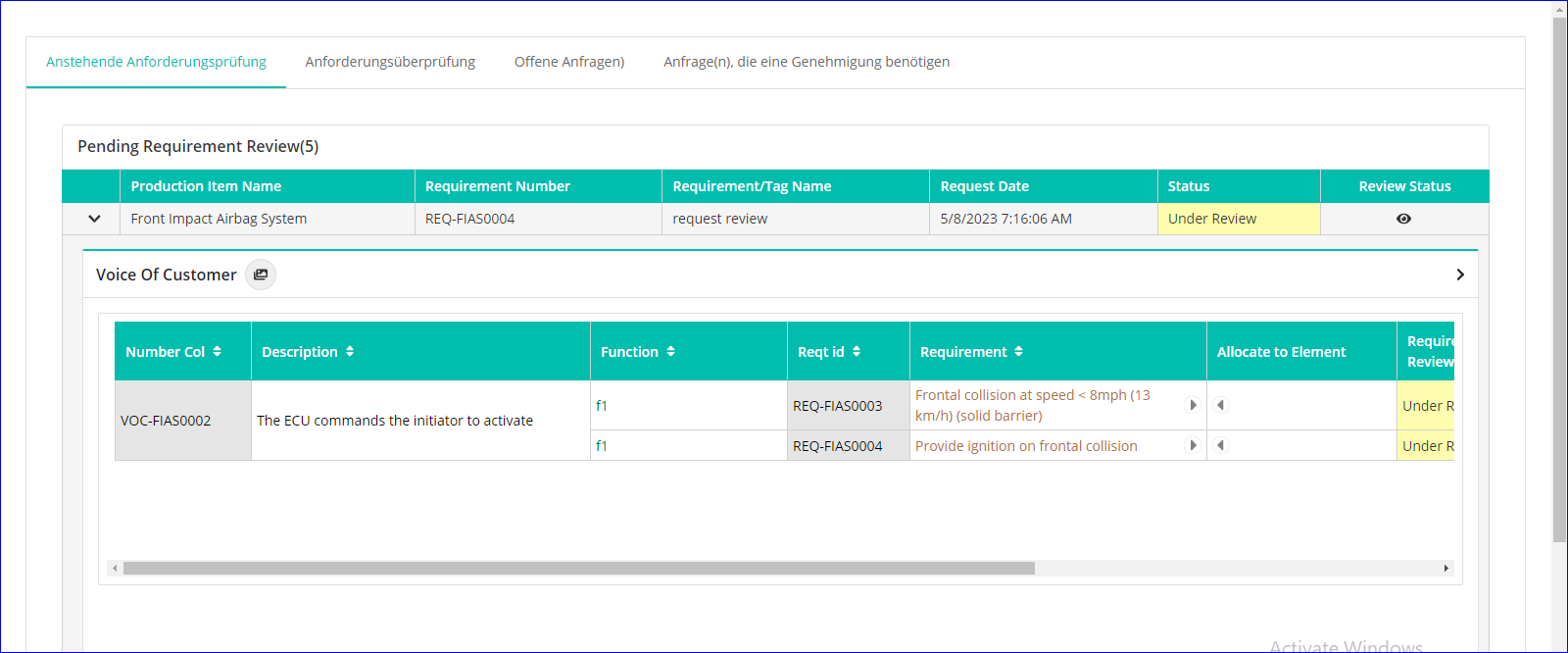
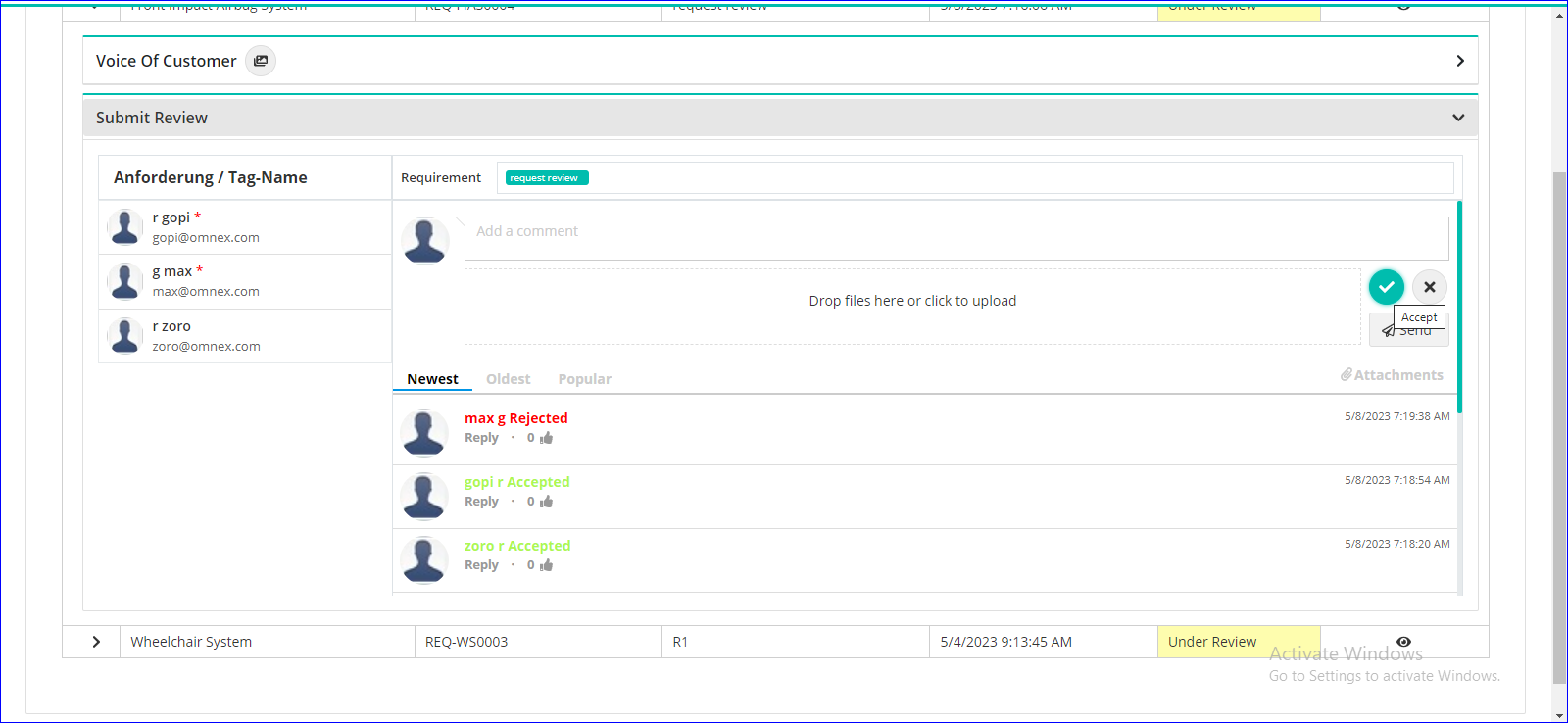
**Preference**



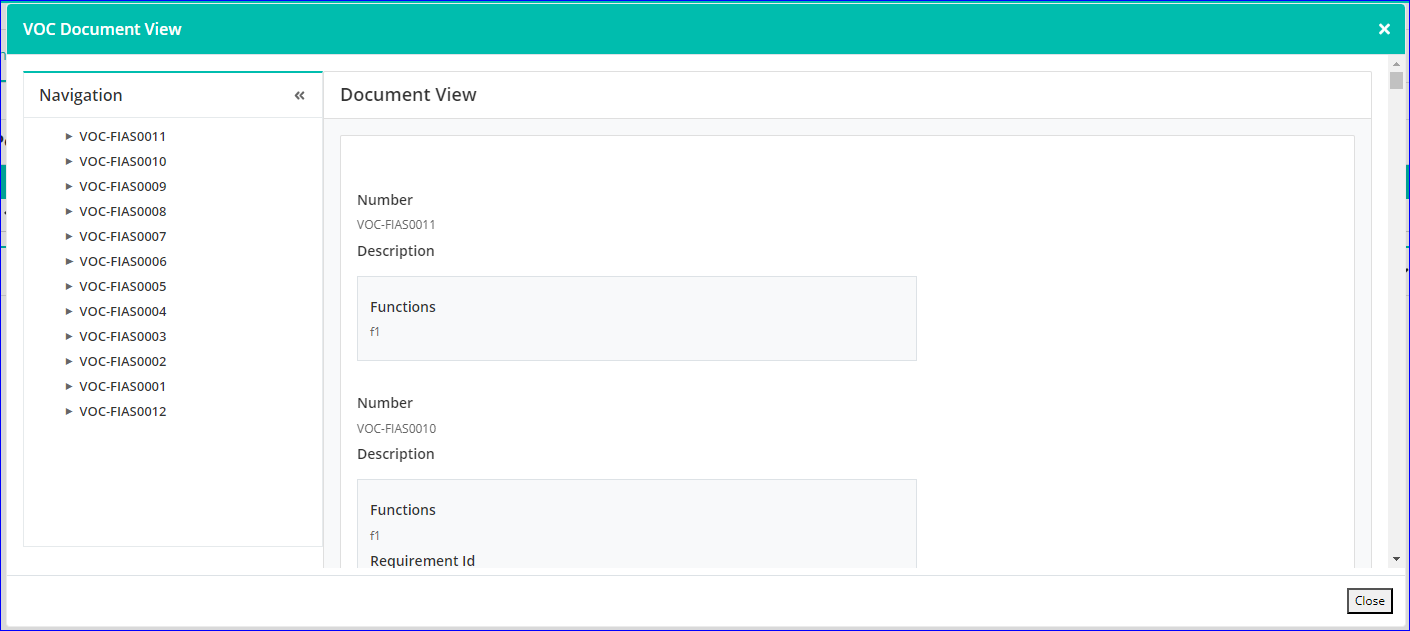
**Action(s)**

**Pending Requirement Review**

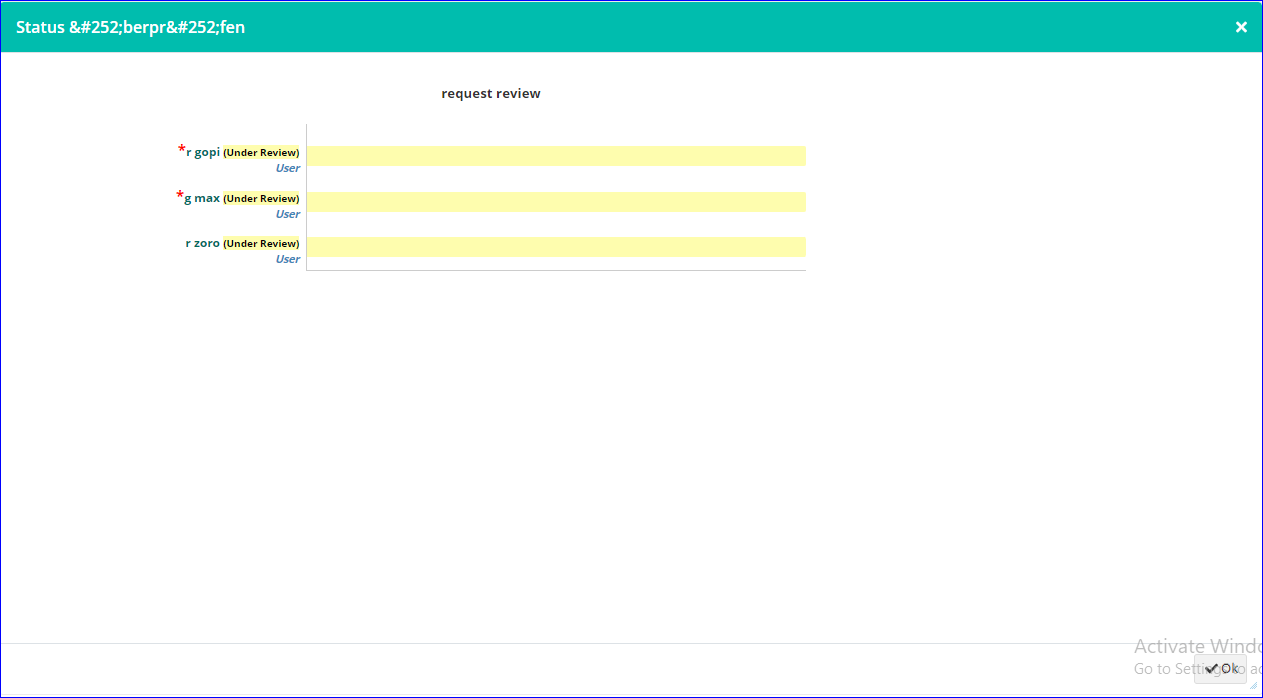
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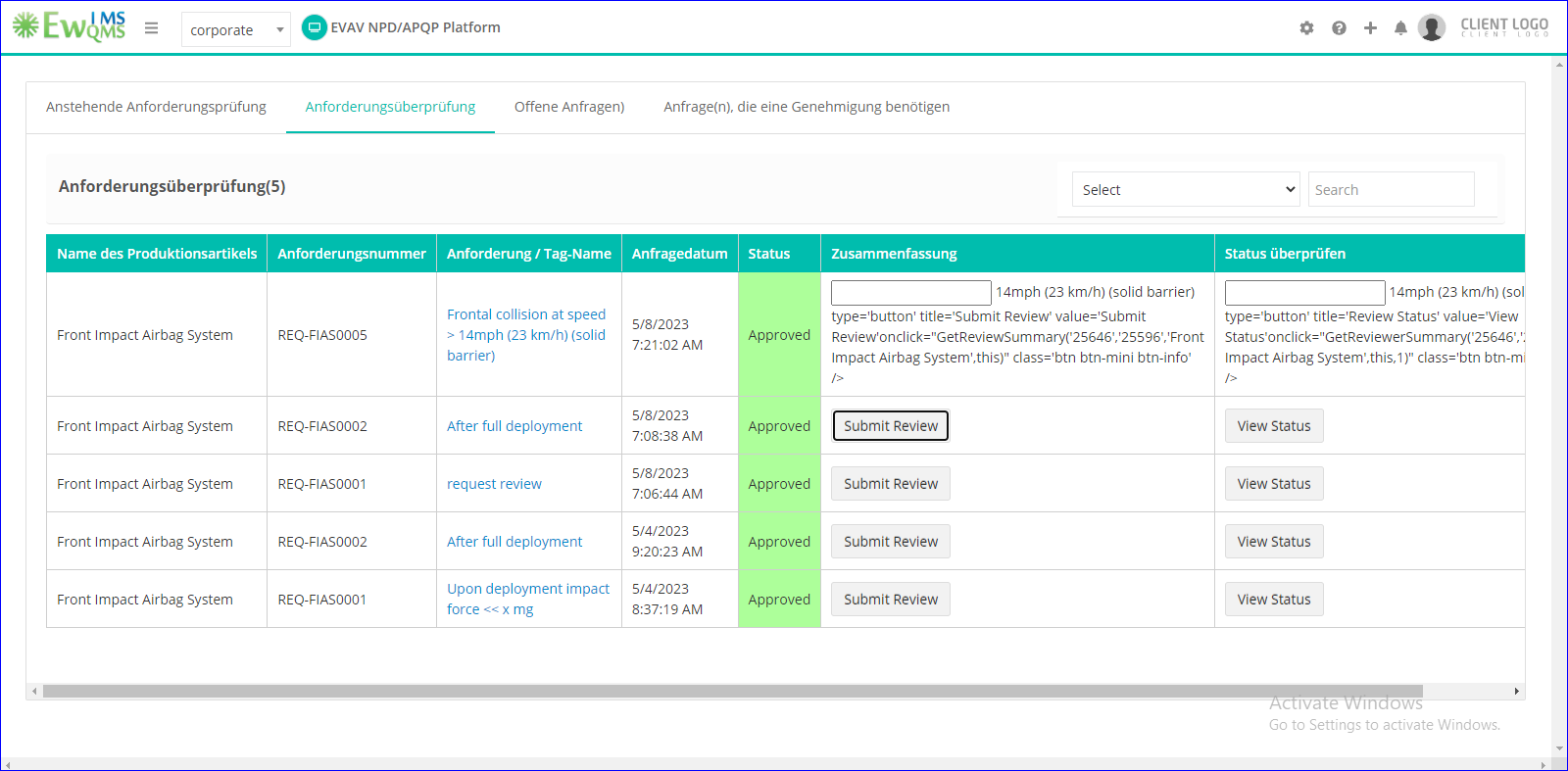
**Document View**

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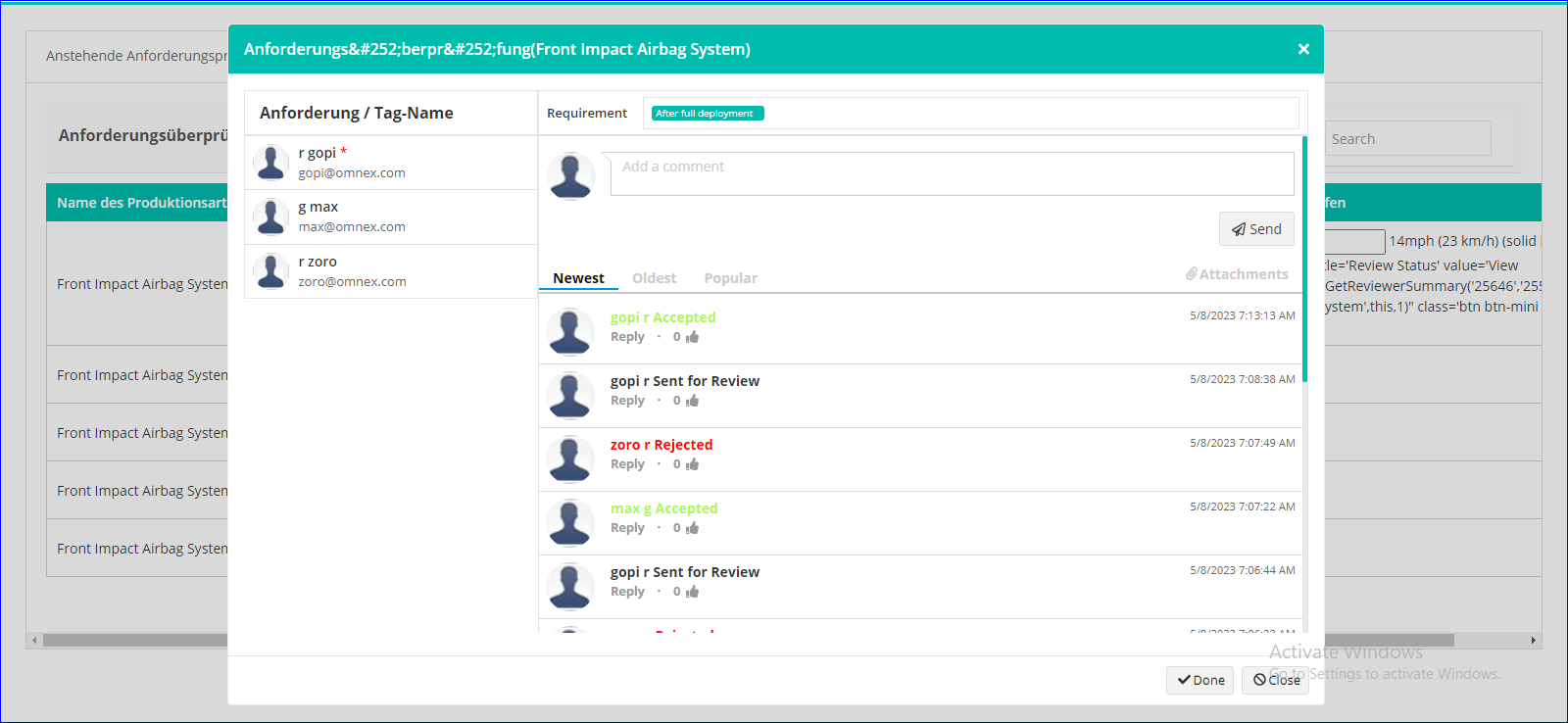
**Review status**



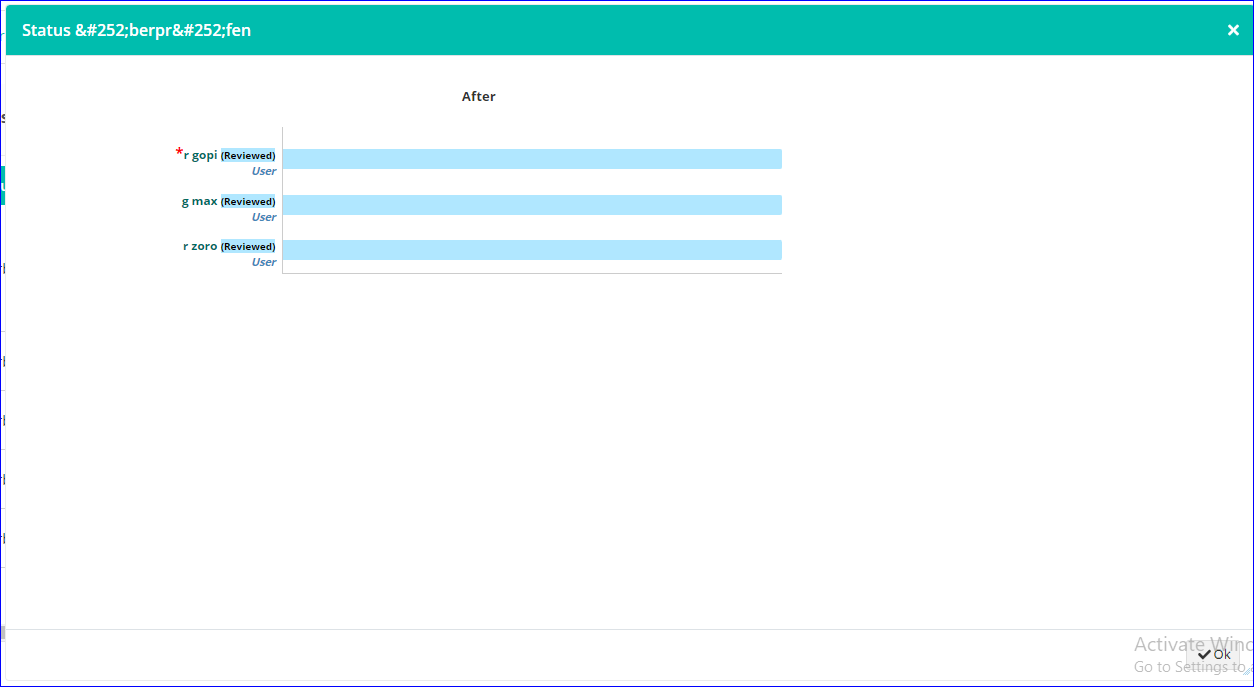
**Requirements Reviewed**

****

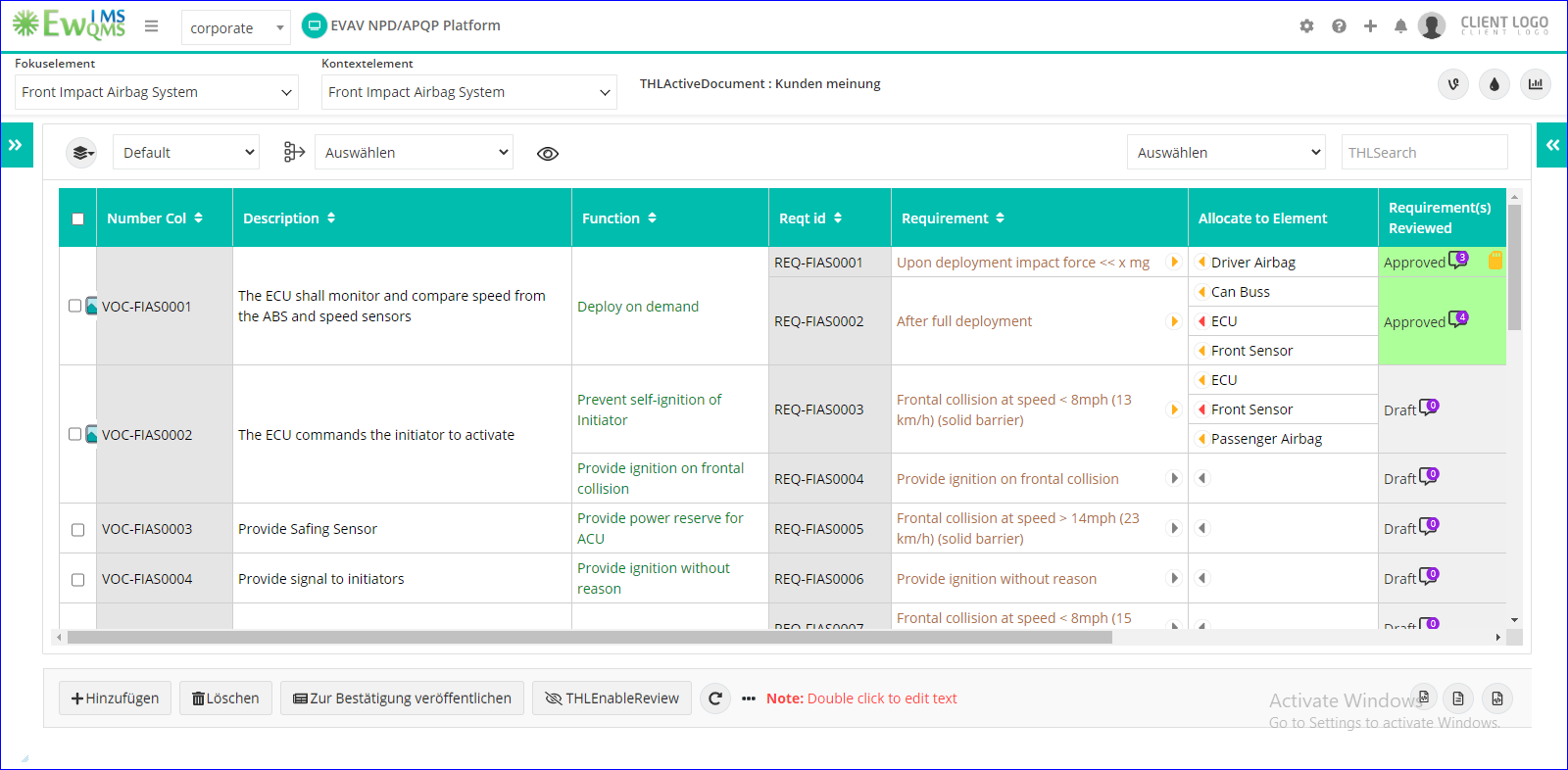
**Submit Review**

****

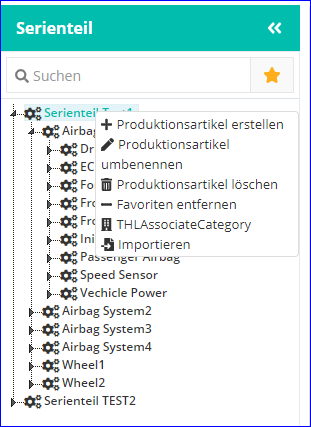
**View status**

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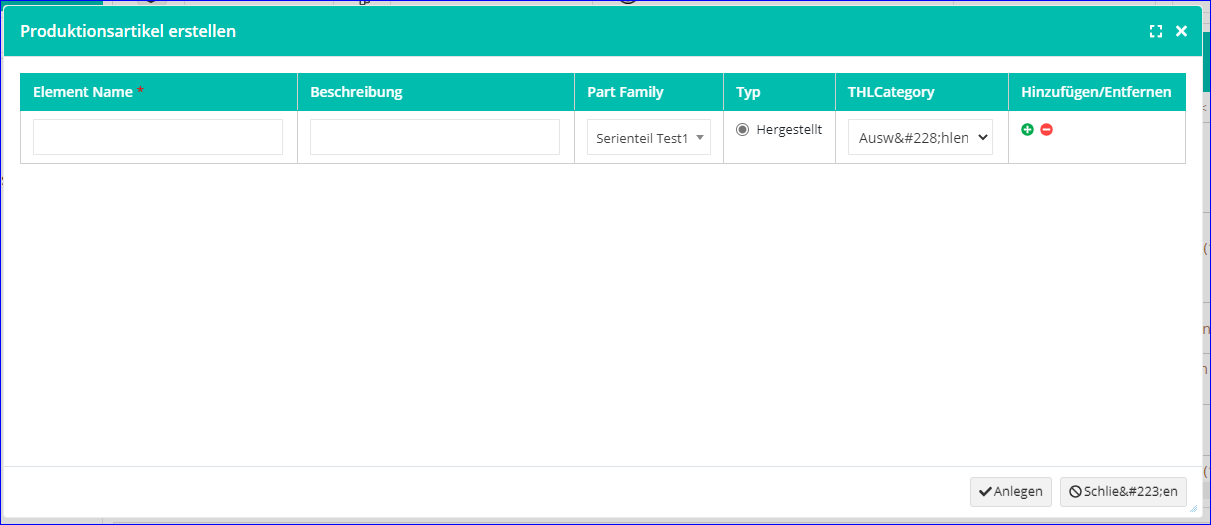
**VOC**

****

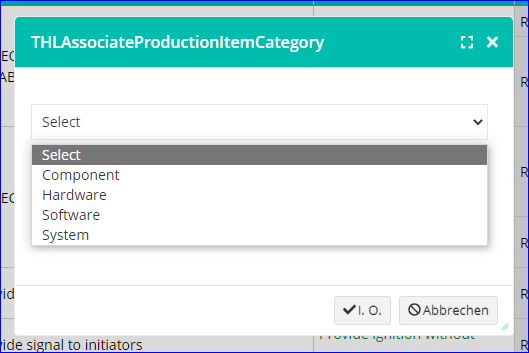
**PI tree**

****

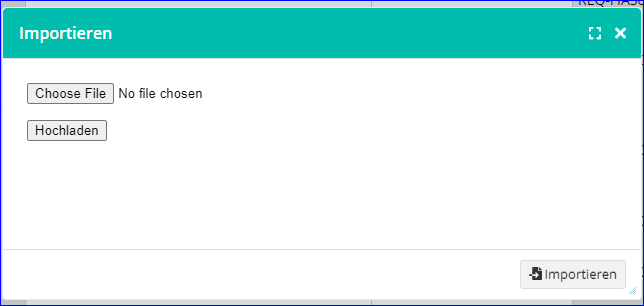
**Create PI**

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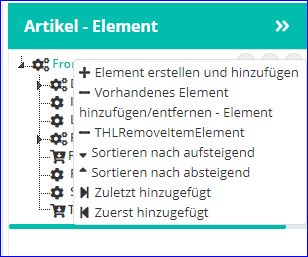
**Associate PI Category**

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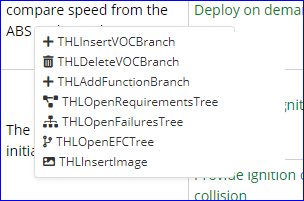
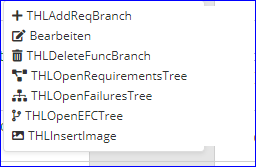
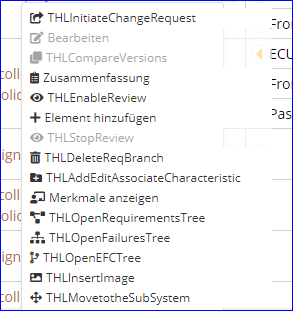
**Import**

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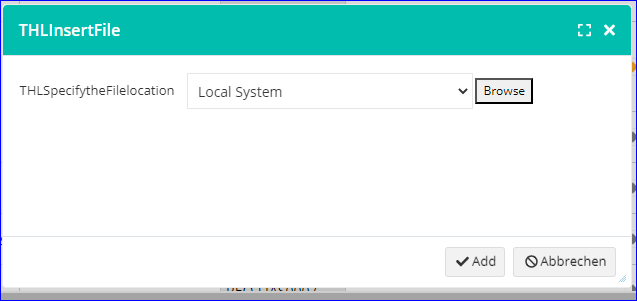
**BOM tree**

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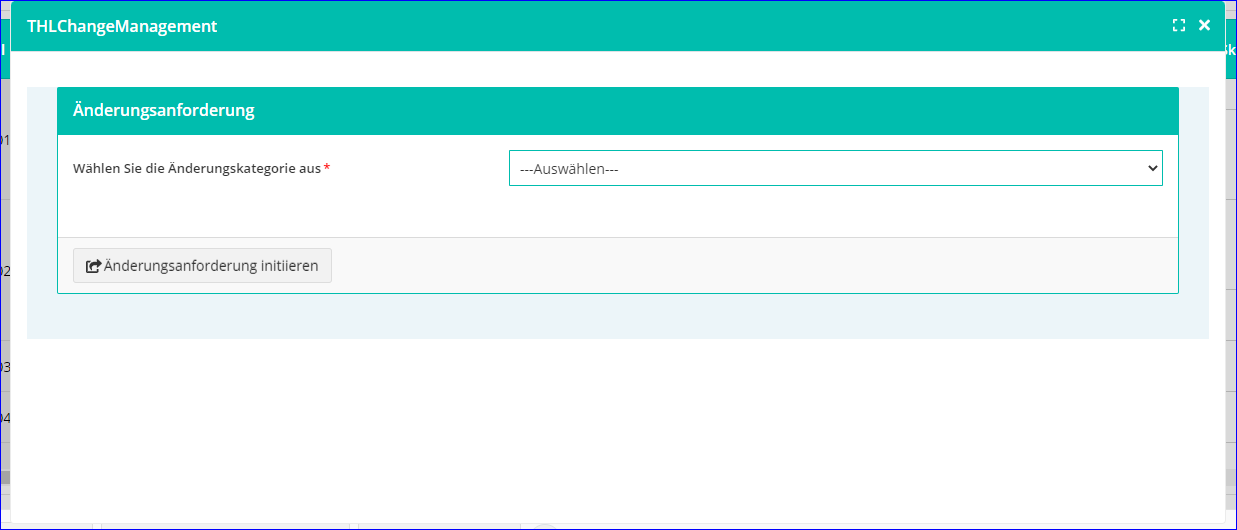
**VOC all the context menus have a THL issue**

**  **

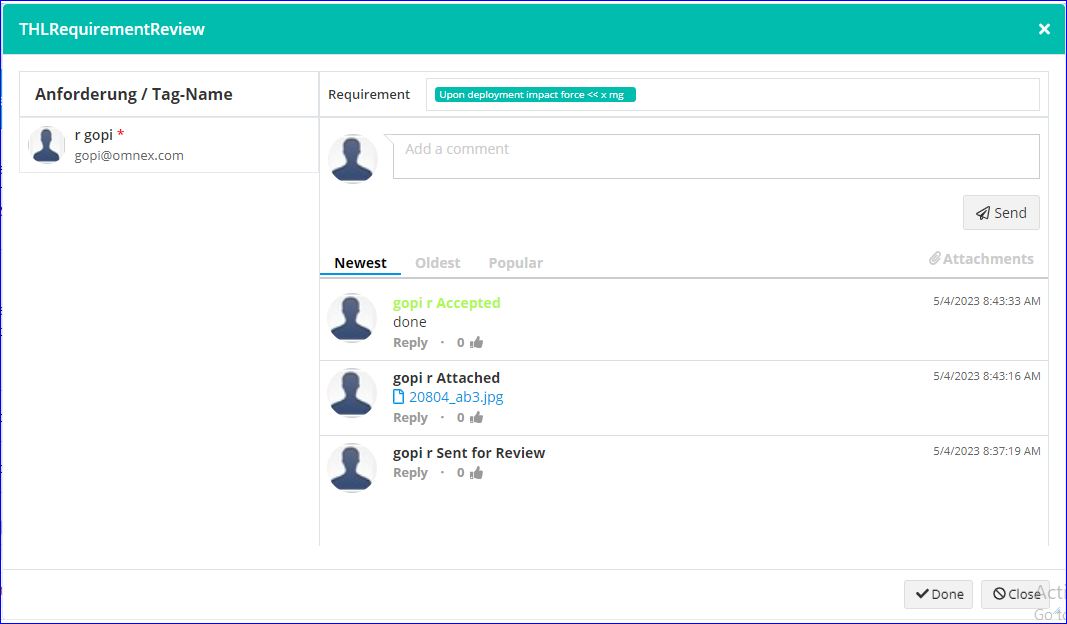
**Insert Image**

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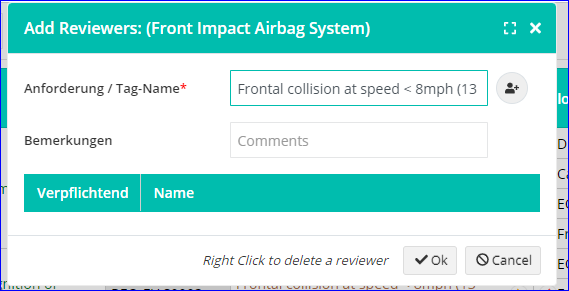
**Initiate Change Request**

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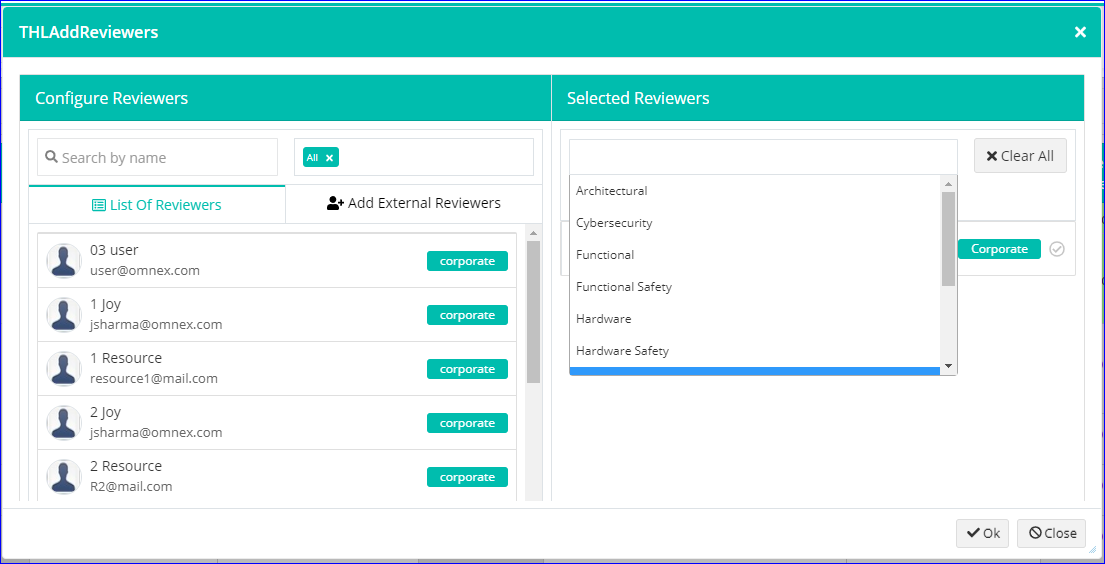
**Submit Review**

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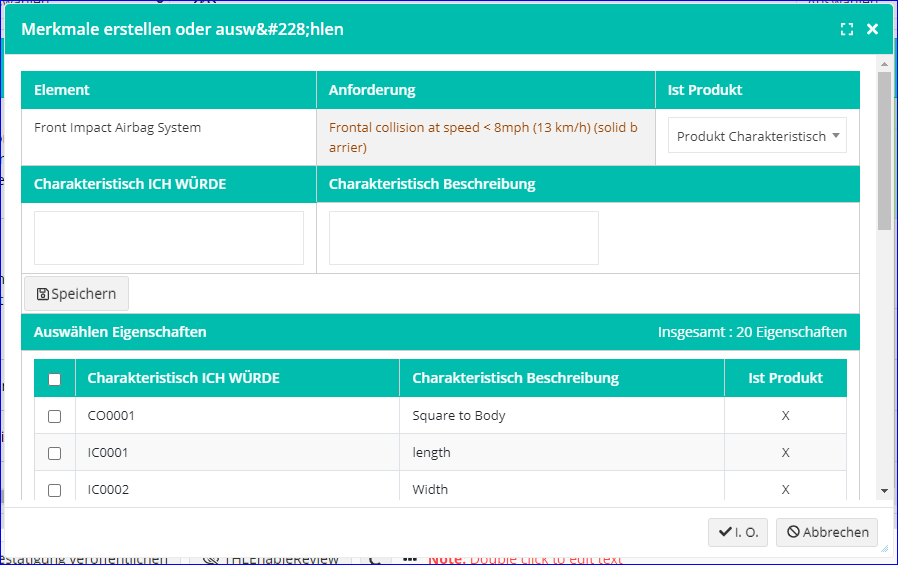
**Enable Review**

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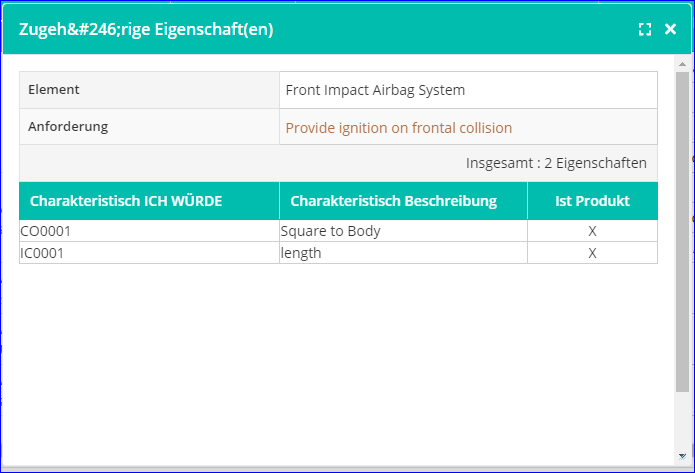
**Add Reviewers popup**

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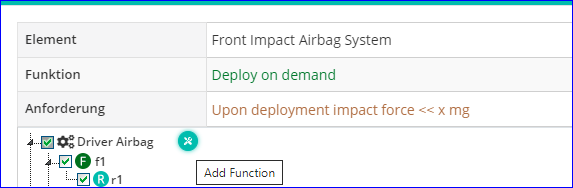
**Add Associate characteristics**

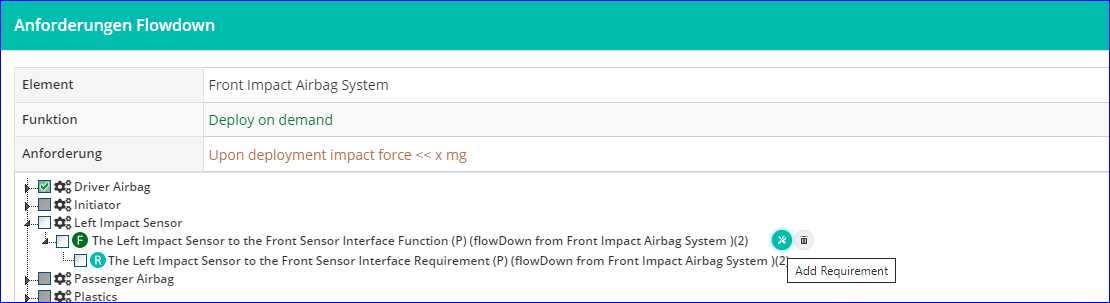
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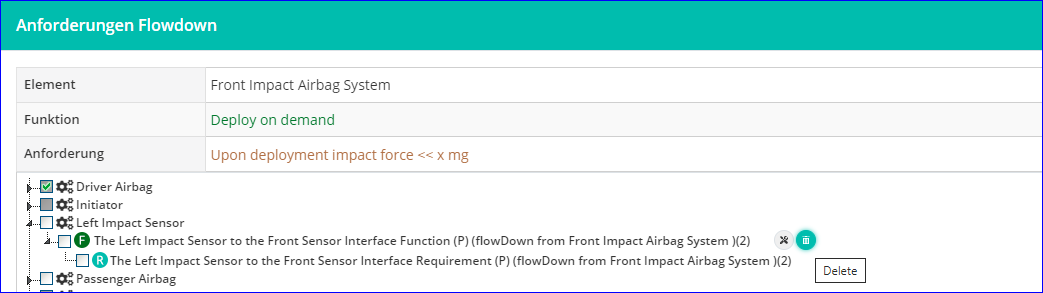
**View Associate characteristics**

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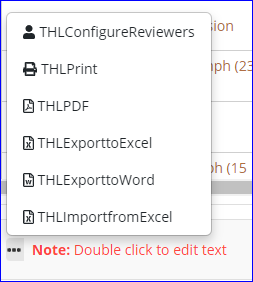
**Requirement Flowdown**



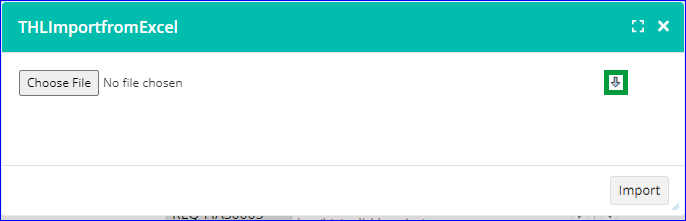




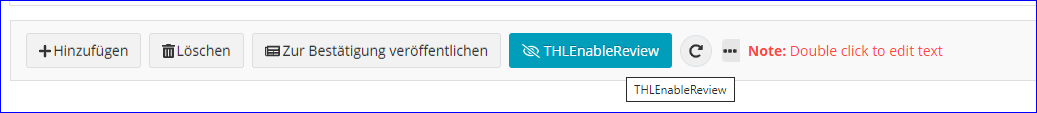
**More options**

****

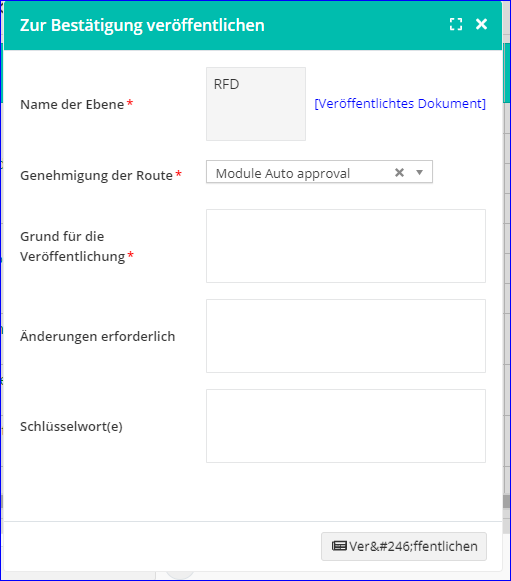
**Import Excel**

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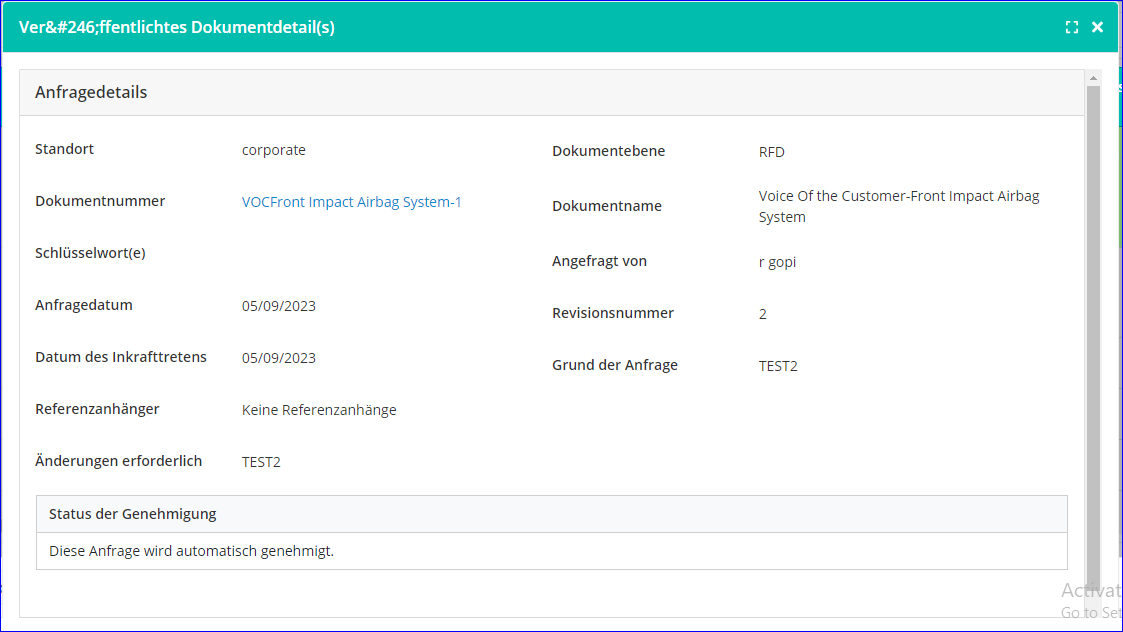
**Enable Review**



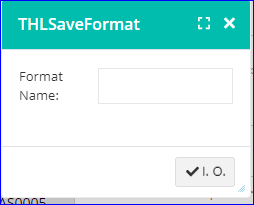
**Publish for approval**

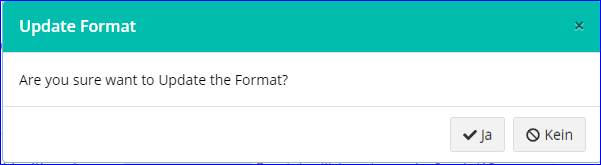
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**Published Document**

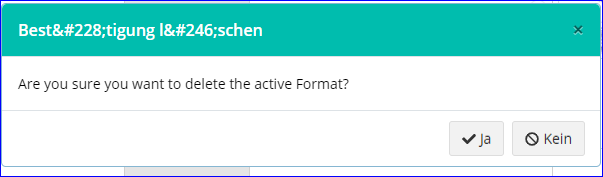
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**Save format**

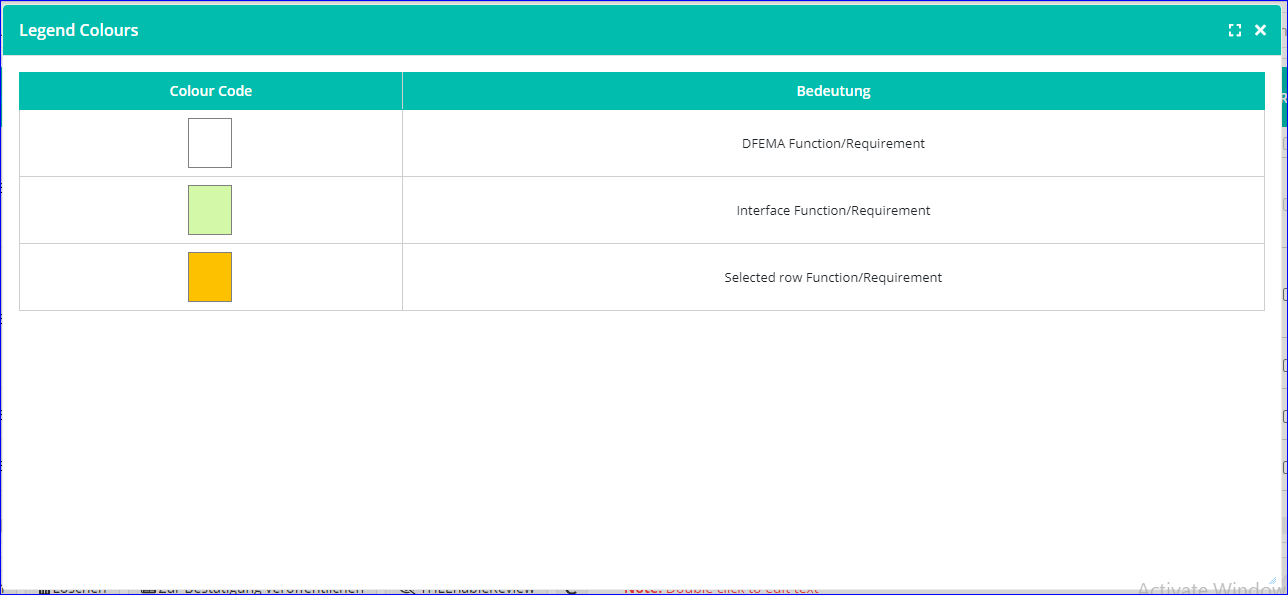


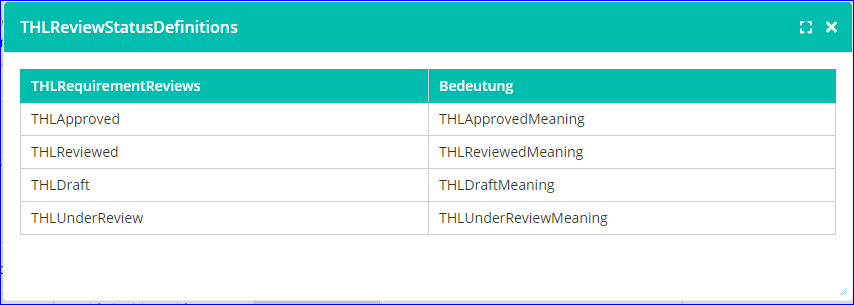
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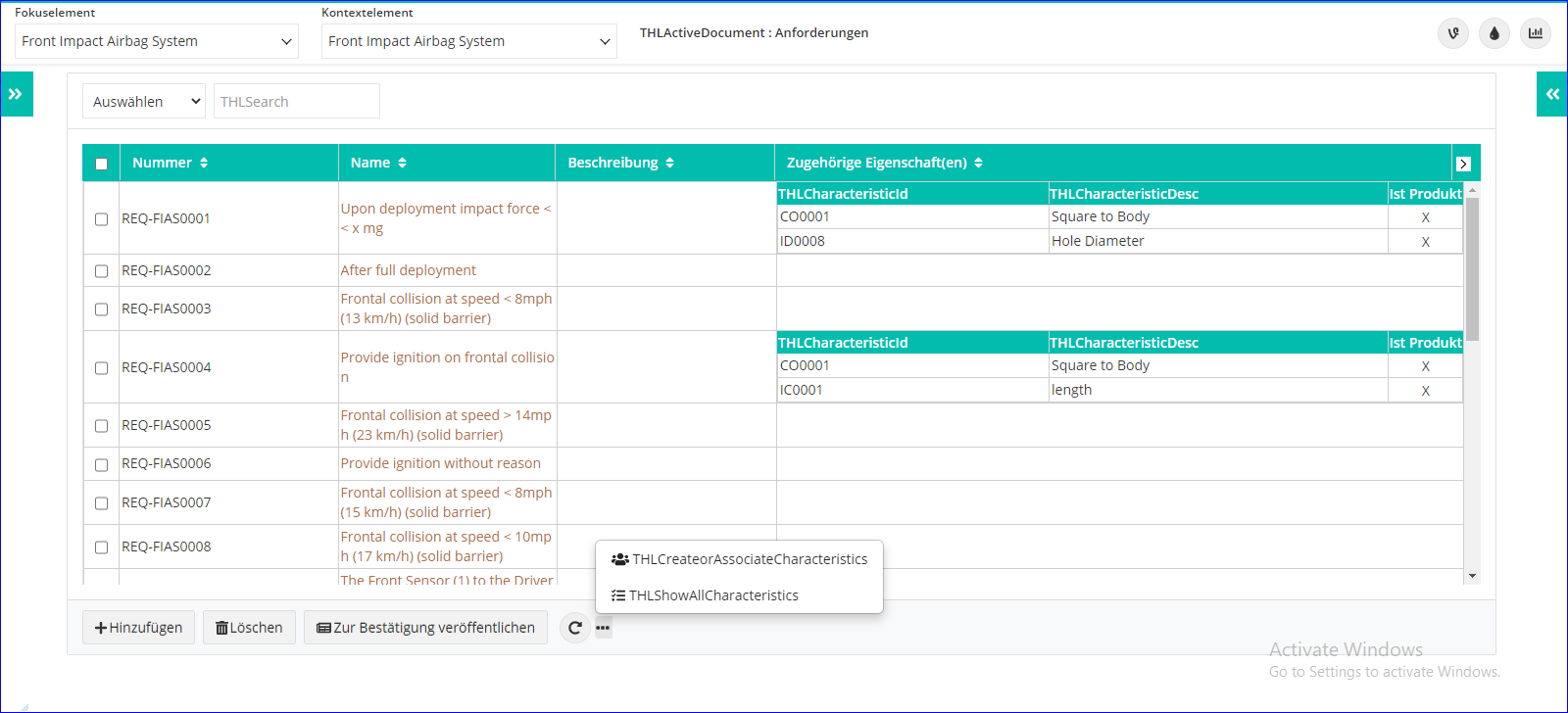


**Legend colors**

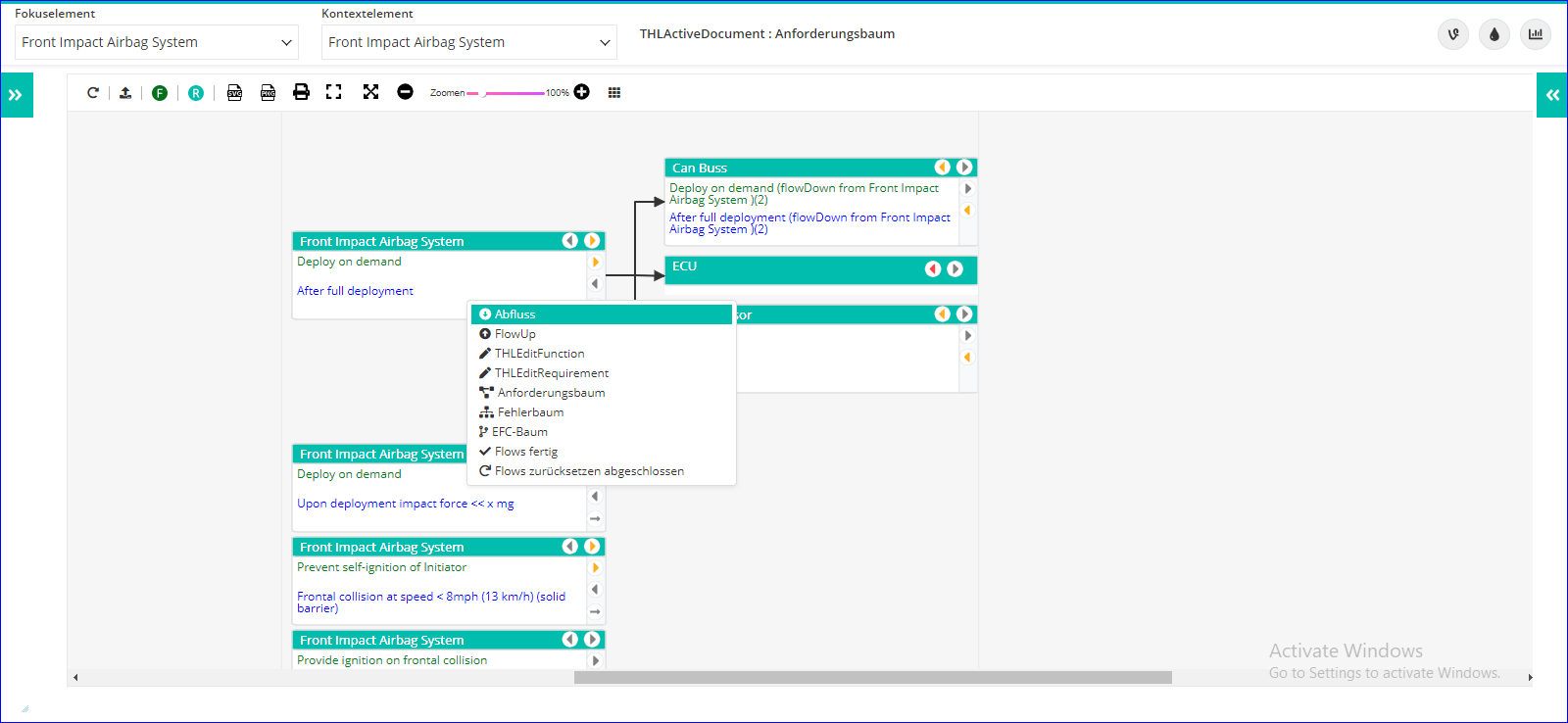


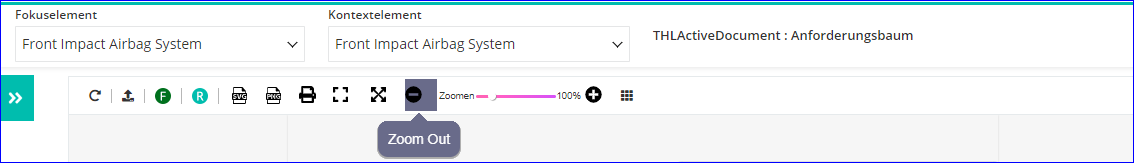


**Requirements**

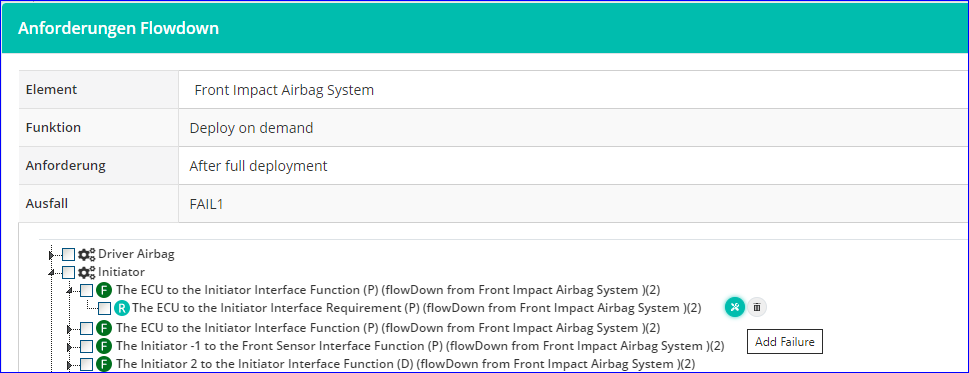


**Requirements Tree**

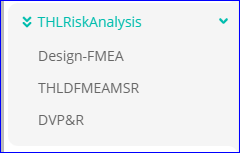




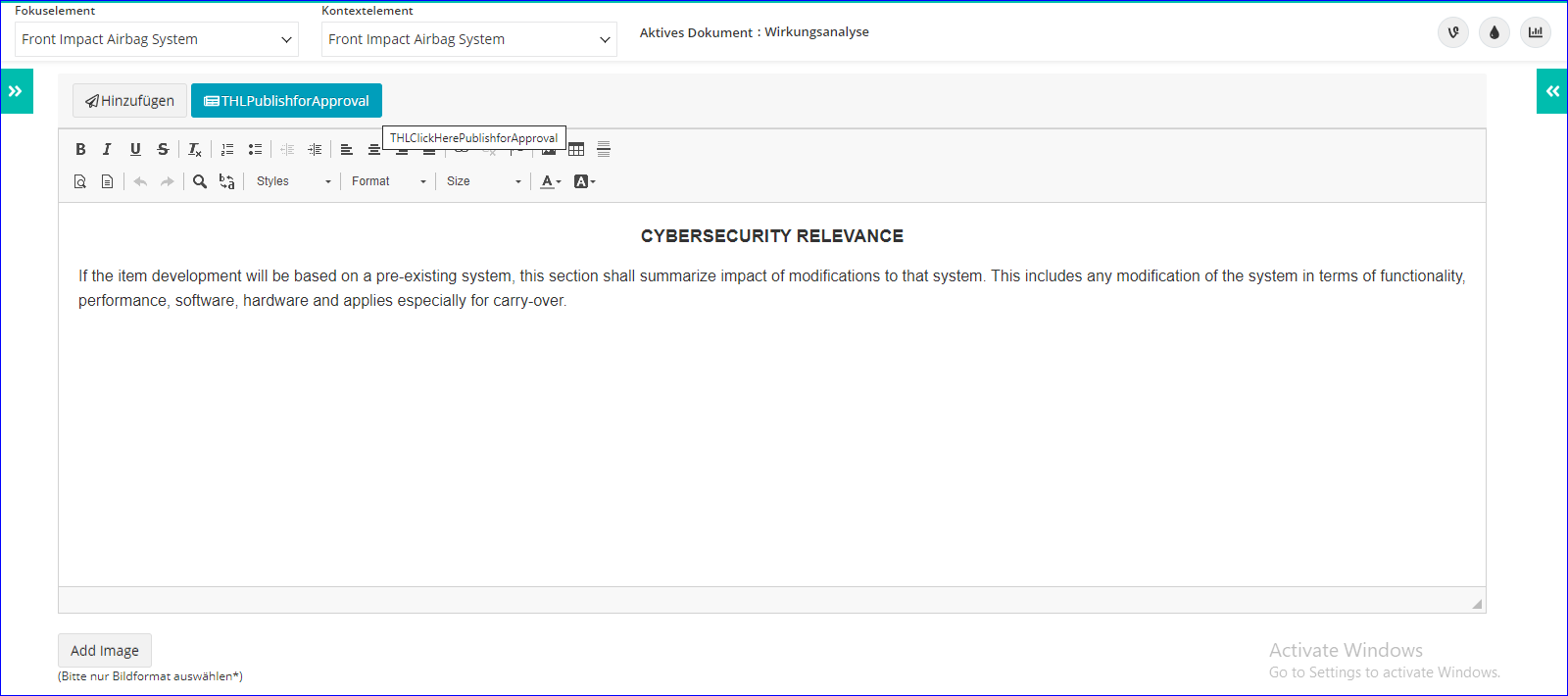
**Failure Tree**

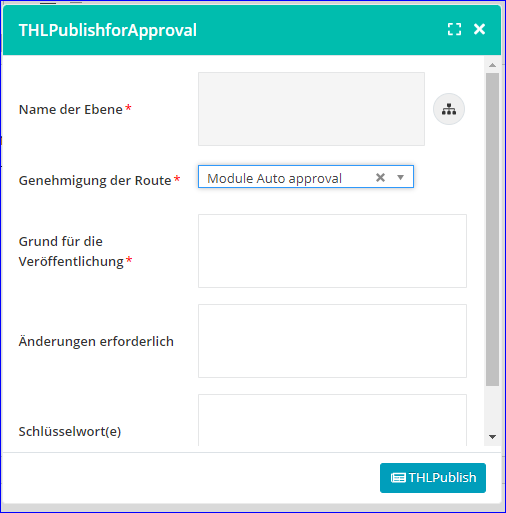


**Risk Analysis**

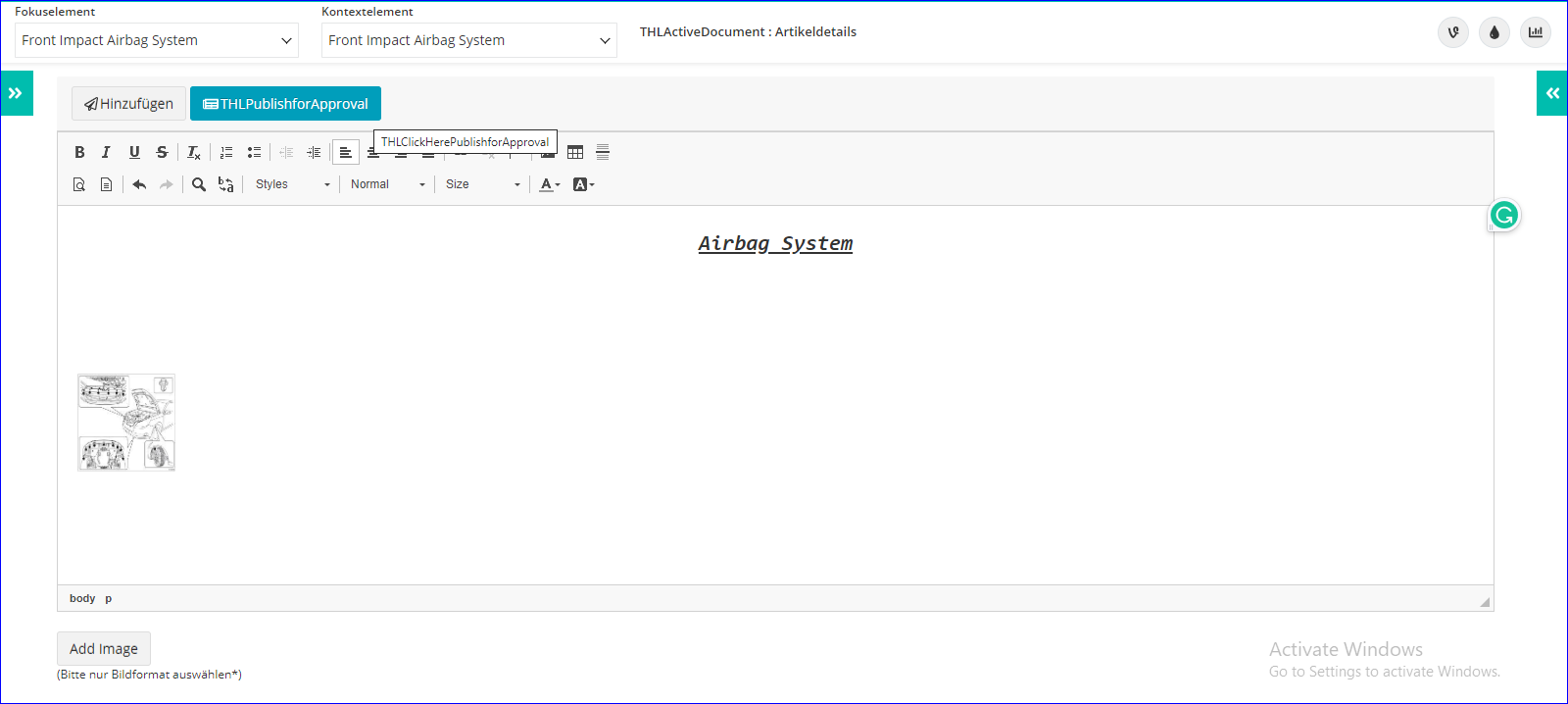


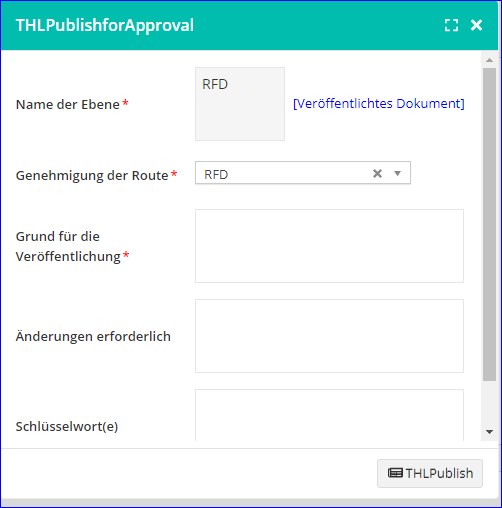
**Impact Analysis**



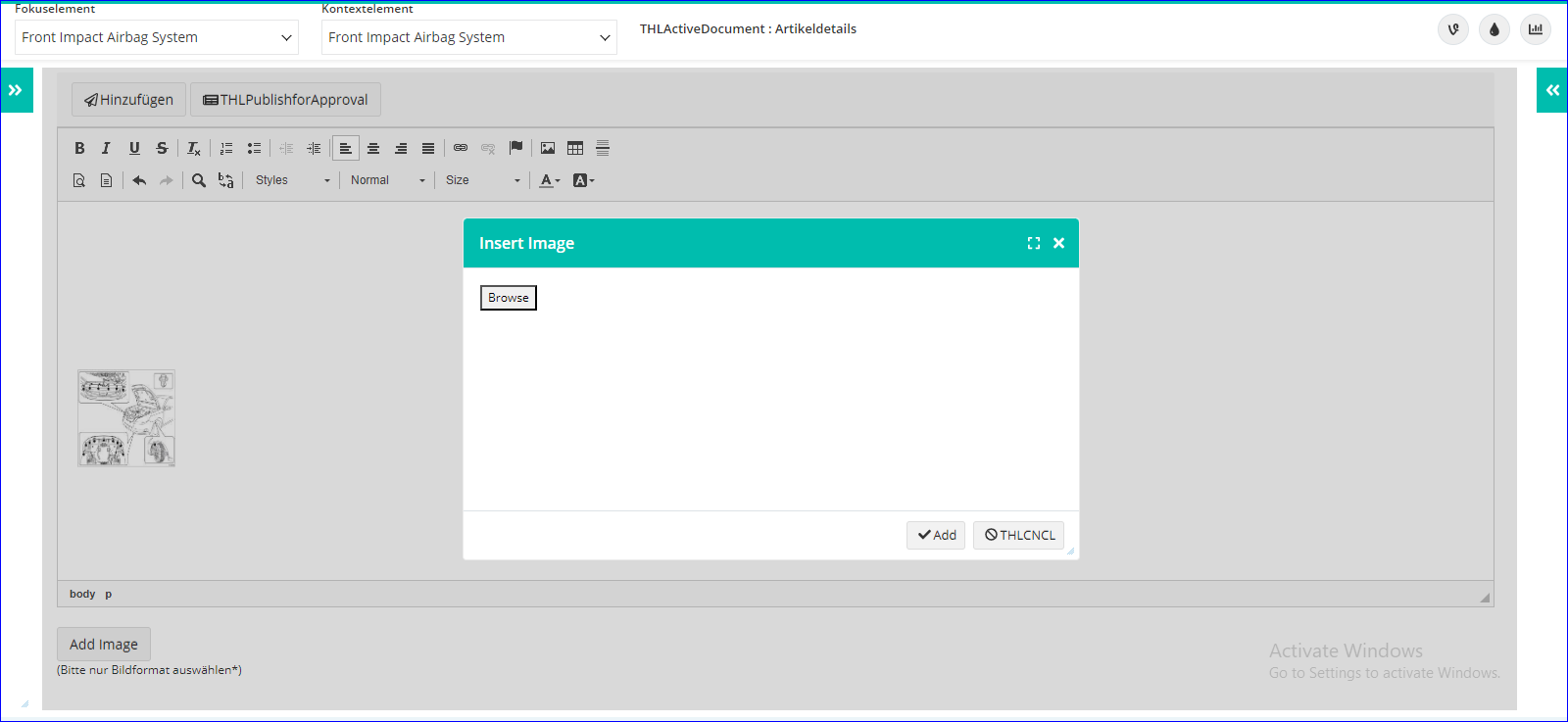


**Item Details**

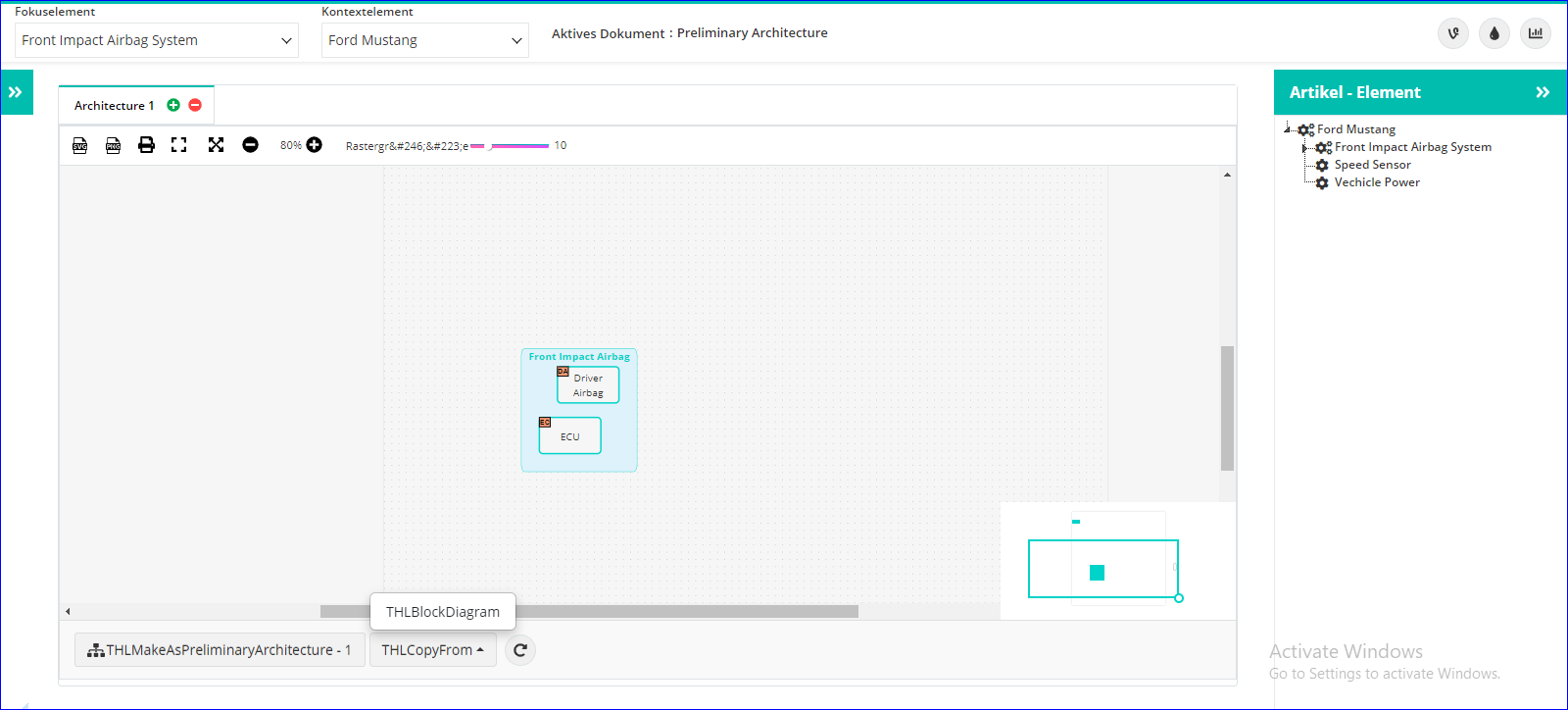




**Add Image**

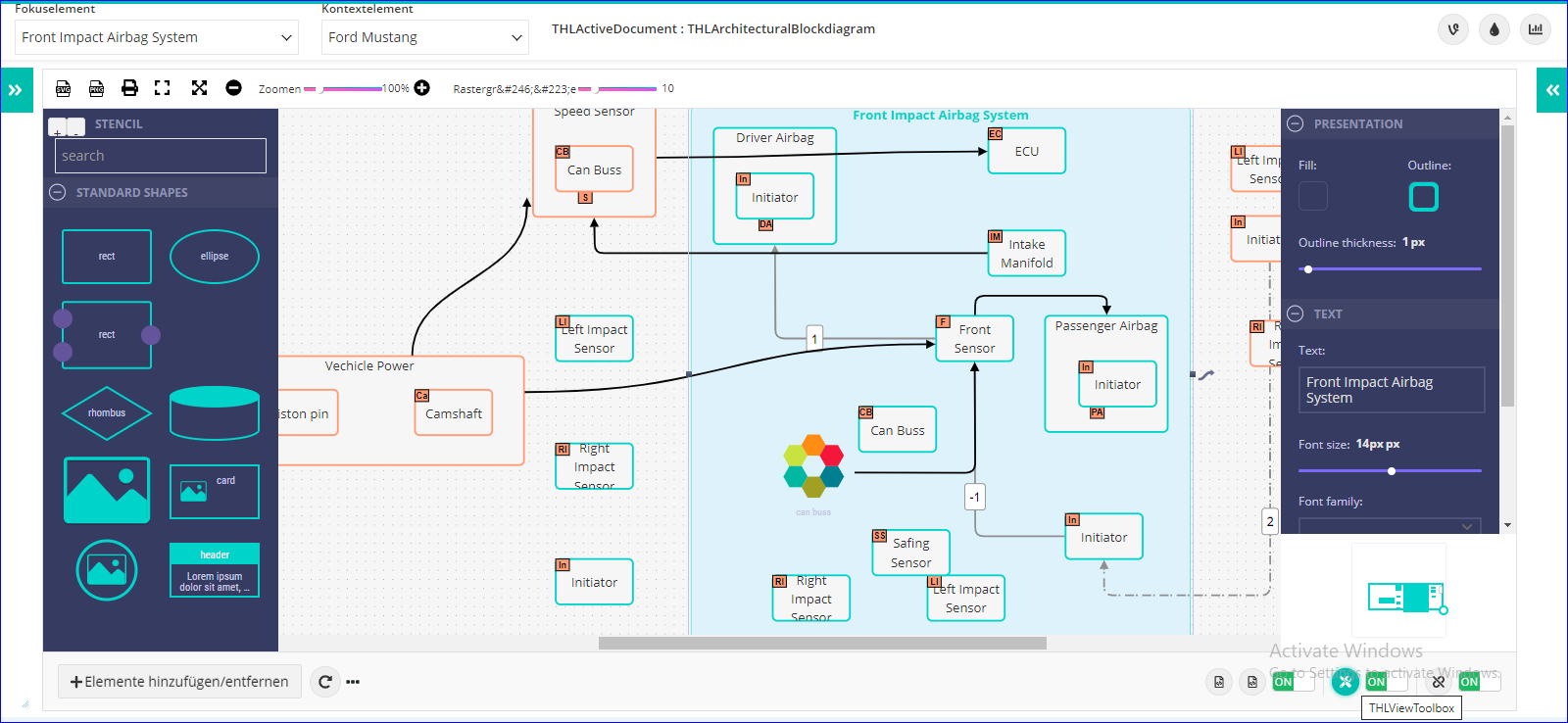


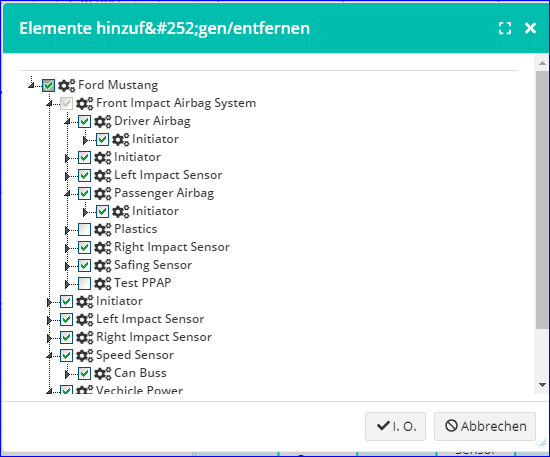
**Preliminary Architecture**



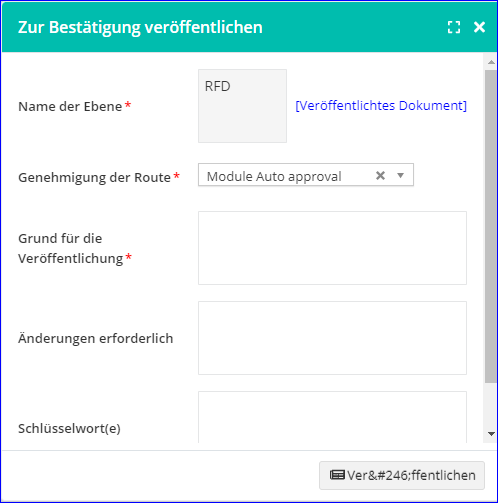
**Block diagram**



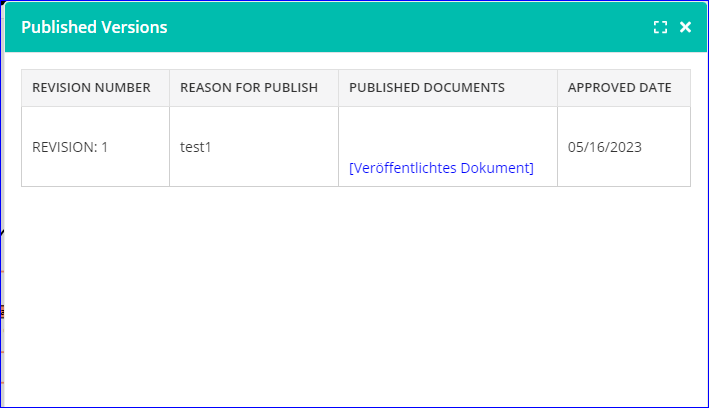




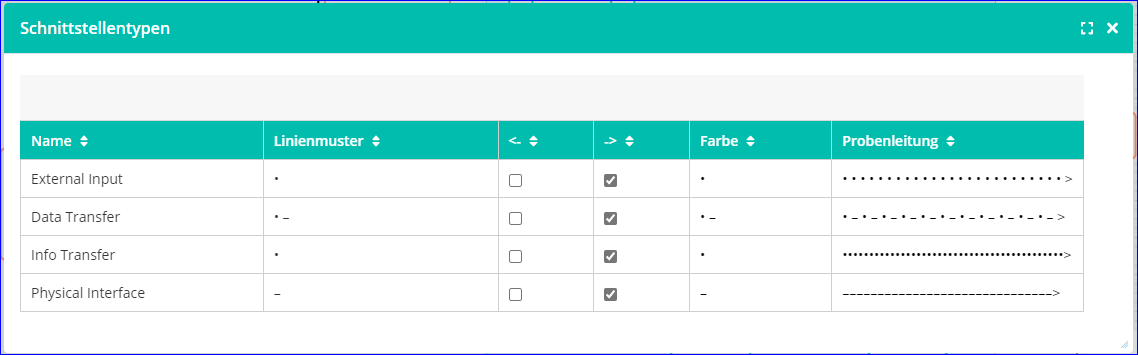
**Publish for approval**



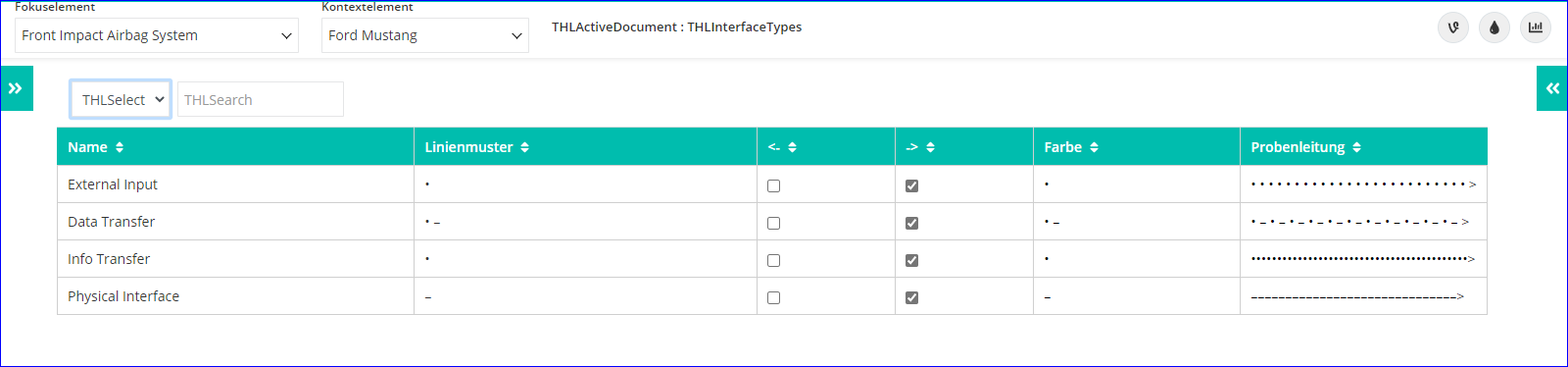
**Published Version**



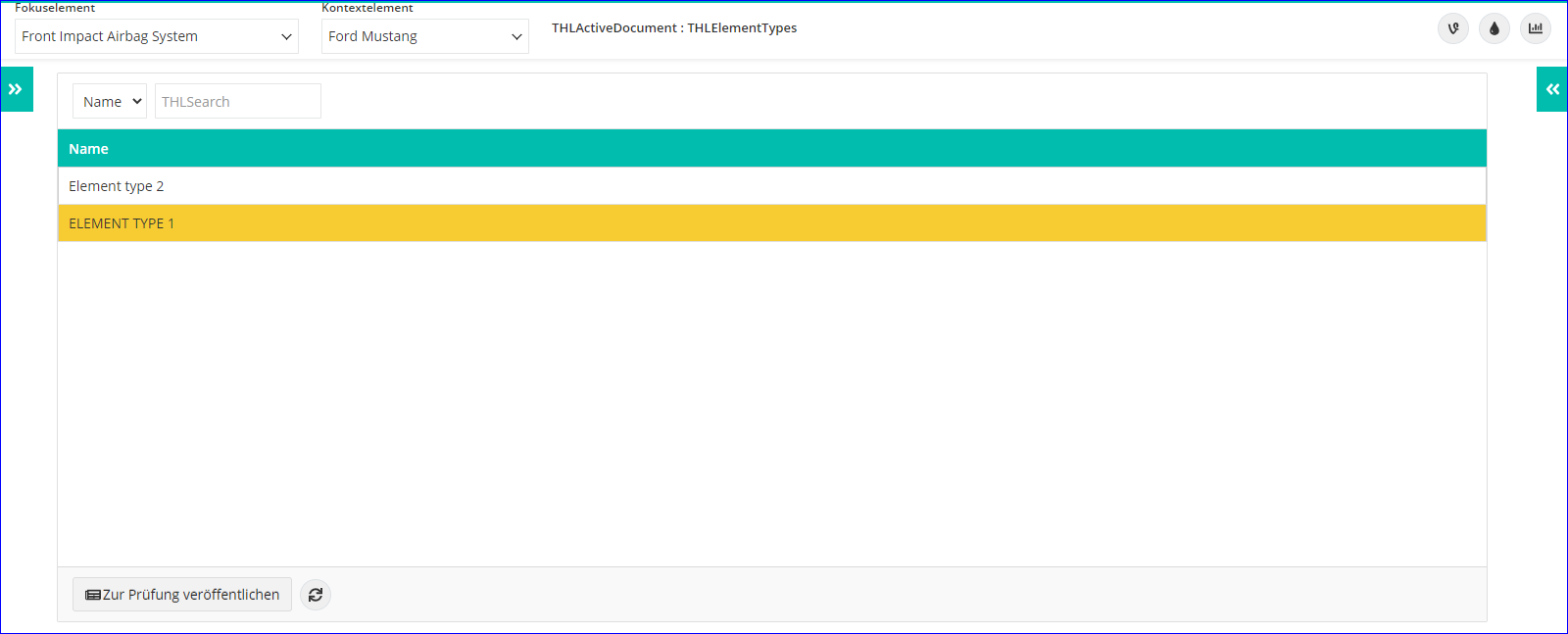
**Interface Type**



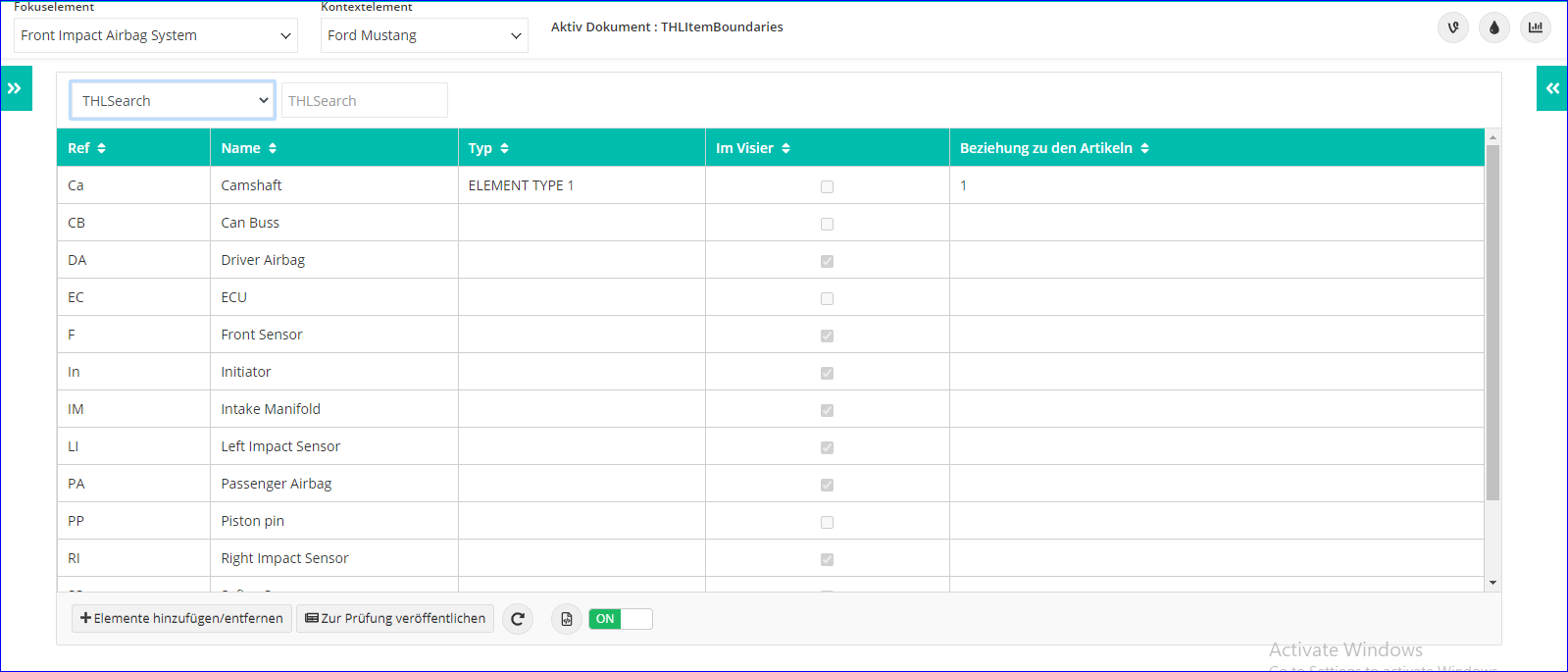
**Interface Type**



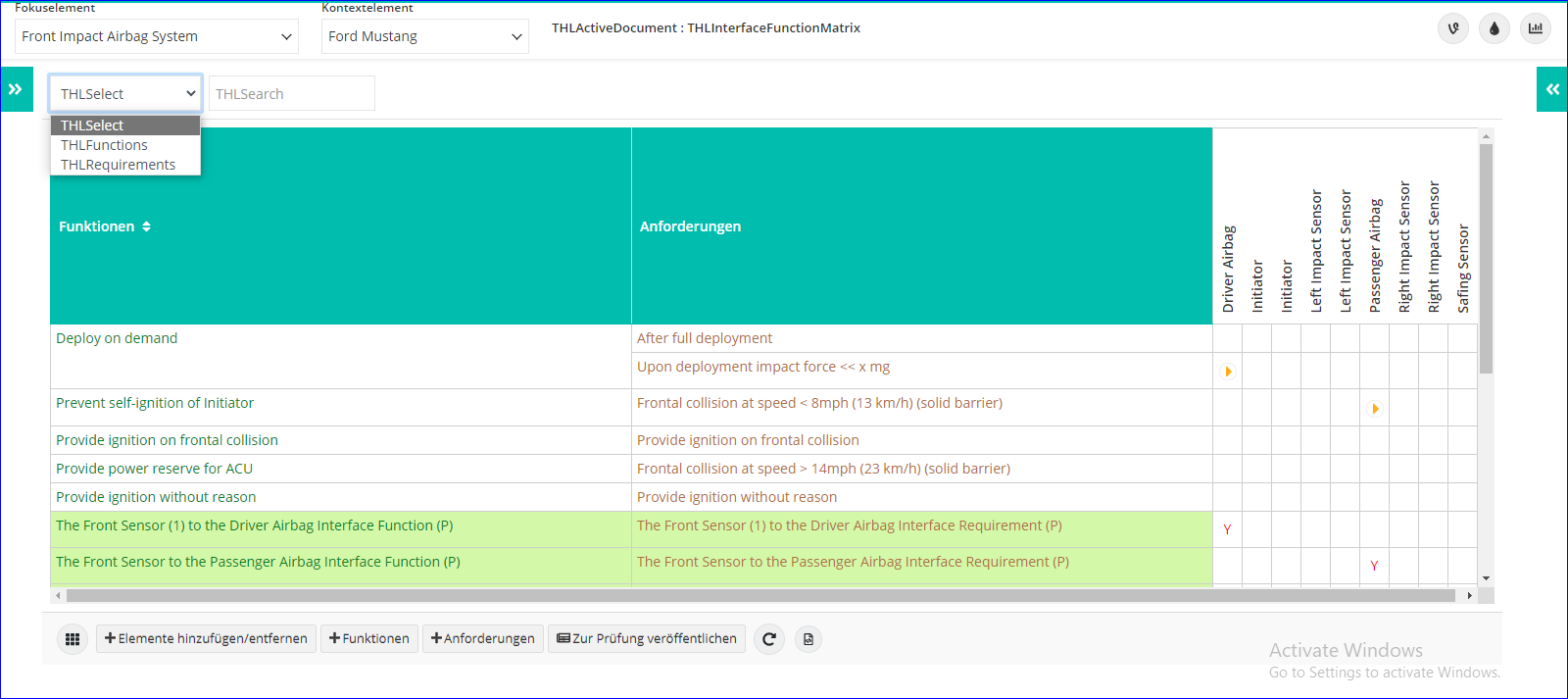
**Element Type**

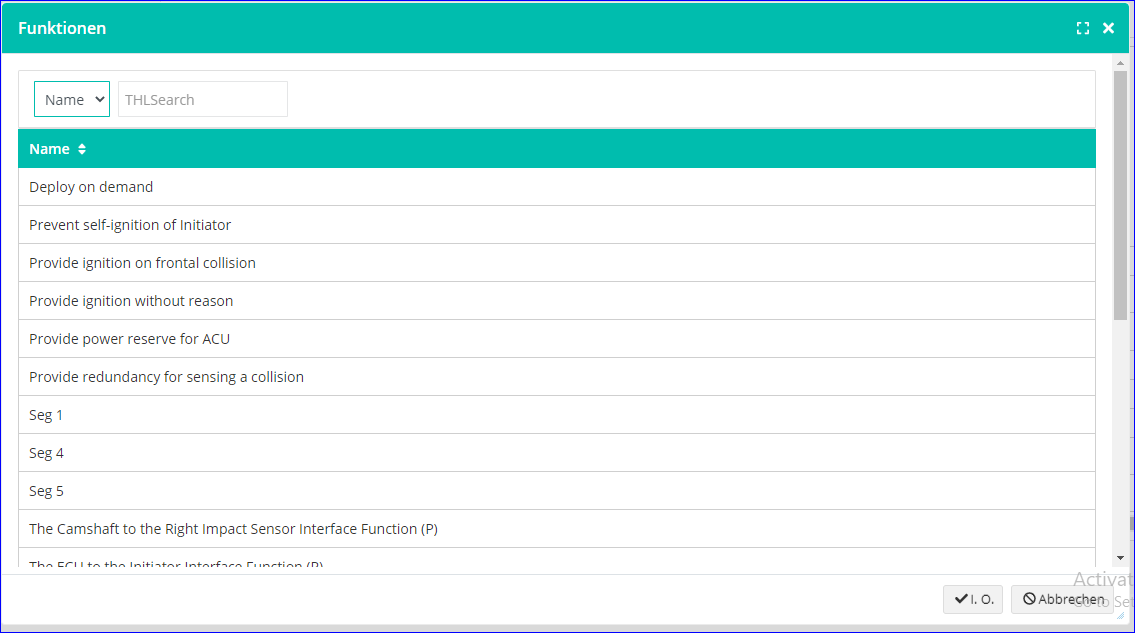


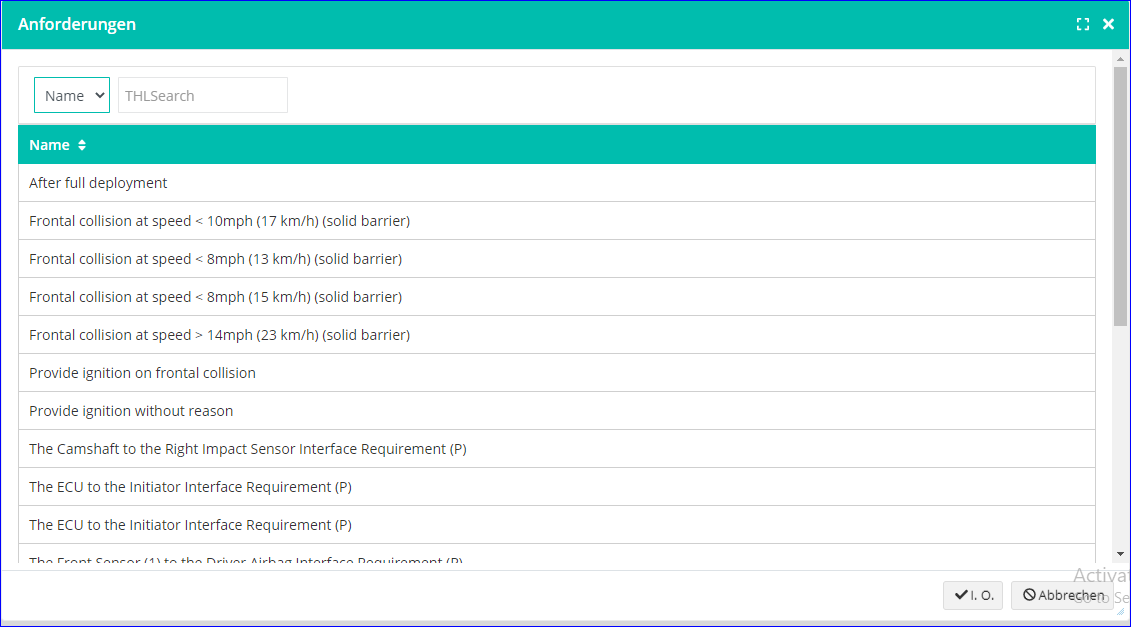
**Item Boundaries**



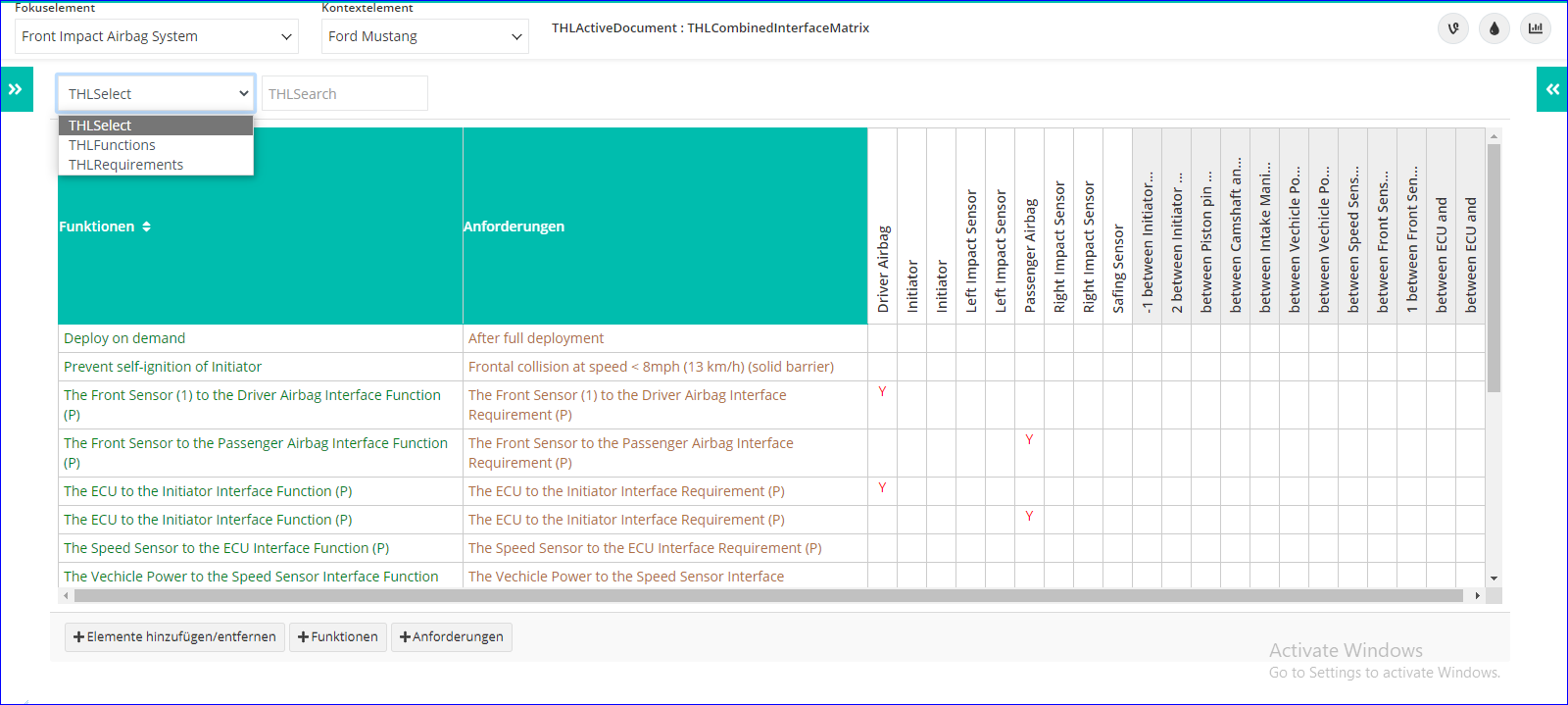
**Interface Function**



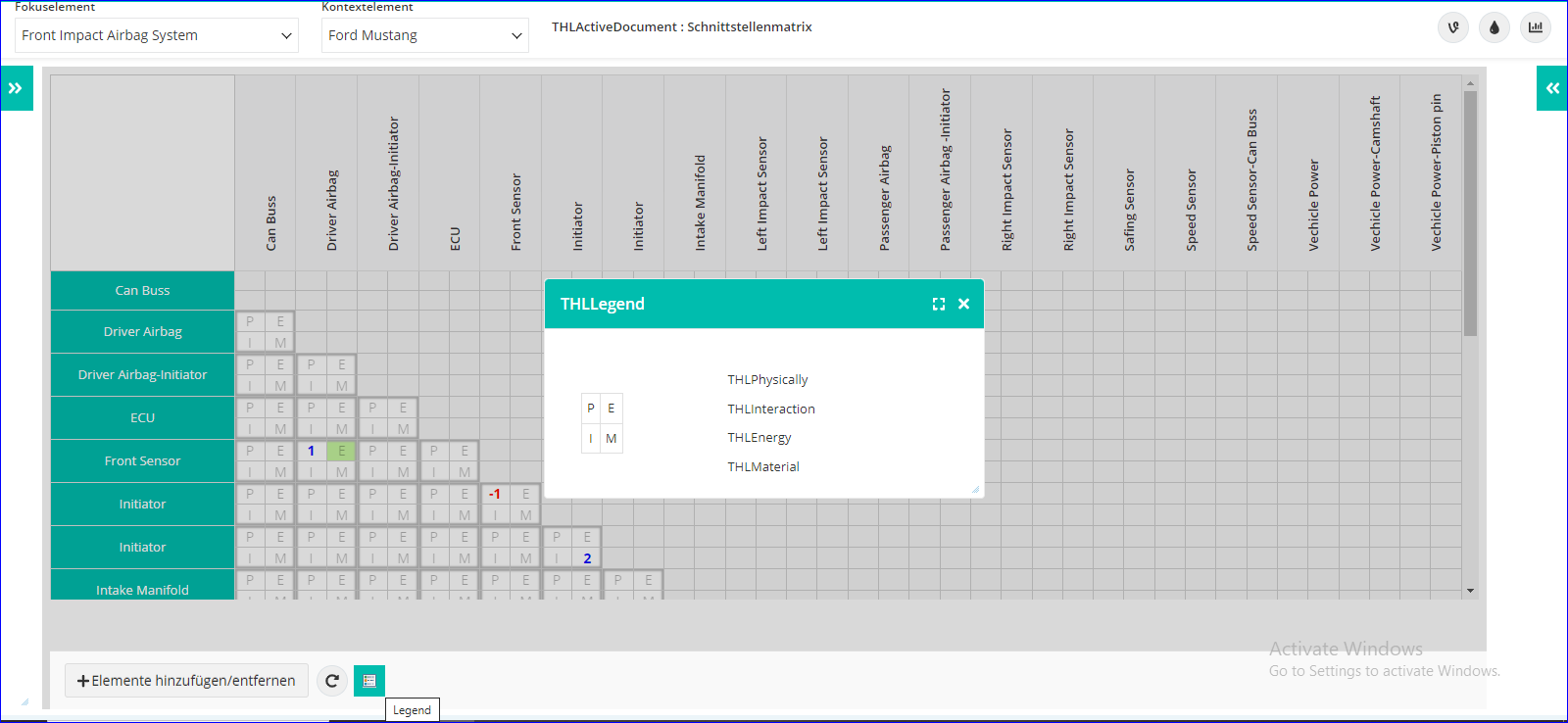




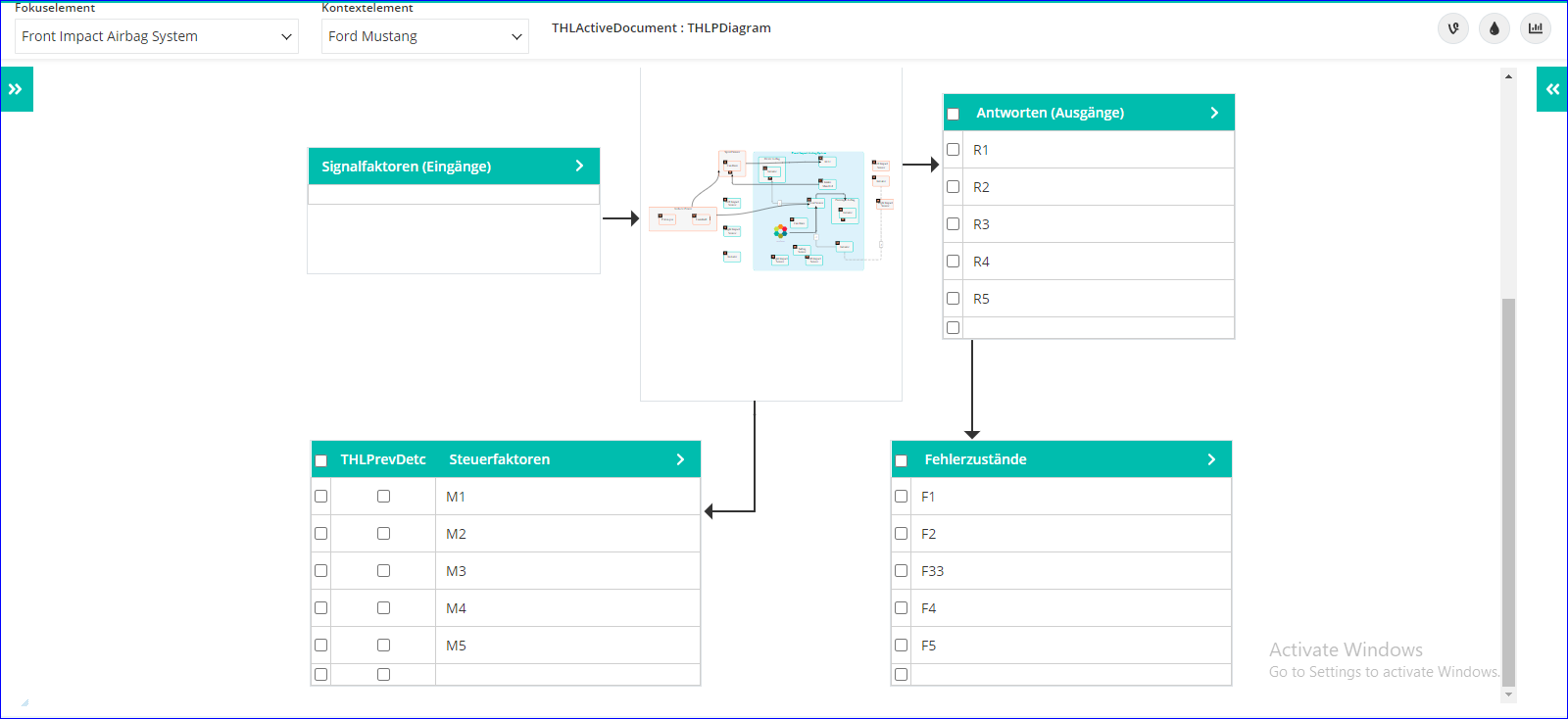
**Combined Interface Matrix**



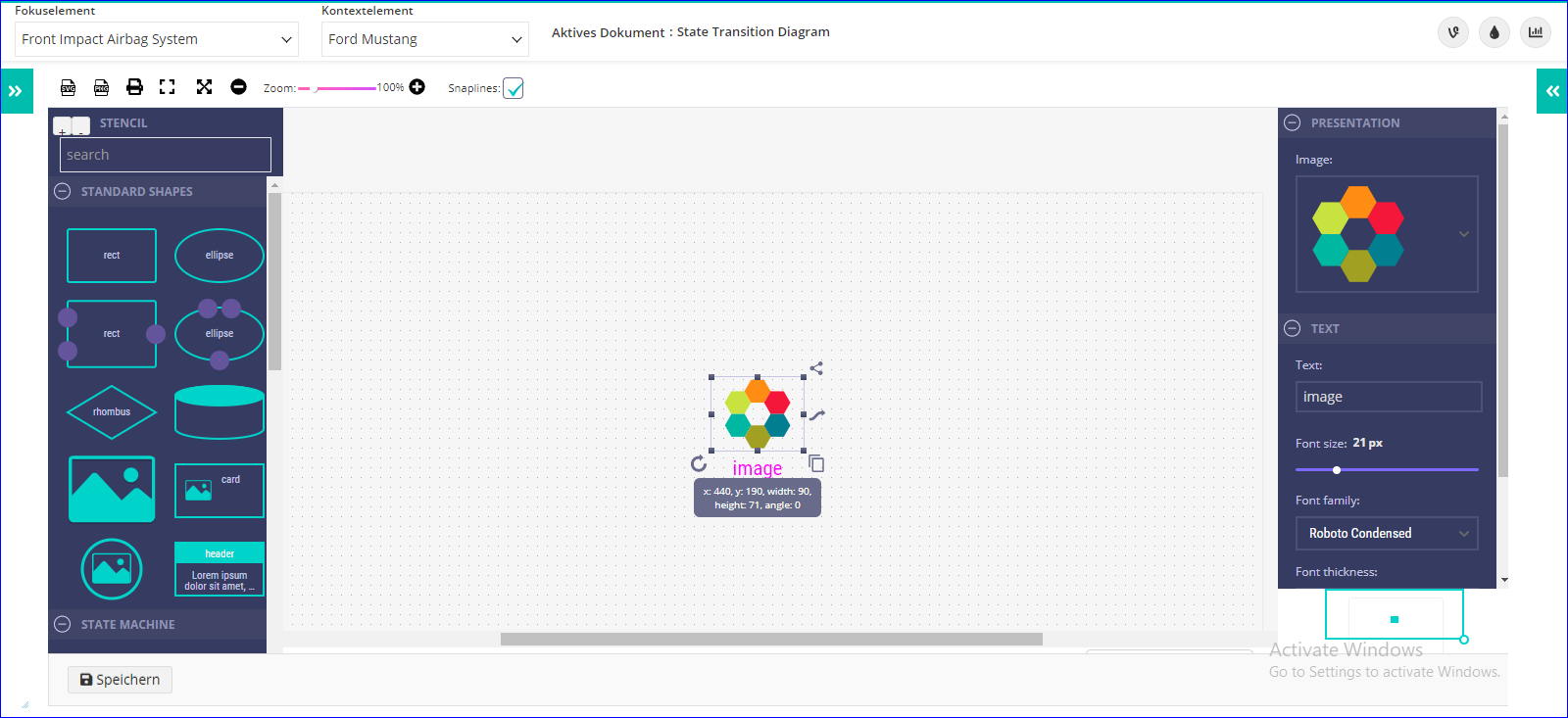
**Interface Matrix**



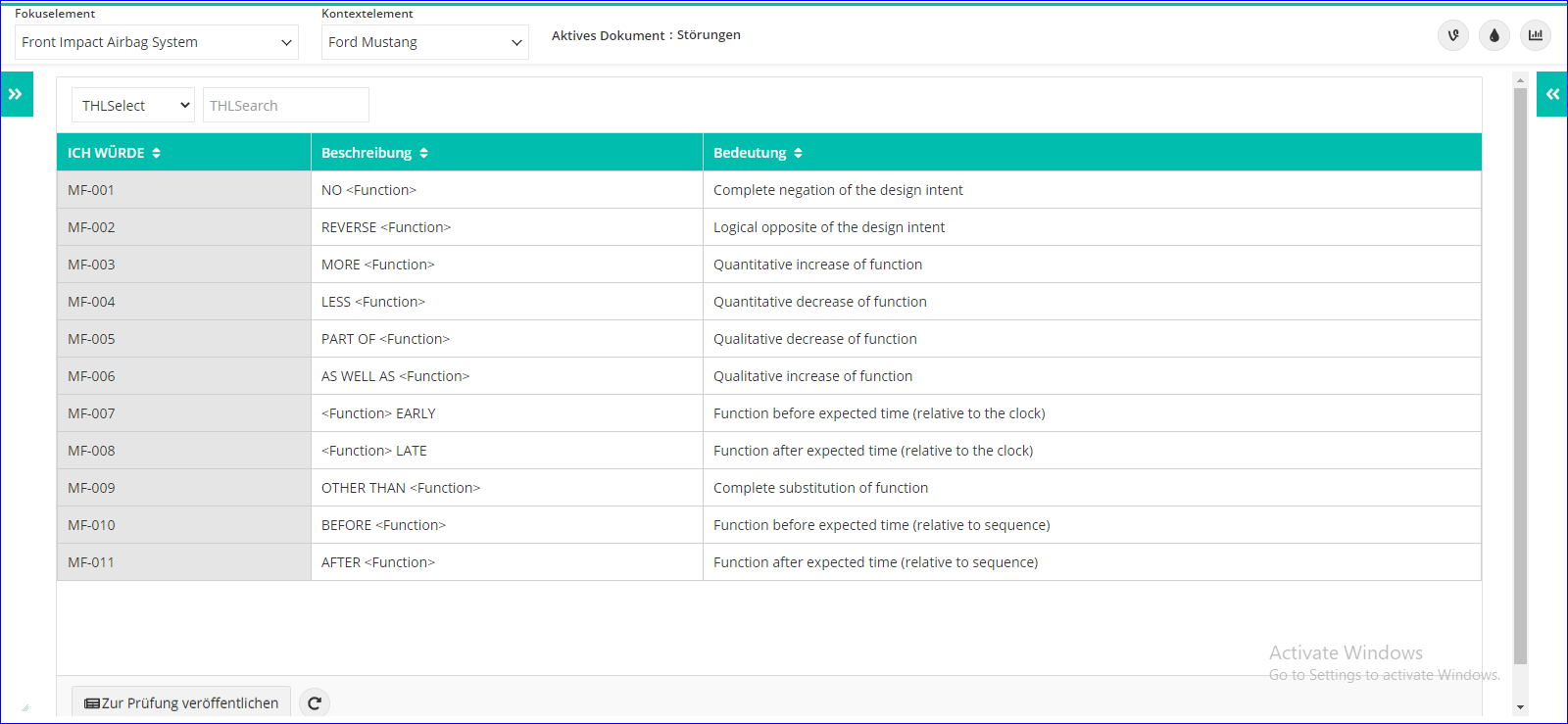
**P Diagram**



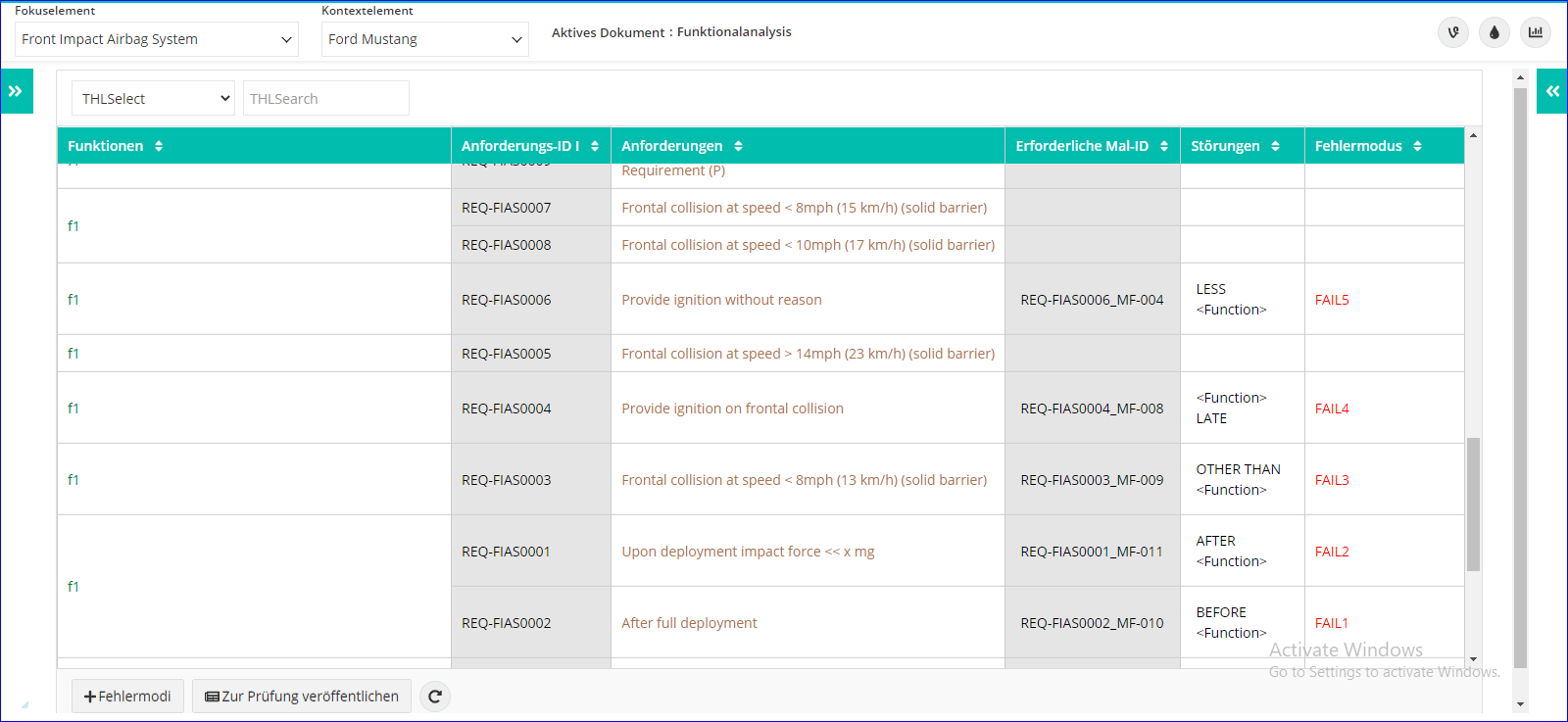
**State Transition Diagram**



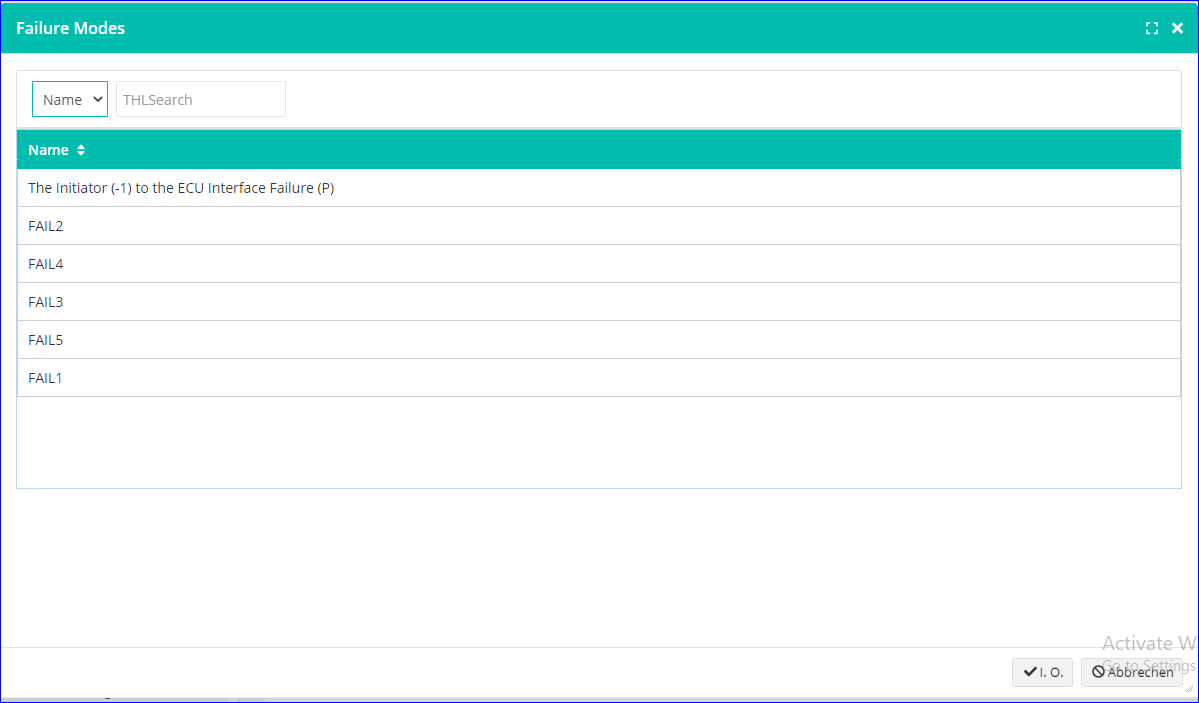
**Malfunction**



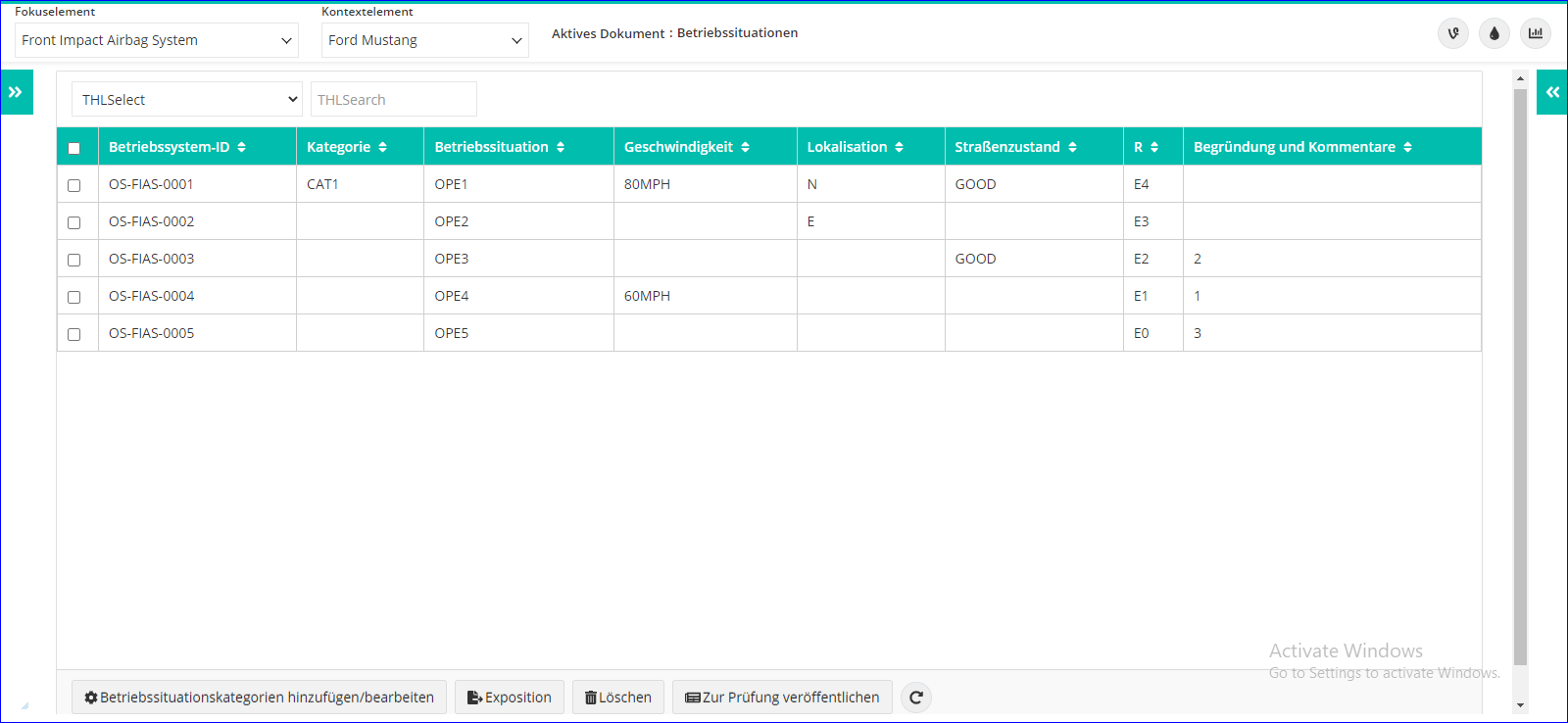
**Functional Analysis**

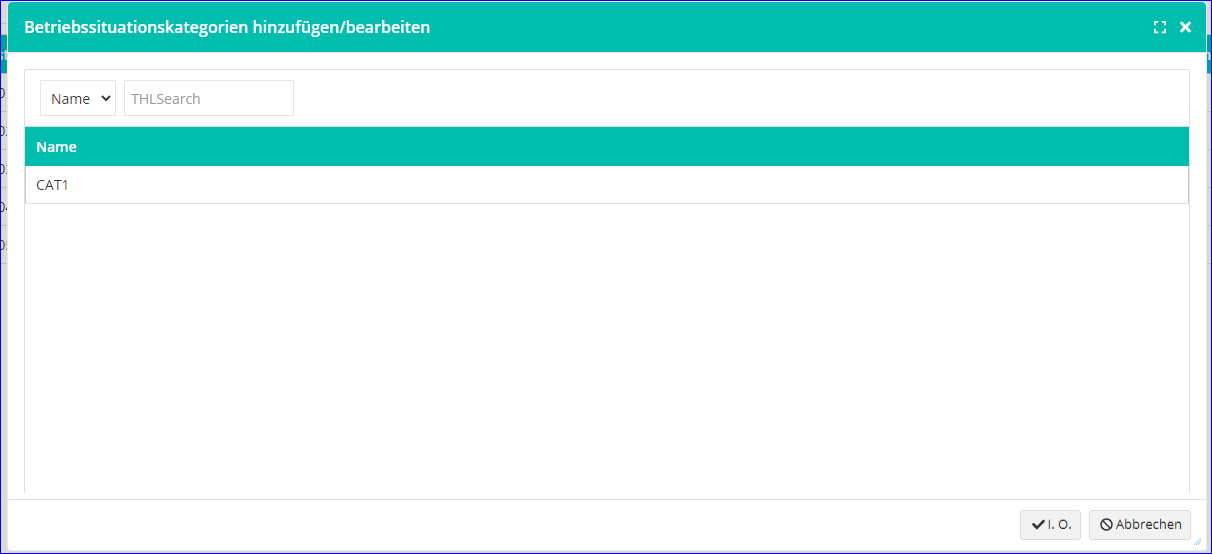


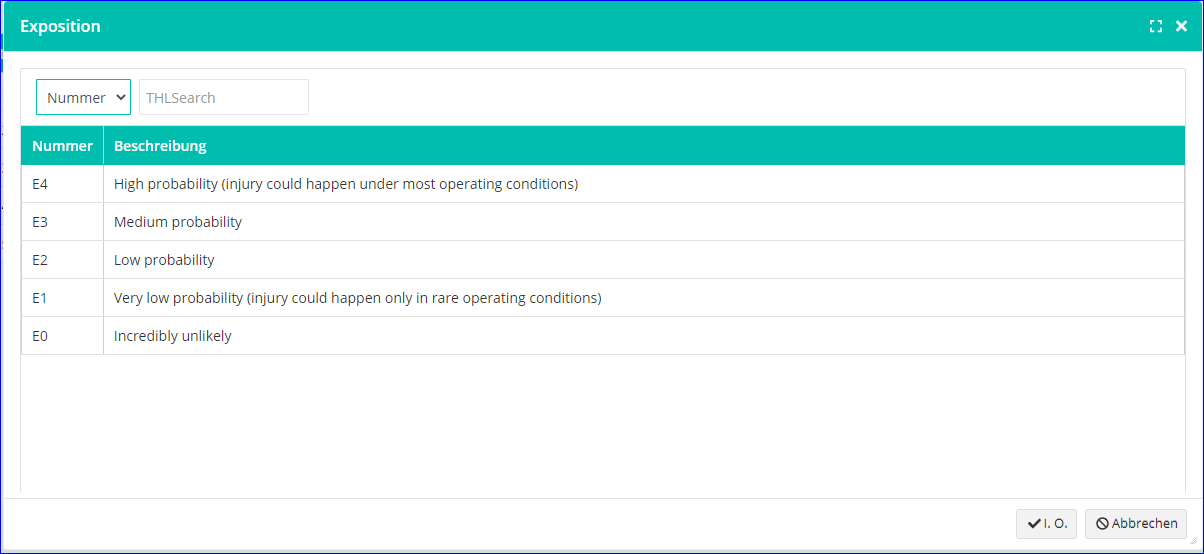
**Failure mode**



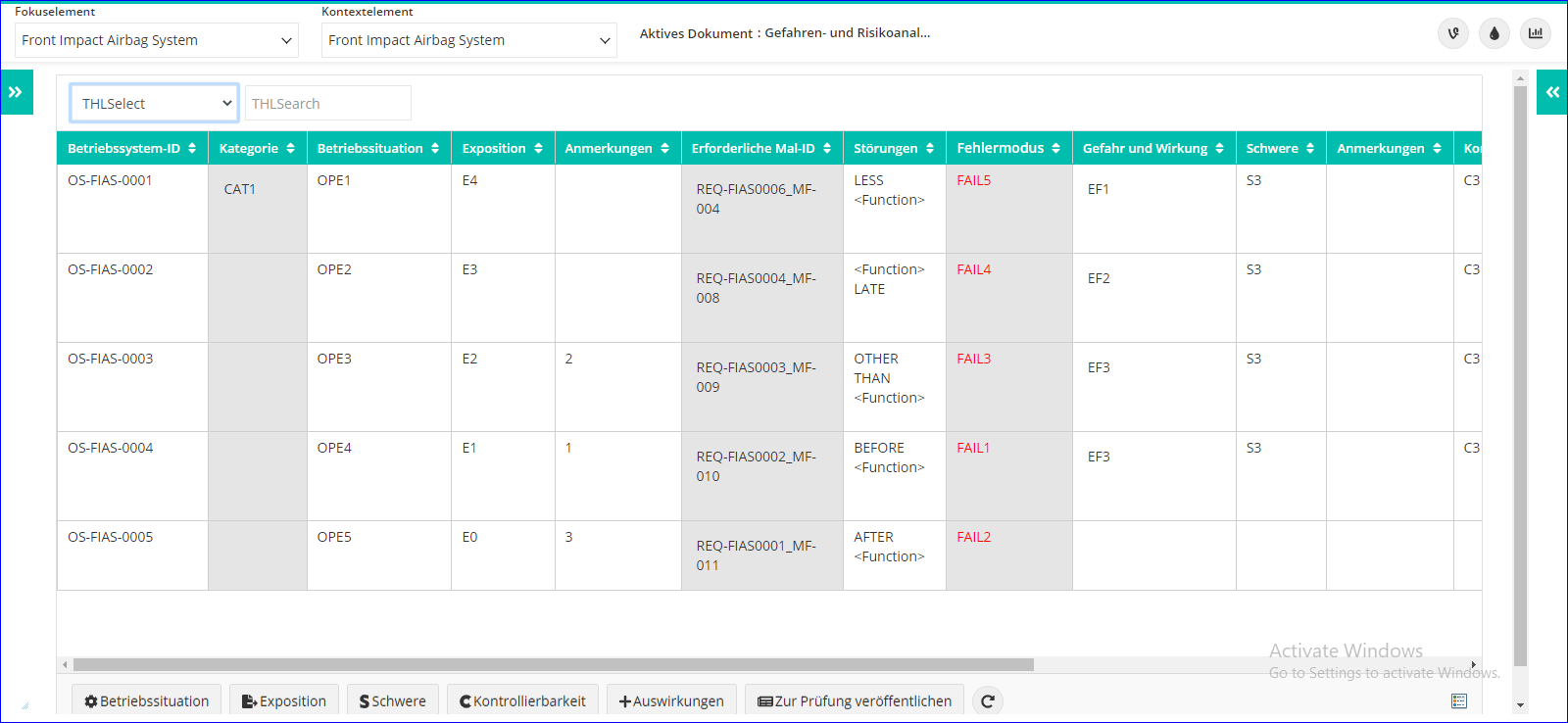
**Operational Situations**

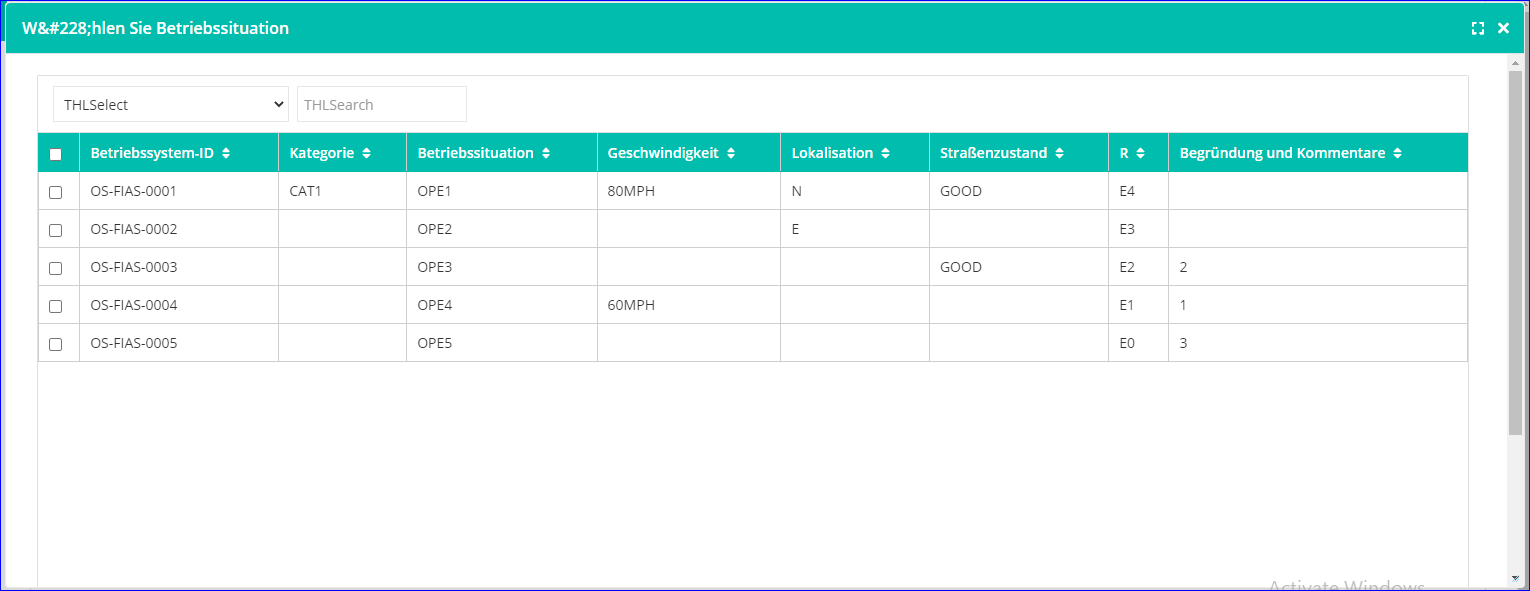


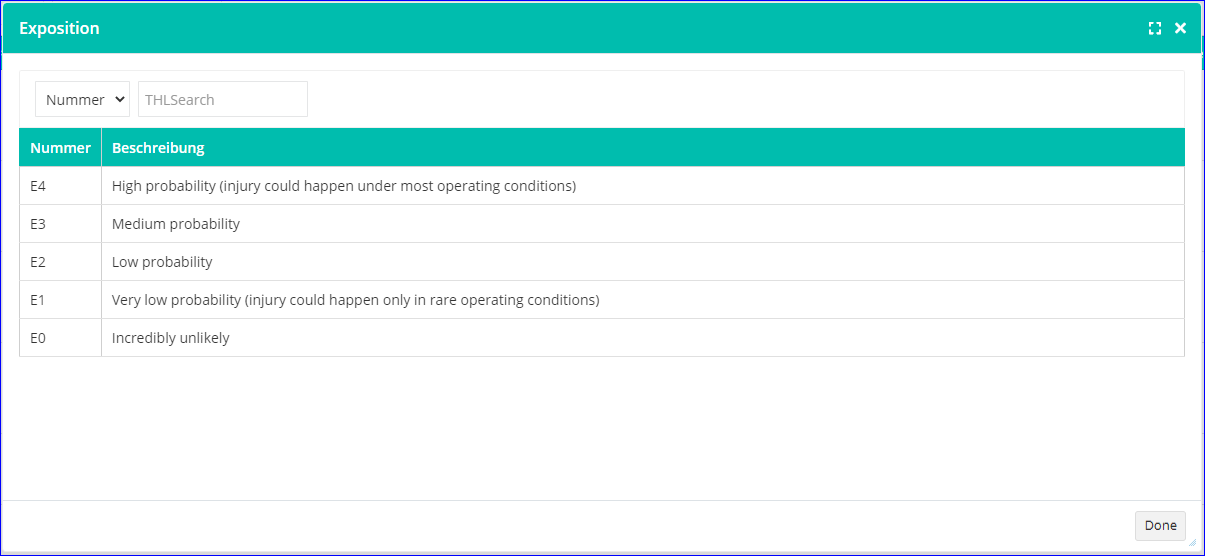


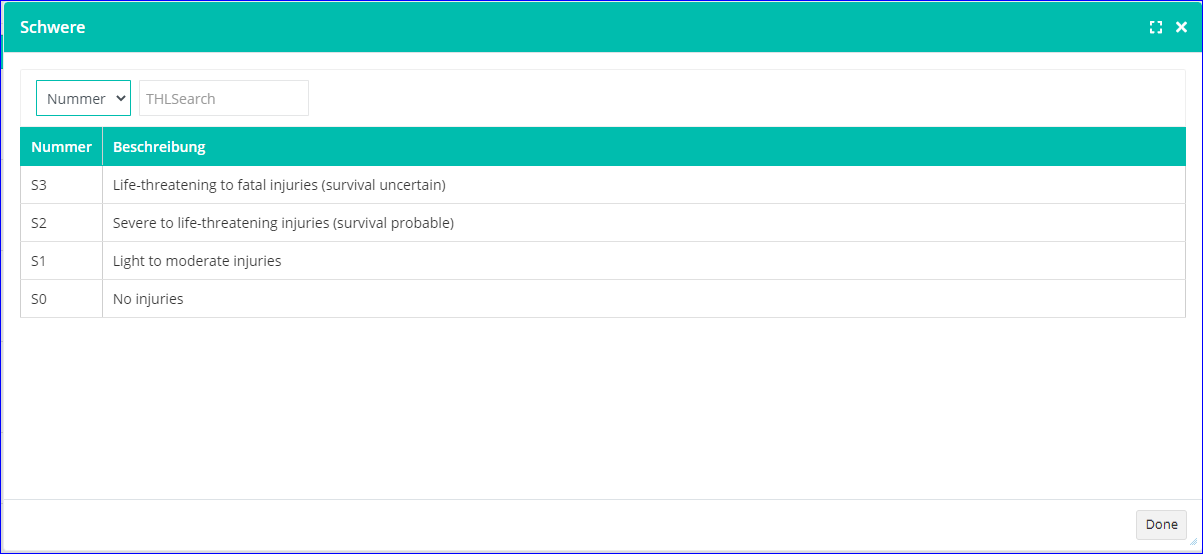


**HARA**

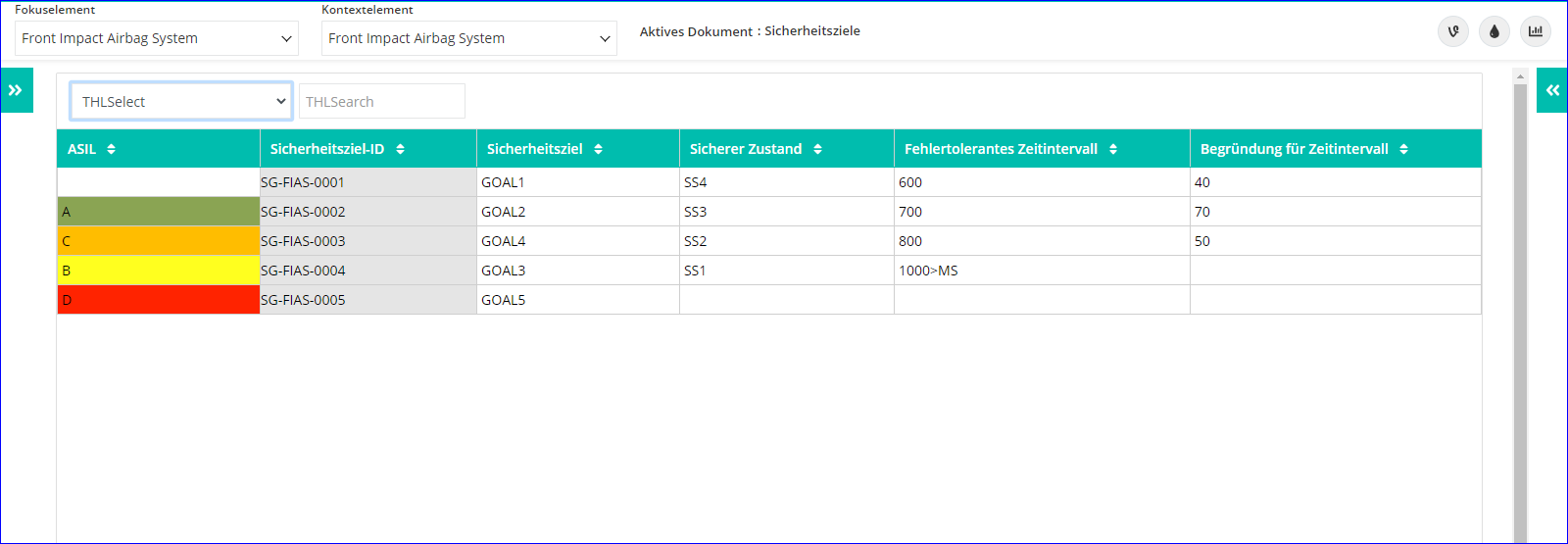


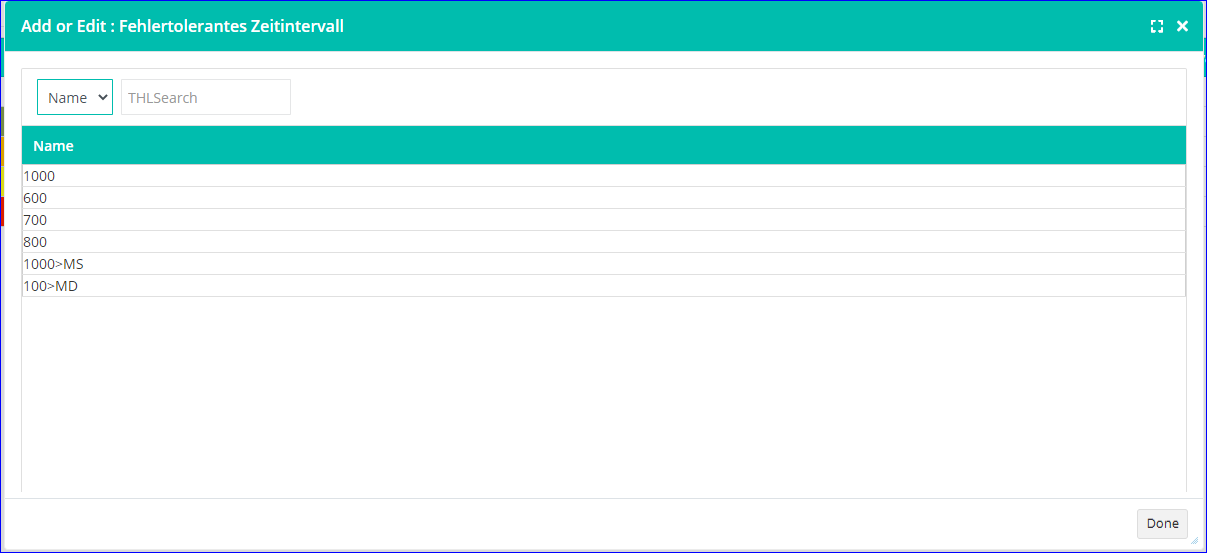


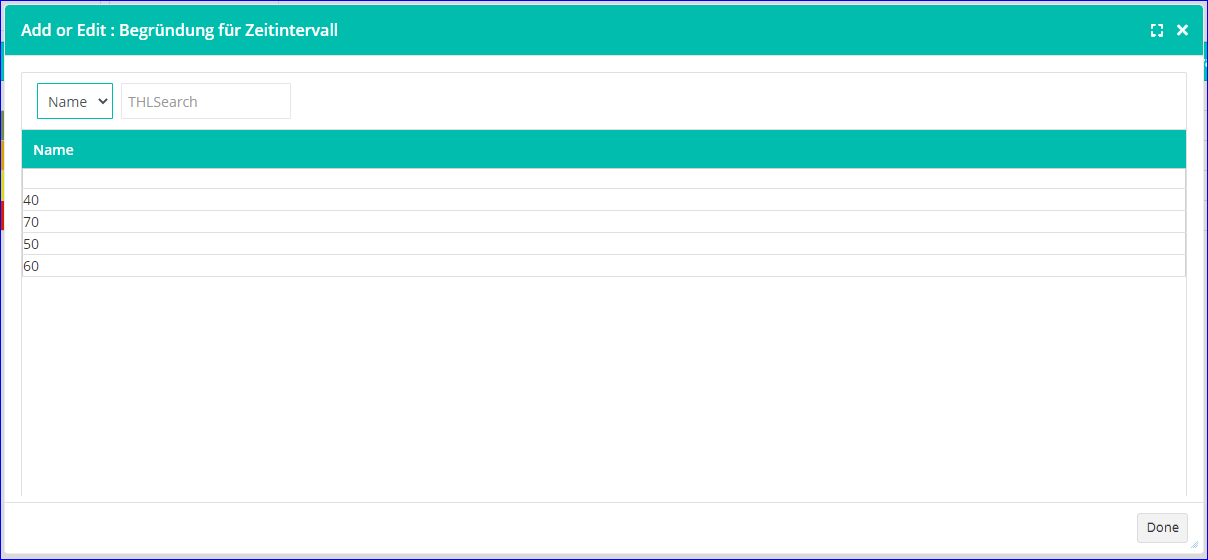




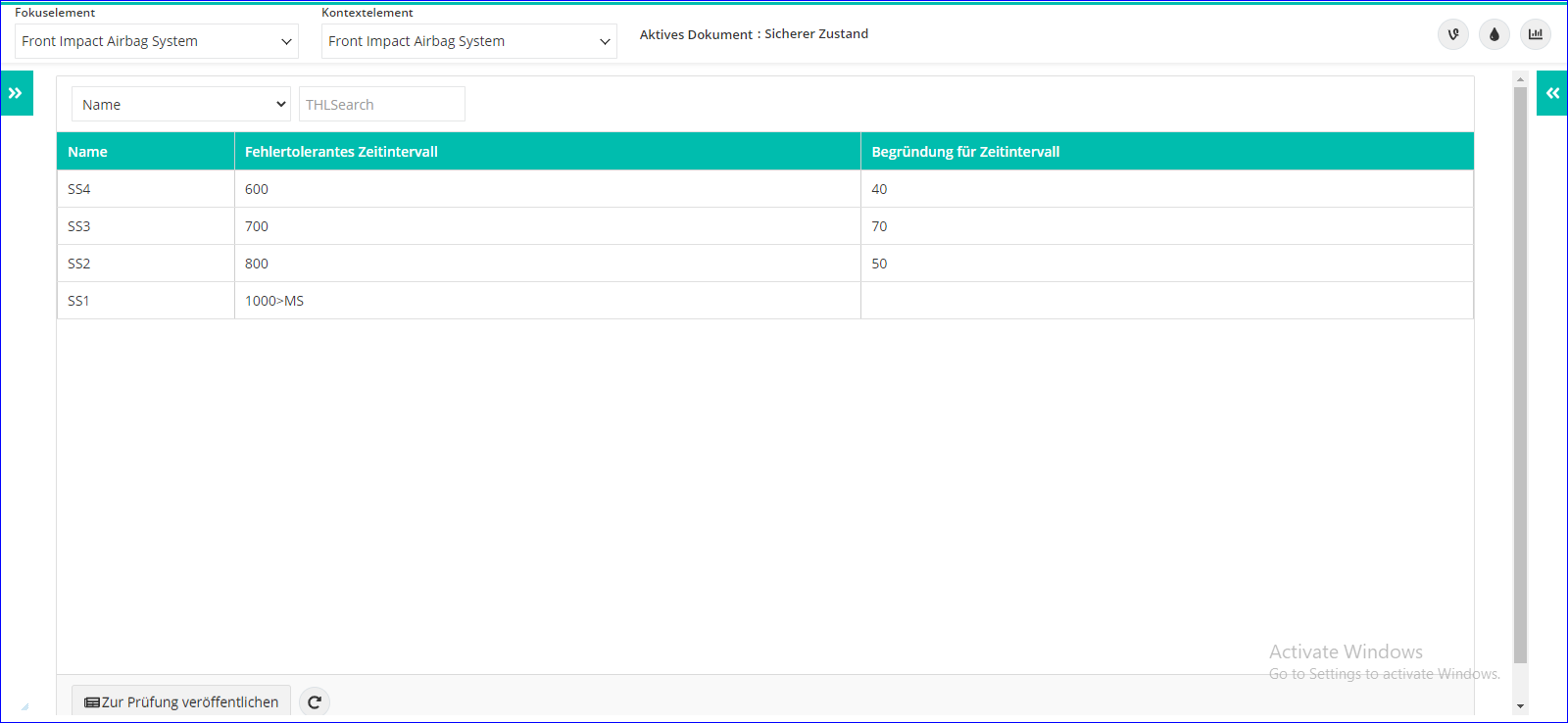
**Safety Goal**



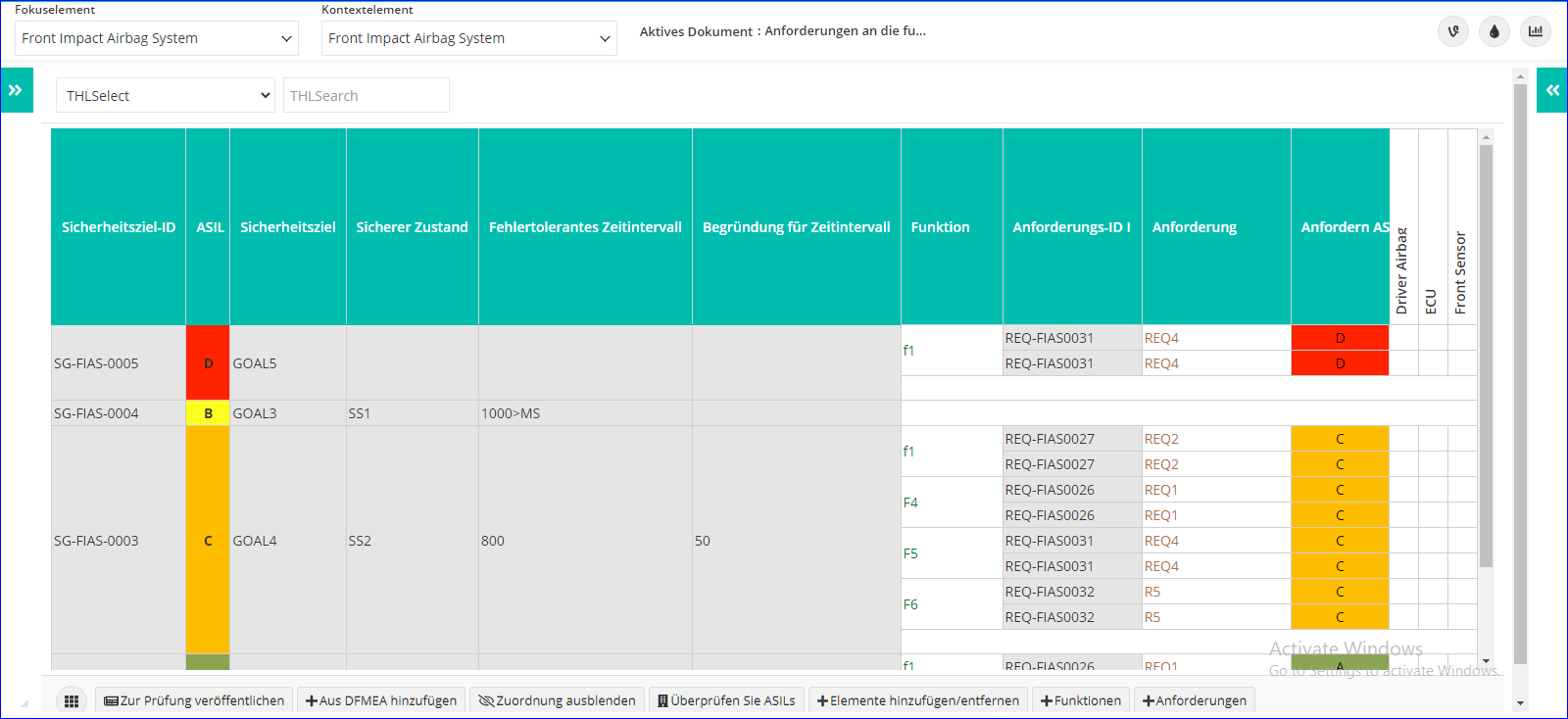


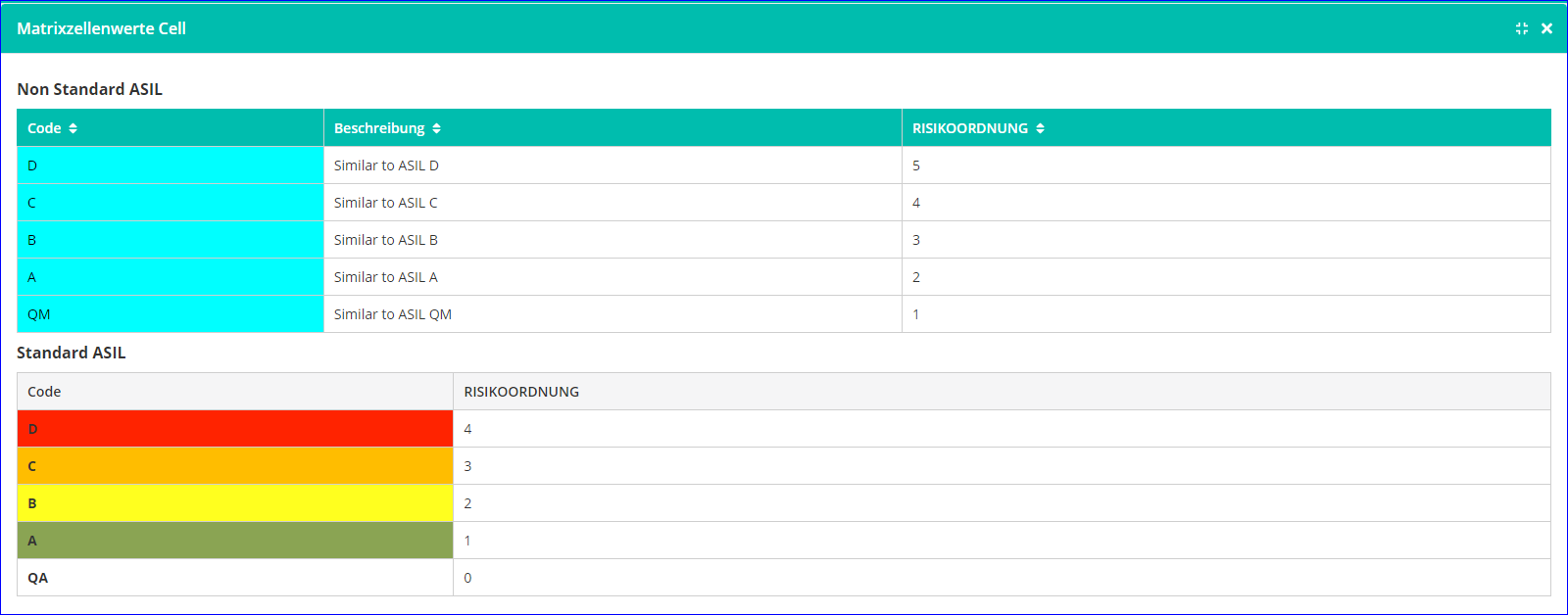


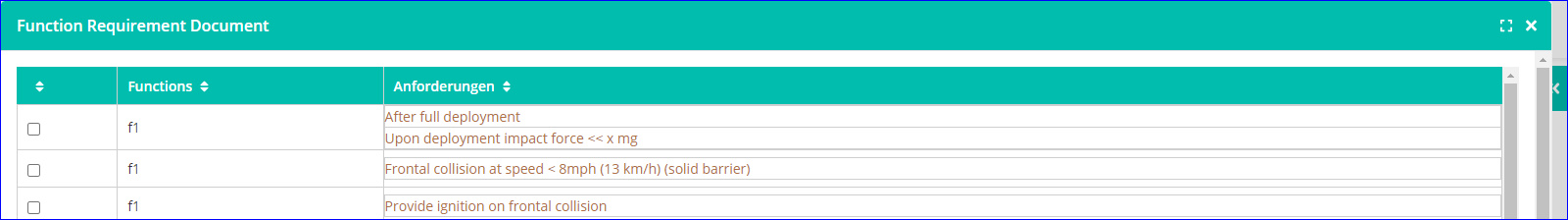
**Safe State**

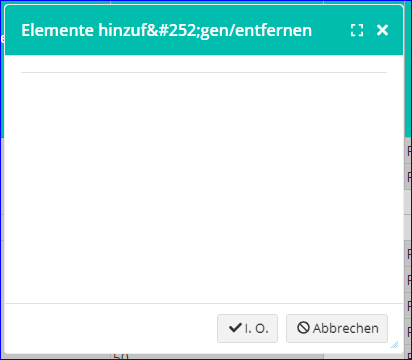


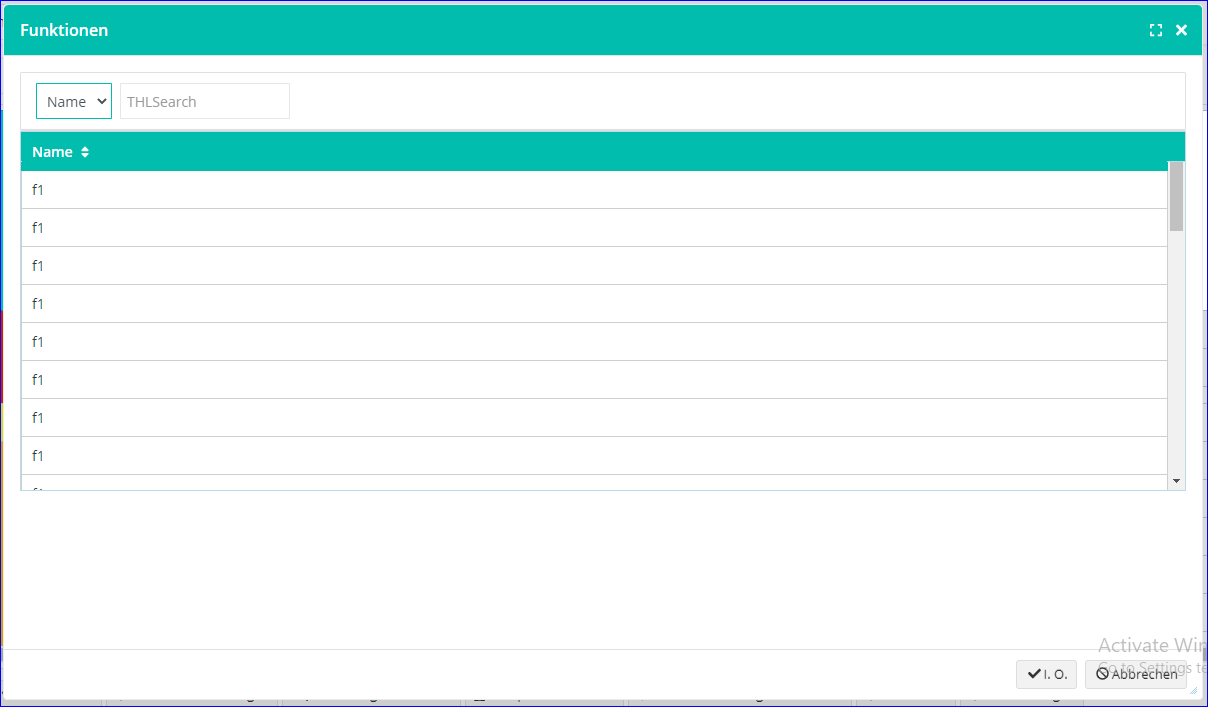
**FSR**

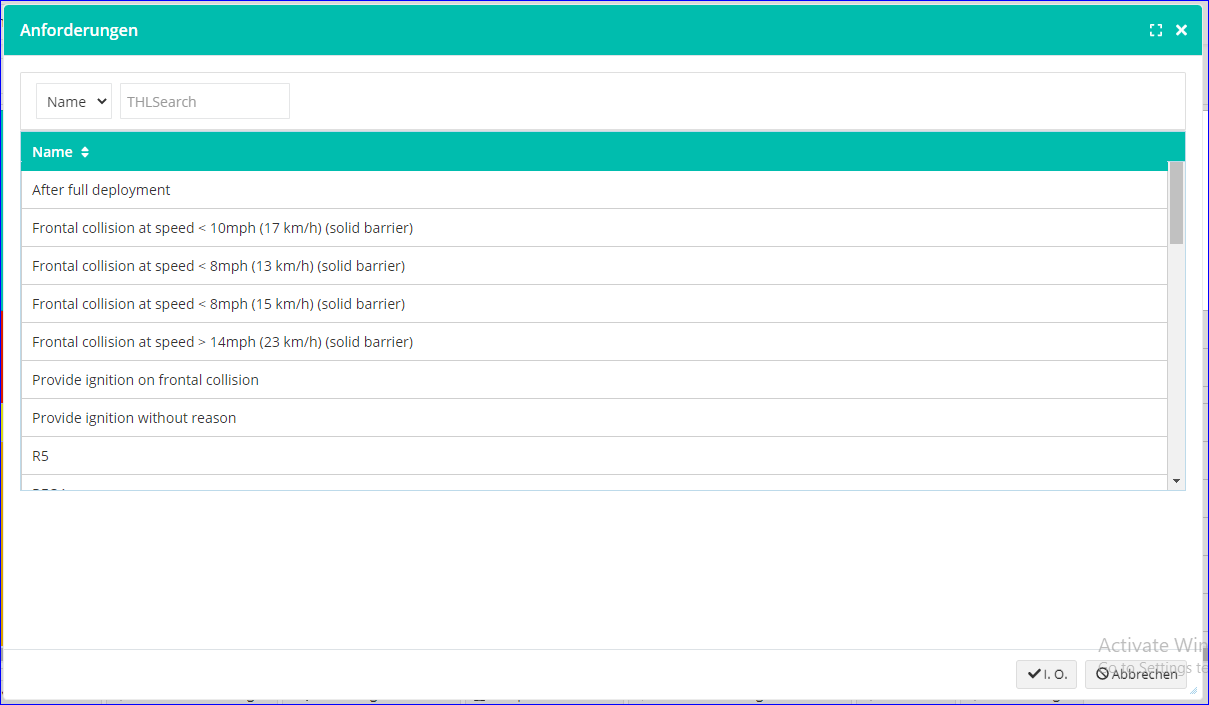




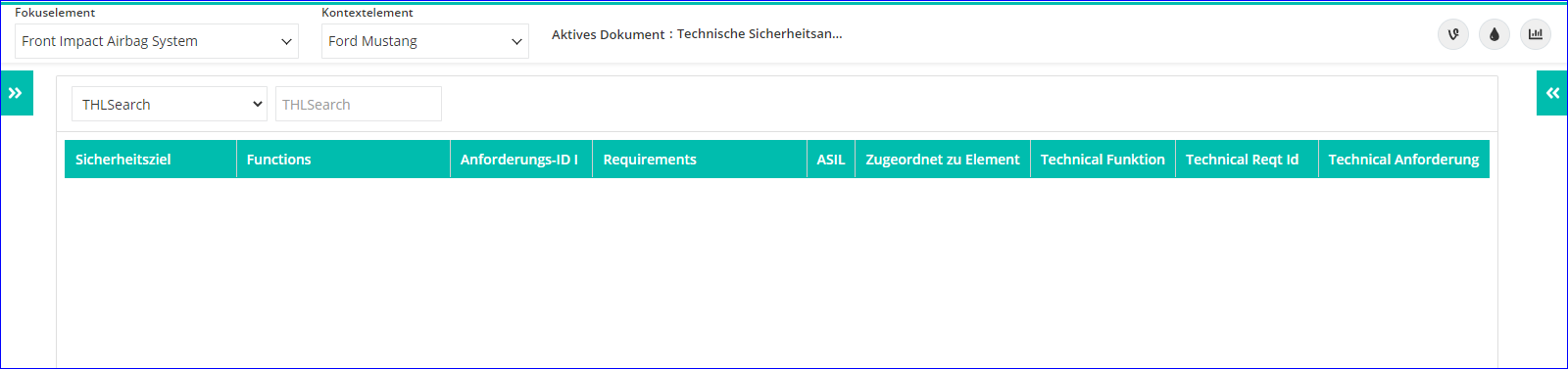




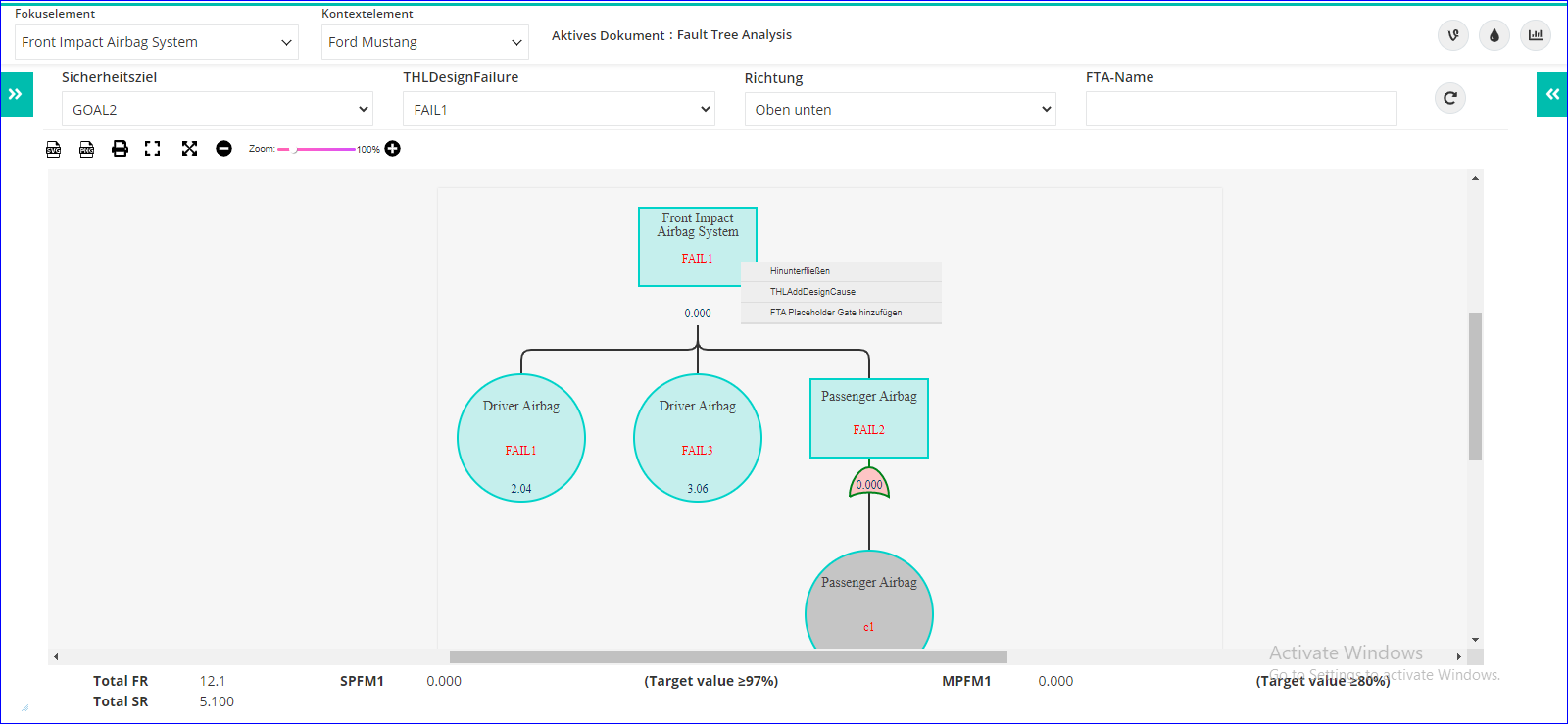




**TSR**



**FTA**



**FMEDA**

